TAPR
TNC-1
TERMINAL NODE CONTROLLER

TERMINAL NODE CONTROLLER

SYSTEMS MANUAL

APPLIED DIGITAL TECHNOLOGY
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NOTICE:

The TNC boards enclosed are produced from TAPR Rev 2 artwork. As a result, there are some minor differences between the board layout and the current Revision 3 Systems Manual assembly instructions. These differences are noted below:

The following parts referred to in the latest Rev 3 manual are NOT used on the Rev 2 board:

Designation	Quadrant	Value
R82	3	10K
R83	3	10K
R84	3	10K
R85	3	10K
R86	2	4.7K
R87	1	470
R88	1	4.7K
R89	?	4.7K
Q2	2	VN10KM

There is a capacitor labeled Cy located near the oscillator crystal on the Rev 2 board which was removed by Rev 3. If you wish to incldude Cy on the board, make it 20pf.

PLEASE NOTE: There is an error on the silkscreen legend on the PCB. C7 the bypass capacitor for the -5 volt regulator, is shown with its polarity reversed. The negative terminal of the capacitor should be installed near the (+) marking on the board.

All other parts may be assembled on the board as instructed in the Systems Manual. The differences in the two revisions will have little effect on the quality of operation of your completed TNC.

Descriptions of some of the refinements made by Revision 3 may be found in past issues of PSR (Packet Status Register, published by TAPR).

NOTICE:

This manual documents the Terminal Node Controller designed by Tucson Amateur Packet Radio Corp. It was originally written to accompany the kit produced by TAPR. Although the kits are no longer available, this manual is so well written and contains such a wealth of information about the TAPR TNC and packet radio, Applied Digital Technology felt it would be a valuable asset to the do-it-yourselfer and therefore has endeavored to reproduce the manual in its entirity. (Although at the time of this printing the permission to print the protocol appendixes B and C were not received, making them unavailable for reproduction).

This manual refers to printed circuit board revision level 3. If you are using earlier revision boards make sure you see Appendix H "Modifications and Other Notes" in order to upgrade your board to Revision 3. Some of the boards sold by Applied Digital Technology are Revision 2 boards so the first time builder will profit from this appendix.

The major difference between Rev 2 and Rev 3 boards are changes made in the oscillator circuit and the addition of J7 which provides the signals necessary to drive a tuning indicator. The same signals are present on the Rev 2 boards, although not on a handy connector. The page labeled "Kit Oscillator Problem" in Appendix H addresses the changes made in the oscillator circuit. If you have a Rev 2 board and are procuring parts to build the TNC, do not purchase the connector which is used for J7 because it will not be needed. All the other parts will be used if the oscillator circuit is modified according to the above mentioned notes.

TUCSON AMATEUR PACKET RADIO CORPORATION

Packet Radio

Terminal Node Controller

Rev 3

SYSTEM MANUAL

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1983

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TABLE OF CONTENTS

CHAPTER 1 Introduction

Welcome to Packet Radio.....1-1
Brief Description of Amateur Packet Radio.....1-1
Protocols.....1-3
Hardware.....1-4
What is TAPR? 1-5
Problem Reporting.....1-6

CHAPTER II Hookup

Board Layout and Power Connections.....2-1
Jumper and Switch Settings.....2-2
Terminal Interfacing.....2-2
Radio Interfacing.....2-3
Radio Interfacing Data.....2-6

CHAPTER III Operation

Introduction.....3-1
Terminal Characteristics.....3-2
Getting Started.....3-3
Operating Modes.....3-5
Flow Control.....3-8
Display Options.....3-11
Editing Commands.....3-13
Packet Operation.....3-15
Packet Timing Functions.....3-18
Monitor Functions.....3-22
HF and OSCAR.....3-23

CHAPTER IV Commands and Messages

Command Syntax.....4-1
Command.....4-4
Messages.....4-32

CHAPTER V Hardware

Overview.....5-1
TNC Specifications.....5-1
Functional Description of TNC.....5-4
Clock and System Timing.....5-6
Central Processing Unit.....5-8
Serial User Port.....5-9
Parallel User Port.....5-9
System Port, Timers, Indicators, and NOVRAM.....5-10
HDLC and Modem.....5-12
Memory Bank.....5-13
Power Supply.....5-13
Jumper Placement.....5-14
DIP Switches.....5-15
Terminal Interfacing.....5-17
Modem Calibration.....5-20
Calibration for Non-Default Tones.....5-24
TNC Modem Disconnect.....5-25
Tuning Indicator Interface.....5-30

CHAPTER VI Troubleshooting

Troubleshooting Hints.....6-1
Diagnostic and Status Signals.....6-7

CHAPTER VII Protocols

Explanation of Protocol.....7-1
HDLC Frames.....7-2
AX.25 Level Two.....7-3
VADCG Level Two.....7-7
Channel Use and Timing Functions.....7-7

APPENDIX A Illustrations

Transformer Wiring.....A-1
Schematics.....A-2
IC List.....A-5
System Block Diagram.....A-6
Board Layout.....A-7
Morse Code Characters Available.....A-8

APPENDIX B Omitted

APPENDIX C Omitted

APPENDIX D Hookup For Specific Radios

APPENDIX E Special Functions

Trace Function.....E-1
Debug Program.....E-2

APPENDIX F Assembly Instructions

Parts List.....F-1
Assembly Instructions.....F-6

APPENDIX G Parts Procurement Notes

APPENDIX H Modifications and Other Notes

BIBLIOGRAPHY

CHAPTER I

INTRODUCTION

Welcome To Packet Radio

Packet radio is one of the newest forms of communication that has been made available to Amateurs. It represents a giant step forward in reliable communication. We are glad you have joined the growing ranks of packet radio users throughout the world, and hope you will find it useful and exciting.

The TAPR Terminal Node Controller (TNC) kit is the culmination of approximately two years of volunteer effort by many people, including nearly 17Ø participants in TAPR's "Beta Test," which involved the distribution of TNCs throughout the world for testing, experimentation, and criticism. As a result, we believe this is one of the most thoroughly tested projects to be done by a non-profit, volunteer-labor Amateur organization. We hope you share our enthusiasm!

Brief Description of Amateur Packet Radio

Communication via Amateur packet radio involves the transmission of digital data by means of radio, making use of typical ham transceivers. Unlike ordinary RTTY or ASCII, the data does not pass directly from the source to the radio or vice-versa. Instead, it is routed through a terminal node controller, where the special characteristics of this mode are implemented. TAPR's terminal node controller is a microprocessor based device which is connected to a source of digital data via its RS-232C serial data port, and to an ordinary Amateur radio transceiver via its radio port. Thus, it stands between the digital device and the radio.

Usually the TNC will be connected to a data terminal or computer via the data port, and all commands to the TNC as well as data to be transmitted will come from this device. The TNC handles all the chores involved in actually sending and receiving the data, including packaging the data into bursts called packets or frames, keying and modulating the

transmitter, and listening for and demodulating packets from the receiver.

In fact, the TNC does many tasks associated with maintaining the data channel, such as checking the integrity of packets received, and acting as a relay station when so requested by other users of the channel. All of these functions are handled completely automatically by the software and hardware of the TNC. The user need only be concerned with producing and making use of the digital data, leaving the multitude of communications tasks to the TNC.

A typical packet QSO begins with one user commanding his TNC to issue a "connect-request" packet directed to the other station. The connect-request packet includes both users' call signs, so the receiving station recognizes that the connect-request is directed toward him. The receiving station acknowledges receipt of this packet and the connection is established. Each user sees

*** CONNECTED to <CALLSIGN>

appear on his terminal, where <CALLSIGN> is the call sign of the other station. The TNC then automatically switches to data transfer mode. From then on all data is sent to the other station whenever a packet of information is completed.

Each packet is a "sandwich" consisting of, in order of transmission, header information, followed by the data (if any -- some packets contain no user data), followed by a very important 16 bit number called the Frame Check Sequence (FCS). It is this packaging of the data that sets packet radio apart from all previous types of Amateur communication, and which gives it special properties.

The two primary characteristics of packet radio communications are first, that it is "bursty" -- most of the time, channel occupants are not transmitting. (However, exceptions to this rule occur, for example, during computer-to-computer file transfers.) Second, the received data which is sent out the TNC's data port is essentially guaranteed to be error-free. This is due to error detection associated with the FCS following the data, which is used by the receiving station to determine the validity of the data. In the event the data is incorrect, the transmitting station will re-transmit the packet, up to a user-specified number of times, in an attempt to get the data through with no errors.

Packet radio QSOs are normally terminated when one of the stations issues a "disconnect-request" packet. This is acknowledged by the receiving station, and both users see

*** DISCONNECTED

appear on their terminal.

At any time the TNC may be switched to "command mode" and one of more than sixty user settable parameters may be inspected or changed. These parameters control all facets of the TNC's operation, and many of them are stored in NOnVolatile Random Access Memory (NOVRAM) (trademark of Xicor Corp.) so they need not be continually reset at every power-up.

Protocols

The rules by which the software determines responses to received data, as well as to the user's commands and data, are known as the protocol. For example, when a TNC issues a connect-request packet it expects a reply in the form of a "connect-request acknowledged" packet. If all goes well the receiving station will issue such a packet, the connection will be established, and each user will see

*** CONNECTED to <CALLSIGN>

appear at his terminal. However, if no proper response is received, the originating TNC will re-issue the request for a predetermined number of times and then give up. All this behavior is dictated by the protocol.

As another example, if two or more QSOs are taking place on the same channel (one of the great advantages of this mode) and a simultaneous transmission by two stations occurs (a collision), the subsequent time delays and retransmissions are determined by rules contained in the protocol.

While a great many of the tasks required of the protocol are determined once the basic ground rules are laid out (some are immediately obvious, others less so), most of the implementation details are completely arbitrary. In particular, the exact composition of the frame is very flexible, and no two designers would be expected to independently hit upon identical digital frames. On the other hand, no communication at all is possible without perfect agreement on just this point, among many others!

For this reason, a meeting hosted by AMSAT was held during October, 1982 which included most of the major packet radio interest groups, in order to decide upon a single mutually agreeable protocol for use on the AMICON channel on the then upcoming Phase III B satellite. Such agreement was reached, and the presently accepted protocol (one of two implemented on the TAPR TNC - the other is the VADCG protocol) is known as AX.25, named after the international commercial protocol X.25, upon which it is based. AX.25 is currently the most widely used protocol in Amateur packet radio throughout the world. A substantial portion of this manual describes the usage of the two protocols on the TNC and their formal definitions are contained in Appendices B and C.

Hardware

In addition to the TNC characteristics implemented in software, several hardware features are notable. The power supply on the board provides four different voltages: +5 volts, -5 volts, +12 volts and -12 volts. The audio circuitry obtains its +5 volt power from its own regulator, and the audio ground is tied to the digital ground at a single point. These features, along with generous bypassing, give high noise immunity to the TNC.

The packet structure outlined above, which is used by both protocols on the TNC, is generated in software. However, the production and reception of these frames as serial, synchronous streams of data is handled by a special purpose Very Large Scale Integrated (VLSI) circuit which relieves the processor of most of the onerous tasks involved in this process, including the jobs of automatic generation and inspection of the FCS value. Not only does this make writing the software more pleasant, it allows full duplex operation at data rates up to 4800 baud. (Modem modification or an outboard modem is required if the radio channel is to be operated above 1200 baud.)

Great attention was paid to the TNC modem design. As a result of many experiments, and suggestions received during our Beta test, the Beta version modem design has been modified in crucial areas, and the design on the present TNC should prove to be one of the most reliable, especially for demodulating signals in the presence of background noise. This is due in part to the input filter system along with a quiet power supply.

Furthermore, in order to make the TNC as versatile as

possible, all critical passive components for both the modulator and demodulator are mounted on special headers which simply plug into ordinary IC sockets. This permits easy tinkering and experimentation, and allows complete modem reconfiguration in seconds. As an additional aid for those wishing to use altogether different modulation/demodulation schemes, all signals to and from the modem are coupled via a multi-pin connector which is normally shorted by jumpers. In the event a separate modem is desired, the jumpers may be removed and these signals passed to the off-board modem via a standard connector.

The details of the TNC's performance are described in this manual, from the exact meaning of each bit in a frame to the type of modulation and demodulation used for the radio port. In addition, the manual contains complete descriptions of each of the many commands available to the TNC user. These commands provide great flexibility in configuring the TNC to provide precisely the type of service each individual user requires.

What Is TAPR?

Tucson Amateur Packet Radio Corporation is a nonprofit R & D group whose primary goal has been, and continues to be, the development of packet radio technology for the Amateur community. It was formed in 1981, originally as a club, and incorporated in mid-1982. From the beginning, the intention was to create an inexpensive way for hams to participate in this mode of communication.

Prior to our Beta test, the only Amateur packet radio was done by dedicated, hard-core experimenters using home-brew systems or systems based on the pioneer VADCG TNC. With the distribution of the 170-odd TAPR Beta boards, packet radio was introduced at the "black box" level. The TNC in this kit incorporates all the hard-won improvements resulting from that test, and should prove highly reliable.

TAPR presently (December, 1984) consists of about 1200 members spread over many countries, and continues to grow. Our interests are diverse, including such projects as higher speed local-area-net operation, PACket SATellite (PACSAT), and inter-net linking. We are always looking for volunteers with interest in these areas, and encourage you to discuss your ideas with us.

The Packet Status Register is the official bi-monthly

publication of TAPR and is distributed to members as a way to keep them up to date on the latest TAPR activities and other interesting aspects of packet radio.

Problem Reporting

Please attempt to solve locally as many of your kit building problems as possible. The distribution of this kit is due primarily to the efforts of a very few people, all of whom do this as a hobby, in their spare time and without pay. For this reason, we are unable to offer a repair service for inoperative kits. However, in the event you are really at an impasse (please take heed of the suggestions in Chapter VI, TROUBLESHOOTING) write us at the following address and we will attempt to offer you suggestions to get your TNC running.

Our address:

Tucson Amateur Packet Radio
P.O. Box 22888
Tucson, AZ 85734

PLEASE DO NOT RETURN TO US ANY CONSTRUCTED TNC OR PARTIALLY CONSTRUCTED KIT WITHOUT CONTACTING US FOR WRITTEN PERMISSION.

CHAPTER II

HOOKUP

Board Layout and Power Connections

Throughout this chapter, references made to parts located on the board use the following convention: viewing the board from the component side, the top is the edge closest to the RAM and EPROM memory bank (U7-U12). The label "Tucson Amateur Packet Radio" and most of the silk screen legends will be right side up if the top edge is away from you as you look at the board. The wire wrap area will be at top left and the three interface connectors will be at the lower edge closest to you. The reverse side of the board, which has no silk-screen legends, will also be referred to as the "solder side" of the board.

Pin 1 of all Integrated Circuits (ICs) is marked on the silk screen. In addition, a label for pin 1 for most ICs is included in the trace etching on the solder side of the board. IC pins are numbered consecutively down the left side of the chip, then back up the right side when viewed from the top. For example, pin 40 of a 40-pin IC will be opposite pin 1. Pinouts for the modem disconnect, power connector, and all interface connectors are illustrated in Fig. 2.1.

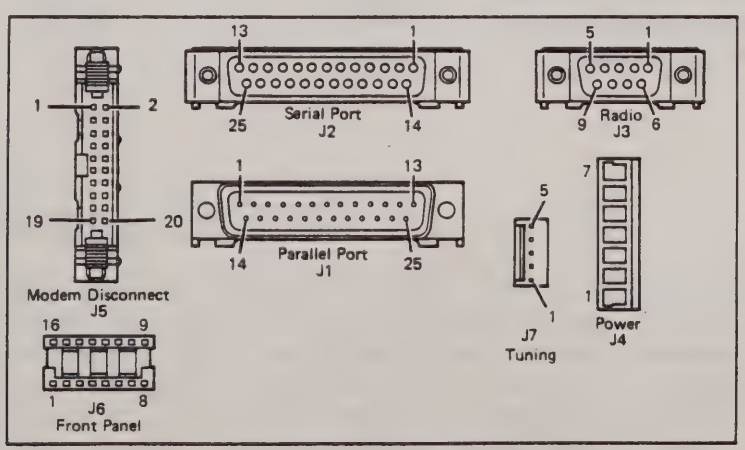


Fig. 2.1 Connector Pin Numbering

The 5-volt regulator, U24, may be mounted either on the TNC board with the heat sink provided or off board. If it is mounted off board, the regulator must be in good thermal contact with an appropriate heat sink. The power cable connects the board to the custom transformer secondary windings and to the 5-volt regulator if it is off board.

The power connector, J4, should be attached to the mating connector so that the nylon locking tab and the "wall" of the on-board half are touching. Make sure the connector is attached properly before applying power to the transformer.

WARNING! Be sure that there are no exposed power connector pins (i.e. misalignment of J4 and its mating connector) or severe damage to your TNC may result!

Jumper and Switch Settings

The onboard jumpers and DIP switches are described in Chapter V. For initial power-up of the TNC, the following switch configurations should be used:

Switch 1	ON
Switch 2	ON
Switch 3	OFF
Switch 4	ON

Switch 3 is the Reset switch. Toggling this switch ON then OFF will perform a hard reset of the TNC microprocessor.

The following jumper configurations should be used:

JP1	ON	JP6	left position
JP2	OFF	JP7	right position
JP3	OFF	JP8	left position
JP4	ON		
JP5	OFF		

Terminal Interfacing

Terminal interfacing is provided via J2, an RS-232C compatible connector. A detailed discussion of the RS-232C communication standard and the pinout of this connector are given in Chapter V. If you construct a cable to connect your computer or terminal to your TNC you should refer to this discussion. The TNC connector is wired exactly the same as

that on a modem, so if you already have a cable which connects your terminal to a modem, you may use that cable.

Radio Interfacing

The TAPR TNC is designed for easy interfacing to a station transceiver or transmitter and receiver. All radio input and output (I/O) is handled through a single 9-pin connector, J3. A mating connector is provided with the TNC.

In general, the easiest transceivers to interface will have all return lines to negative ground, separate microphone and PTT input lines and clean audio sections with no spurious audio tones or audio harmonic distortion. The audio response resistor-programmed into the TNC modem input filter (U28) should be adequate for most receivers, but there may be a few "hard case" units. This is one reason that the input filter is configured via a 16-pin DIP header resistor pack (U30). Note that, while simpler input coupling methods do exist and have been tried during the development of the TNC, the extensive filtering provided on this TNC permits operation in the presence of higher background noise than is possible with simple passive filters.

Due to the wide variety of audio input and output connectors in common Amateur Radio use, TAPR cannot provide a prewired cable for mating to your particular radio. However, detailed connection information for a number of popular transceivers is compiled in Appendix D and we suggest you look for your particular radio there. If your radio isn't listed in the Appendix, we request that you fill out the RADIO INTERFACING DATA page, located at the end of this chapter, and send a copy to TAPR. The following explanation of the connector pinout should be helpful to you in working out your own radio interface.

Radio Interface Pinout

Audio Input (pin 3):

This lead brings the audio from the radio to the TNC. It is normally connected to the headphone or speaker audio output of the receiver. The TNC input impedance as normally configured, is about 10k ohms, bypassed by 0.01 uF. If your receiver requires a lower impedance load, change C26 to 10 uF and add the load at R59. Most radios do not require this even though they normally drive a low impedance speaker.

If the radio's speaker is also connected, be alert to possible distortion it may introduce, particularly at high drive levels.

Common (pins 6,7,8 and 9):

These pins are tied directly to the negative return bus on the TNC and should be connected to the negative return bus on the radio. This common serves as a return for transmit control (PTT) and both send and receive audio. If a ribbon cable is used for radio connection and all four ground wires are used, every other wire in the cable should be a ground.

NOTE: Some Japanese HTs, such as the Yaesu FT-2Ø8R, use their positive supply bus as audio return. DO NOT connect this positive audio return to TNC common! Instead, the HT's negative common should be connected to the TNC. This should not cause any problems, as the radio power supply should be adequately bypassed for audio.

Transmit Key Line (PTT) (pin 4):

This lead is designed to connect to the PTT or other transmitter activation line on the attached radio. It will be connected to TNC common, or "ground" when the TNC attempts to key the transmitter. This line is rated at +3Ø volts dc open circuit maximum and Ø volts dc open circuit minimum. DO NOT connect it to a negative potential with respect to common. It can safely sink up to 25Ø mA in the closed (transmitter keyed) configuration. If the above ratings of the transmitter key line are exceeded, extensive damage may occur to your TNC. If your radio fails to meet the above criteria, a relay or other means of keying should be used.

The VFET used to key the transmitter has been very successful at operating transmitters previously thought to require a relay contact to properly key.

KNOW YOUR RADIO PTT CHARACTERISTICS BEFORE CONNECTING THE TNC!

Transmitter Audio (pin 5):

This lead provides an audio signal to the station transmitter microphone input.

The TNC audio output is AC coupled through C19, a non-polarized Ø.1 uF 12-volt capacitor OR a 1 uF polarized electrolytic capacitor (assembly option). If your radio has a microphone input impedance below about 3ØØØ ohms, you should probably use the 1 uF capacitor and you should be alert to polarity reversal problems. The TNC side of the capacitor is at a potential of about Ø volts dc. If your input impedance is above about 3ØØØ ohms, you should use the Ø.1 uF capacitor.

NOTE: Do not attempt to use the TNC to directly drive a so-called carbon microphone input circuit as this is a low impedance input that requires considerable drive current. While damage to your TNC is not likely, excessive distortion of the transmitted signal is probable, which will result in unreadable packet transmissions.

Receiver and Transmitter Audio Level Adjustments:

Before connecting a radio to the TNC, connect the terminal and apply power. Ensure that the board has been properly initialized and that the sign-on message appears (see Chapter III, Operation).

Starting with the radio volume control at minimum, open the squelch (if applicable) and advance the volume control. Observing LEDs D7 and D8 (marked RCVR AUDIO on the silk screen), continue advancing the volume until the LEDs begin to flicker and glow from the noise. This is probably the optimum setting of the audio level to your TNC. It should be used as a starting point for final adjustment.

The transmitter audio signal level is set by R33 and should typically be about 2Ø mV peak-to-peak at the microphone connector, or about Ø.6 volts p-p (Ø.2 volts RMS) at test point TP1, located just above U31. In lieu of using an oscilloscope for this adjustment, on-the-air monitoring of your packet transmissions by other stations will generally prove adequate. For further details, consult the Calibration section of Chapter V.

RADIO INTERFACING DATA

Station (Call Sign):

Radio Manufacturer:

Model:

Microphone Connector--
Make:
Model:
Mic Audio Pin no.:
Mic Key Pin no.:
Mic Common Pin no.:

Headphone/Speaker Connector--
Make:
Model:
Audio Pin no.:
Common Pin no.:

Microphone audio drive level:

Measured open-circuit voltage from PTT line to common:

Measured "key-down" current through PTT line to common:

Schematic of any adapter needed to mate PTT and microphone audio to radio:

Copy and forward to:

Tucson Amateur Packet Radio Corp.
P.O. Box 22888
Tucson, AZ 85734

CHAPTER III

OPERATION

Introduction

In order to use your Terminal Node Controller for packet radio communication, you must attach it to a radio and to a terminal or computer. This Chapter assumes that you have followed the instructions in Chapter II to get your station hooked up. If you have gotten the TNC to type a sign-on message on your terminal, you are ready to operate. We will call the terminal or computer a "terminal" for simplicity, except when we discuss features which are peculiar to computers. You can use your computer to emulate a terminal by running a "terminal program" or a "modem program" or a "communications support program". We will use the term "terminal program" to refer to this type of program.

A TAPR TNC running the TAPR/AMSAT AX.25 software has several operating modes. When the TNC is turned on or reset, it will be in Command Mode. In this mode, everything you type is interpreted as instructions for the TNC. Instructions to the TNC are in the form of command lines terminated by a carriage-return (<cr>). These commands allow you to change the TNC operating parameters, perform special functions, or change modes. If your TNC receives packets while it is in Command Mode, you will be able to see them, but to send packets you must instruct the TNC to enter a data mode. Two data modes are provided: Converse Mode, and Transparent Mode. In these modes, the information you type to the TNC is assembled into packets and transmitted on the radio.

In addition to these operating modes, a special Debug Mode is provided which allows the user to examine and change memory locations. This mode is primarily of use to the software developers, and to people trying to track down obscure hardware faults. A list of Debug Mode commands is given in Appendix E for those who are interested.

The remainder of this Chapter describes how to use the commands to configure the TNC to suit you and your station, and how to get started talking to other hams on packet radio. This is not intended to be an exhaustive description of

every command, but rather a discussion of how the various commands are related and how they might be used. An alphabetical catalog of commands which describes the format and parameters of each is given in Chapter IV.

Terminal Characteristics

Most terminals use serial communications, in which each bit of a 7- or 8-bit character is sent in sequence over the same wire. Serial communications are usually described as being variations of the EIA standard called RS-232C. Serial data must be transmitted at a predetermined bit-per-second rate (baud). There are many standard rates, and unless the TNC and the terminal are using the same rate, they will not be able to communicate. The TNC supports all standard rates from 50 baud to 19,200 baud. Baud rates are selected from a table using the command ABAUD.

In addition to the data rate, there are three other characteristics of serial data which should be the same for the TNC and the terminal for best results. These are word length, parity, and number of stop bits. These characteristics are set using the commands AWLEN, ABIT, and PARITY. Serial data may represent ASCII data, which has 7 data bits per character, or binary data, which usually has 8 data bits per byte. Unless you are operating in Transparent Mode, the TNC will ignore the extra bit if you use 8-bit characters. This means that the eighth bit will be set to 0 before data is assembled into a packet.

Data may include a parity bit, which helps to detect data errors if there is noise on the data line. Parity may be even or odd. Even parity means that the parity bit will be set to a 1 or 0 to make the total number of 1s in the word even. The parity bit for odd parity is the reverse of that for even parity. Many terminals have an option of "no parity" which may mean different things for different manufacturers. This should mean that there is no parity bit included in the word, which would make the transmission one bit short. However, it may mean that the parity bit is there but not used for checking, and is always set to the same thing -- 0 (space parity) or 1 (mark parity). The TNC never checks the parity bit, but it can transmit any of these parity options for compatibility with any terminal. There are some restrictions on parity and stop-bit combinations.

The beginning and end of a word or character are marked by

start and stop bits. All serial data has one start bit and one or two stop bits. The stop bits are the minimum interval between successive characters. Usually, the only terminals which require two stop bits are old-style slow (110 baud) teletypes. The TNC does not support the combination of 8 bits, parity, and 2 stop bits.

If you change any of these terminal attributes, the new values will not take effect until you perform a reset of the TNC (using Switch 3, power-off, or by typing RESET).

If you start up the TNC in the default-parameter mode, with Switch 1 set, the serial port will be initialized at 300 baud, space parity, 7-bit characters, and one stop bit. A message will be typed at 300 baud, and if an * (asterisk) is entered before the message is completed, the TNC will sign on at 300 baud. If this does not occur, an autobaud routine will start. If the user types *s at one of the baud rates supported by the autobaud routine, the TNC will eventually sign on at that rate. When the TNC signs on, ABAUD will correspond to the result of the autobaud routine. The other terminal attributes are not affected by the autobaud routine.

Getting Started

When you have gotten your TNC and terminal set up properly, you should see the TNC sign on with a message, followed by a prompt:

```
cmd:
```

The first thing you should do when you see this prompt is to set your call sign and save it. To do this, type

```
MYCALL WB9FLW
```

using your own call, of course. The TNC will respond with

```
was
```

indicating that the call sign was blank. Save this information by typing

```
PERM
```

This writes your call and the terminal baud rate into the NOVRAM so that they will be set automatically the next time

you power your TNC up. You should move Switch 1 to the OFF position so that the TNC will use the NOVRAM information instead of the default values.

Once you have set your call sign, you are ready to try sending a packet. First, you should type the command CONVERS to enter Converse Mode. Now type a message to be transmitted as a packet:

Hello World!

End your message with a <cr>. If you watch the LEDs on the TNC as you do this, you should see several things happen. The PTT light will come on, indicating that the transmitter is being keyed. The TxD light will flicker on and off. If this is the first packet you have transmitted, and if CWID is ON, you will see the CWID light blink on and off as your call sign is transmitted in FSK. When the transmission stops, the PTT light will be off, and if a Morse Code ID was sent, the CWID light will be off. Don't worry if the TxD light is on at the end of the transmission. The encoding scheme used by the TNC represents data as transitions of the transmit-data line, so the light will be on at the end of a transmission about half the time.

The message you just transmitted was sent to the address specified by the UNPROTO command. This address will be set to CQ when the TNC is turned on or reset.

In order to make full use of the TNC's capabilities for reliable data communications, you should establish a connection with another station. This means that everything you type while in Converse Mode will be automatically addressed to that station, and packets sent between your station and the other station will be automatically acknowledged by the recipient. The sending station will continue retransmitting a message until it has been received correctly. To connect to NK6K, for example, return to Command Mode by typing <ctl-C> (or the character you have specified with COMMAND) and type

CONNECT NK6K

(The symbol <ctl-C> means the character produced by typing "C" while simultaneously pressing the "control" key.) If NK6K is on the air, tuned to your frequency, and within range of your transmission, you should notice a message coming back to your TNC. If you have your radio attached to a speaker as well as the TNC, you will hear the packets;

otherwise, you can see the DCD LED light up, and you may see the two incoming-data lights flicker. When your connect request (the packet your TNC sent) has been acknowledged, the TNC will display the message

*** CONNECTED to NK6K

and automatically move to Converse Mode. If you now type a message, it will be formed into a packet and sent to NK6K. When you are through with the conversation, either you or the operator of the other station may initiate a disconnect. To do this, return to Command Mode and type the command

DISCONNE

After an exchange of packets, you will see the message

*** DISCONNECTED

which indicates that your disconnect request packet was acknowledged by the station you were connected to. If you have enabled CWID, the TNC will send a final Morse Code identification as you disconnect.

Now that you have gotten your packet radio station on the air, we will take a few pages to describe the TNC operation in more detail. The remainder of this chapter will help you get the most out of your TNC. You should also refer to the alphabetical list of commands in Chapter IV.

Operating Modes

Command Mode

Command Mode is used to enter commands which alter the TNC's operating parameters. All other modes (except Debug Mode) must be entered from Command Mode. When the TNC is in Command Mode, the characters, "cmd:" are printed as a prompt at the beginning of each input line. This is the TNC's signal that it is waiting for instructions.

The TNC is always in Command Mode after a reset or power-up. A reset can be accomplished by disconnecting power from the TNC for several seconds, by toggling the reset switch (Switch 3) on the board, or by issuing the command RESET. After a reset operation, all operating parameters of the TNC are re-initialized by the resident software.

The values of most parameters can be stored in a permanent but easily changed form in the NOVRAM memory. Since this space is limited, some of the more complex parameters are not saved and must be initialized from the default values every time. If you change some of the parameters and want the new values used upon reset, you can store them in NOVRAM with the command PERM. In order to use these values, you must make sure that Switch 1 is not set ON when the TNC is reset, as this causes the default parameters stored in the software to be loaded.

There are several ways to get to a data mode from Command Mode. You can type the command CONVERS or TRANS, depending on the data mode desired. This will cause an immediate mode change. If you issue a CONNECT command to initiate a conversation with another station, or if your TNC receives a connect request packet, the TNC will automatically change to a data mode after the connection is established. The data mode used is specified by the CONMODE command as CONVERS or TRANS. However, if you specify the data mode in the CONNECT command, that mode will be used, without altering the setting of CONMODE.

There are two special modes which you can enter from Command Mode, Calibration Mode and Programmer Mode. These modes are actually special sub-programs. You can not send or receive packets in these modes (the packet controller chip is disabled), and you can only exit to Command Mode. Calibration Mode is discussed in detail in Chapter V. Programmer Mode is intended for using the TAPR EPROM Programmer Attachment, and is discussed in the manual for the attachment. If you accidentally get into the Programmer Mode, you will have to answer a few questions (<cr>s will do for answers) until you see the prompt "Option:". To return to Command Mode, type the letter "R" when you see this prompt.

Converse Mode

The data mode you will probably use most often for ordinary QSOs is Converse Mode. In Converse Mode, the information you type is assembled by the TNC into packets and transmitted over the radio. A packet is terminated whenever you type the send-packet character, which is set by the command SENDPAC and may optionally be included in the packet. In order to allow you to correct typing errors in your messages and to return to command mode, there are nine characters which have special meanings to the TNC

and are not ordinarily included in packets. These include input editing characters, which are discussed in a later section. Many of these special characters are control characters, which are typed by simultaneously holding down the "control" key and another key on the keyboard. The ASCII character set includes control characters corresponding to all the letters and a few other characters. As an example, a control C character is typed by holding down the control key and typing an upper or lower case letter C. This character is designated <ctl-C>. Some computers which do not have a control key, such as the Radio Shack Color Computer, have other methods of entering such characters. Also, some of the special-function keys (such as backspace and tab) may actually enter control characters. If you have no convenient way of typing control characters you may change these parameters to ordinary printing characters. Also, you should change any characters which will be used by your terminal program or computer operating system.

To get back to Command Mode from Converse Mode, you must type a special character. The default character is a <ctl-C>, and it may be changed with the COMMAND command. Typing the COMMAND character when the TNC is already in Command Mode does nothing.

Transparent Mode

An application for which packet radio is very well suited is transfer of large amounts of data between computers. For some types of data transfer operations, Converse Mode will work very well. However, you may want to send special information such as ready-to-run programs to another Amateur. A .COM file on a CP/M system or even a BASIC program may contain many strange characters which could be confused with the special reserved characters in Converse Mode. For this type of application, you will want to use Transparent Mode. Transparent Mode is a data mode like Converse Mode, except that in this mode there are no special characters -- everything you type (or everything your computer sends to the TNC) is sent over the radio exactly as it appeared to the TNC. Packets are sent at regular time intervals or when a full packet of information is ready. The time intervals at which data is packetized may be changed with the PACTIME command.

The display characteristics of the TNC are also modified in Transparent Mode. Data is sent to the terminal from

the TNC exactly as it is received over the radio channel, including all 8 bits of each byte received. All features such as line-feed (<lf>) and <cr> insertion, <escape> translation, and case conversion are disabled. In addition, echoing of input characters is disabled. None of the parameters which control these features in Converse Mode are changed by entering Transparent Mode, and all display features are re-enabled when the TNC is returned to Command Mode. Most of the informative messages which appear in Converse Mode as the TNC moves between disconnected and connected states are also disabled.

In order to escape from Transparent Mode to Command Mode, you must follow a special procedure. After a time interval equal to PACTIME the last data typed will have been packetized for transmission (although it may not be transmitted yet). You must then wait an additional time, which is set by the command CMDTIME. Following this wait, you must type three <ctl-C>s (or whatever character is set by COMMAND) within an interval CMDTIME of each other. After a final CMDTIME interval in which no characters are typed, you will see the "cmd:" prompt. If any characters are typed in this interval (even if they are more COMMAND characters) the escape will be aborted and the three COMMAND characters will be sent as packet data. If you set CMDTIME or PACTIME to zero you will not be able to escape from Transparent Mode except by performing a hard reset (switch or power-down reset).

Flow Control

Whenever data is transferred to computers (home computers or TNCs), there is a chance that the data will be received faster than the computer can handle it. Some programs try to prevent this problem by providing data buffers for storing incoming data until the program is ready for it. However, this merely postpones the problem, since there is a finite amount of room in any buffer. In order to prevent loss of data, the computer must be able to make whatever is sending data stop sending, and later tell it to resume sending. If you are a home computer user, you are probably already familiar with one type of flow control, which allows you to stop the output from the computer while you read it and restart it when you have finished.

There are two methods of providing flow control which are supported by the TNC. XON/XOFF flow control, sometimes

called "software flow control," is accomplished by sending a special character (usually a <ctl-S>) to request that the output stop and another special character (usually a <ctl-Q>) to restart output. Hardware flow control may be used if both computers use the Request To Send (RTS) and Clear To Send (CTS) lines of the RS-232C standard.

The single greatest difficulty experienced during Beta Test was correct implementation of flow control on the user's computer. Many inexpensive and commonly used terminal programs and file transfer programs for home computers do not implement flow control in software, and many "RS-232C" ports do not support hardware flow control. Even if the RTS and CTS lines appear at the connector, software which directly reads the CTS line may be required in order for flow control to be implemented. If you find that the TNCs seem to lose data during file transfers, you should immediately suspect a flow control problem.

XON/XOFF Flow Control

If you are using a terminal (rather than a computer) or if your computer does not support RTS/CTS flow control, you should use XON/XOFF flow control. This method of flow control is enabled by setting XFLOW ON. The special flow control characters are set to <ctl-S> and <ctl-Q> by default, but they may be changed. The commands XON and XOFF select the characters which will be sent to the terminal by the TNC, and the commands START and STOP select the characters which should be sent to the TNC by the terminal. If you set the codes for these characters to zero, you will disable that function. If you set START or XON to zero, STOP or XOFF will be automatically disabled as well.

The TNC's input buffer may fill up in Command Mode if you try to type too long a command. In data mode, the buffer may fill up if you are using your computer to transfer data at a rate faster than the data rate for radio transmission, if radio data transmission has slowed down because of noise or other users on the channel, or if the person or computer at the other end has stopped output from his TNC. The TNC will send the terminal an XOFF character when there is room remaining for about ten characters in the buffer. If you continue sending data until there are only five spaces left, the TNC will send an XOFF character after each character received. When the buffer fills up entirely, data will be lost. When

the buffer empties out, the TNC will send a single XON character to the terminal.

A computer file transfer program can very easily be unable to process data fast enough to keep up with output from the TNC. In order to be sure of reading every character, a computer must respond to "interrupts" from its I/O devices. Some simple programs, especially those written in BASIC, may "poll" the input register for new data. If the polling is not done often enough, data may be lost. If the program enters a routine which will not allow it to check for data regularly, it should send a STOP character to the TNC. Some disk operating systems, such as Apple DOS, disable all interrupts when they access the disk. The program should always send a STOP character before a disk access. If you send a STOP (START) character to the TNC when it is already stopped (started), the character will be treated as data. If you have enabled echoes, and the STOP or START character is echoed, this may have undesired consequences (a control-S will lock some keyboards). If the STOP and START character are the same character, this character will "toggle" the output, turning it off if it is on, and on if it is off.

If you disable XON and XOFF by setting them to zero, the TNC will automatically use RTS/CTS flow control to stop input from the terminal.

If you are using a terminal or terminal-emulating program, you may want to set XON and XOFF to something you can respond to, such as <ctl-G> (bell) or a printing character that you don't use.

XON/XOFF flow control is normally disabled in transparent mode, since all characters are treated as data. If you can not use RTS/CTS flow control, you may enable the XON and XOFF characters (the commands from the TNC to the terminal) by setting TXFLOW ON and XFLOW ON. However, START and STOP characters (the commands to the TNC from the terminal) will still be treated as data.

Hardware Flow Control

This method of flow control is preferred, since it usually does not depend on the programming of a particular communications program. The RS-232C handshaking signals are described in Chapter V.

Display Options

Several parameters control the way output is formatted for display on your terminal. Most of these are parameters which are determined by the display capabilities of your terminal and would be changed only if you changed terminals.

In Converse Mode it is natural to choose a line-termination character such as a <cr> or <lf> to terminate packets. However, for some applications, you may want to use an "invisible" command character to force the TNC to transmit a packet. In the first case, the send-packet character is interpreted as part of the input as well as a command; in the second case it is a command only. You can choose either option with command CR. If CR is on, the send-packet character is data, and is echoed to the terminal and included in the packet. You should disable CR if you are using packet timeout (CPACTIME ON) in Converse Mode.

A common occurrence when two stations are exchanging packets is for incoming packets to arrive when the user is in the middle of typing a line. In order to prevent the new line from disrupting the screen display, you can enable FLOW. In this mode, output to the screen is disabled as soon as you begin to type, and is enabled when a packet is completed. If you want to see the incoming packet before you transmit your line, you can type the redisplay-line character (normally <ctl-R>), which is set by the command REDISPLA. This will display the incoming packet and then retype your partially completed line. If FLOW is OFF and an incoming packet disrupts your typing, you can also use this character to redisplay your input line.

The parameter SCREENL sets the width of the terminal screen or page. Whenever this number of characters has been sent to the terminal without an intervening <cr>, a <cr> is inserted in the output. A <cr> is also echoed if you type a line that exceeds the width of your screen; however, the extra <cr> is not included in a packet. If your terminal performs automatic line-wrap you should disable this feature by setting the SCREENL parameter to Ø. The TNC does not carefully distinguish between printing and non-printing characters and it does not correct its line count for horizontal tab characters; however, backspace characters are counted correctly.

For normal display, a <cr> <lf> is the new-line sequence. However, a user terminates a line of typing with a single

character, usually a <cr> (called ENTER on some terminals). If only a <cr> is displayed, the next line will be typed over the previous one instead of appearing below it. Some terminals automatically display a <lf> following each <cr>, but most do not. If auto line-feed is enabled by AUTOLF the TNC will add a <lf> after every <cr> displayed or echoed.

A few terminals and many home computer monitors do not display lower case characters. Most of these terminals convert all characters to upper case for display, but some of them may use lower case character codes for special control functions. If you have such a terminal, you should disable lower case display with LCOK OFF. This does not affect the way echoed characters are displayed. This feature can be used to easily distinguish your outgoing messages from incoming packets on the screen. If you disable lower case display, incoming data will be converted to upper case, while your own messages will appear in lower case if you type them that way.

The conventional response to character deletion on display terminals is to output to the terminal a backspace, space, backspace sequence. This removes the character from the screen and leaves the cursor ready to type a new character in its place. On a hard-copy terminal, however, this results in unreadable text. If backspace display is disabled with BKONDEL OFF, a backslash symbol (\) will be displayed for each character deleted. The redisplay-line character can be used to see the corrected line.

The <escape> character (ASCII code 27 or hex $1B) is used by many terminal to control cursor movement and special display modes. (Note: throughout this manual the dollar sign symbol prefaces all hexadecimal numbers.) If this effect is not desired, <escape> translation should be enabled with ESCAPE OFF. This will cause all <escape> characters to be sent to the terminal as the dollar sign symbol ($). This does not affect <escape> characters which are transmitted in packets.

Some terminals echo characters typed in locally, before transmitting the character to the I/O port. Also, some terminal programs on computers may perform local echoing. If the TNC also echoes characters, you will see two of every character. Echo mode can be disabled with ECHO OFF.

A few terminals require particularly long times to respond to <cr>s or <lf>. Some hardcopy terminals, including old Silent 700 terminals, require time to move the print head to

the beginning of the line following a <cr>. Some display terminals require long times to scroll their screens following a <lf> character. If characters are sent to such a terminal before it is ready, the characters will be lost. If your terminal always loses a few characters at the beginning of a line, you need to enable null insertion. A null is a character with ASCII code Ø, and the TNC does not actually transmit nulls in this mode, since they are misinterpreted by some computer's terminal programs as a BREAK signal. Instead, the TNC waits the specified number of character time intervals before transmitting more data. The number of null intervals is set by the command NULLS, and null insertion after <cr>s and <lf>s is separately enabled by NUCR and NULF.

Editing Commands

Several characters are used to correct mistakes in the text typed into the TNC. Except in Transparent Mode or if timed packets are in effect in Convers Mode, no text characters are interpreted by the TNC until it receives a <cr> or the send-packet character (in Converse Mode). Until then, you can delete and retype characters or cancel the line completely.

Editing characters would ordinarily be chosen to be control characters or other non-printing characters. Editing functions can be disabled by setting the character used for the function to $ØØ. This prevents any character (even a null) from being matched to that function.

The key usually used to remove the last character on a line may be either a <delete> character (hex $7F) or a backspace (hex $Ø8 or <ctl-H>). The character used is determined by the DELETE command, which has options ON (<delete>) and OFF (backspace). The key used for rub out on your terminal may be labelled "back space", "delete", "rub out", or simply "<--". You may have to experiment to find out whether it produces a backspace or a <delete> character.

Attempts to delete past the beginning of a line, or, in Converse Mode, past the beginning of a packet, will have no effect. You can not delete a <cr> or any of the special characters which are not inserted into the text, such as the send-packet character or flow-control characters. These characters cause actions which take place immediately. Some home computers allow you to fix errors in input lines by

backing the cursor up to the mistake, retyping the wrong characters, and then spacing forward with an arrow key. The TNC does not have this sort of input editing feature -- you must delete all the characters back to the ones you want to change and then retype the rest of the line.

If you want to cancel an entire line, text back to the last-occurring <cr> can be deleted by typing the character specified by the CANLINE command (default <ctl-X>). In Converse Mode, this character does the same thing as the cancel-packet character unless you have set the send-packet character to something other than a <cr>.

In Converse Mode, text back to the last-occurring send-packet character is deleted by typing the character specified by the CANPAC command (default <ctl-Y>). This will delete any intervening <cr>s. Packets which have already been placed in the outgoing packet buffer can not be cancelled.

The cancel-packet character has a special function in Command Mode. Typing this character will cause the terminal output buffer to be flushed and all output to be diverted to the write-only memory (otherwise known as the Bit Pit). Normal output can be resumed by typing another cancel-packet character or by changing modes, e.g., going into Converse Mode. Echoing of input is not affected by this command. This feature makes it possible to get rid of unwanted output which may occur. For example, the TRACE command can very quickly produce enormous amounts of output which, because of the large TNC buffers, may not all be typed until long after the function has been disabled.

You may occasionally wish to transmit one of the characters that you have assigned to a special function. The pass character is intended to increase the flexibility of Converse Mode by providing this capability. To insert such a character into the input buffer, precede it with the character specified by the PASS command (default <ctl-V>). Any character (including non-special characters and the pass character) can be sent this way. Since the pass character is kept in the buffer until a packet is formed, two <delete>s are required to remove both the quoted character and the pass character. Note that the PASS character will cause only one special character to be inserted -- you must type it again for each such character.

The pass character can be used in Command Mode in order to include <cr>s in the beacon text.

Packet Operation

The primary function of the TNC is to enable you to communicate via packet radio with other Amateurs. The TAPR TNC implements two protocols, or sets of rules for formatting messages to other TNCs. These protocols, AX.25 and VADCG, are both designed primarily for point-to-point two-party communications, although they can be used to simulate the common Amateur net or roundtable type of contact. The protocol used by the TNC can be specified as AX.25 with the command AX25 ON, or as VADCG with the command AX25 OFF. Packets heard by the TNC not corresponding to the protocol selected will be ignored.

Earlier in this Chapter you found out how to set your call sign and issue the CONNECT command to talk to a specific station. These commands are the beginning of packet operation, which we will now discuss in more detail.

In order to establish a two-way connection, the TNC must know your station address and the address of the party you want to talk to. To prepare your TNC for operation, the first thing you should do is to establish your call sign as the station address using the command MYCALL. This sets the string used to identify packets transmitted by your station. If you will be operating using VADCG protocol, you must also set your VADCG address using the command MYVADR. The VADCG address is a number from 1 to 31 which is used to identify VADCG packets once a connection is established. Neither protocol will work properly if there is more than one station on the air with a given address. The VADCG address of your TNC must be coordinated locally to insure that there will be no conflicts. If you will have more than one station operating the AX.25 protocol using your call sign, you can give them different addresses using the substation ID (SSID) extension, a number from Ø to 15. This number is appended with a dash, as in this example:

```
MYCALL W3IWI-3
```

If you don't specify the SSID extension, it will be Ø. The extension does not affect the Morse ID of your station.

The call sign specified by MYCALL is ordinarily used by the TNC for Morse Code identification, which is sent automatically every 9-1/2 minutes (or as soon thereafter as the channel is available). In some locations the address string included in the packets may be considered adequate

identification for legal purposes. However, you should be aware that the call sign address within the packets is actually bit-shifted ASCII (occupying bits 1-7 of the byte rather than bits Ø-6). Also, if you use the VADCG protocol, your call sign is included only in the initial packets establishing a connection. TAPR recommends that you identify your station in either Morse Code or voice. This makes it easy for other operators to tell who is on the air without displaying every packet, and also improves relations with non-packet operators using the same frequencies. The automatic Morse Code ID can be disabled by the command CWID OFF. If you wish to use an identifier other than your call sign as your packet address, you can set the Morse ID with the command IDTEXT. This also allows you to include other information along with the call sign in the ID. If you disable the automatic ID, you can still command the TNC to identify in Morse Code at any time with the command ID.

You have already been introduced to the CONNECT command, which causes your packets to be sent to a specific station. If the station you want to talk to is a little too far away for you to contact directly, you can use the digipeating feature of the TNC. This feature works differently depending on whether the AX.25 protocol or VADCG protocol is used. A digipeater accomplishes much the same thing as an ordinary repeater in extending the range over which you can communicate. The difference is that your messages are copied and relayed by the digipeating packet station. This achieves better quality of the signal received at the destination at the expense of some delay while the intermediate message is received and retransmitted.

To request digipeating under AX.25 protocol, you must specify the intermediate packet station or stations which you want to relay your messages. You do this as part of the CONNECT command by using the VIA subcommand.

```
CONNECT WA7GXD VIA NØADI-2,KV7B
```

You should list the intermediate stations in the order in which you want them to relay the packets as they go from your station to the destination station. In this example, your connect message to WA7GXD will be repeated by NØADI-2 and then by KV7B. Reply packets from WA7GXD will be relayed first by KV7B and then by NØADI-2.

You can specify as many as eight intermediate stations; however, bear in mind that using more than one digipeater is a

TAPR extension to AX.25 and may not be compatible with other implementations of this protocol. In particular, the TAPR Beta Test TNCs were released with a software version which supports up to three intermediate stations. These TNCs will not function properly with their original software if other stations in the area send packets addressing more than three relay stations. The delay between your transmission and the receipt of a reply will naturally increase as more intermediate relays are used, and the likelihood of losing information due to interference or noise on the channel also increases.

You can specify intermediate digipeaters to be used for unconnected packets using the UNPROTO command with the same format as the CONNECT command:

```
UNPROTO QST VIA NK6K
```

This will cause packets sent when you are not connected to another station to be sent to QST (rather than the default of CQ), digipeated by NK6K.

To request digipeating under VADCG protocol, give the command

```
VRPT ON
```

This causes your packets to be repeated by any VADCG digipeater. For a TAPR TNC to act as a VADCG digipeater, the command

```
VDIGIPEA ON
```

must be given. The VADCG protocol will not function properly if there is more than one VADCG digipeater in an area, since all digipeaters will simultaneously attempt to relay packets requesting digipeating.

For special applications you can disable the TNC's ability to connect or to transmit. If you have left your TNC running to monitor channel activity in you absence but want to inhibit it from transmitting, set XMITOK OFF. The TNC will conduct normal operations in this condition, including formatting and "sending" packets, but will not key the transmitter. You may also want to specify CONOK OFF. This prevents the TNC from accepting connect requests from other stations (although it does not stop you from initiating a connect request of your own).

If a connect request is received when CONOK is OFF, the TNC will send a "station busy" packet to the requesting station and display a message such as:

*** connect request: KD4NL

identifying the requesting station. If the TNC receives a "station busy" message in response to a connect request, it will display a message such as:

*** KD2S station busy

showing the call sign of the station you tried to connect to. These messages are also used if a TNC is connected to another station when a request is received.

In addition to transmitting information typed in from a data mode, the TNC can be commanded to send a specific message at regular intervals. This message is called a beacon, and the function can be used to send announcements or allow other packet users to test their equipment. To set the beacon text to your message, type the command

BTEXT

Everything you enter on the command line following the space after BTEXT will be entered into your message string. To transmit the beacon at, for example, 1Ø-second intervals, give the command

BEACON EVERY 1

The beacon function also has a transmit-after mode, in which a beacon packet is only transmitted after activity is heard on the channel. This feature has been used to leave a message for other packet users. If someone initiates a connection (or sends anything, for that matter) on an otherwise idle channel, a beacon can be sent a short time later with a message such as "I'll be back on the air on packet after dinner -- call me then." If the station is monitoring beacon packets (see monitor mode discussion below) he will see this message. No beacons are sent in this mode if there is a lot of packet activity on the channel, since the required period of quiet will not occur.

Packet Timing Functions

Five adjustable timing parameters are provided for

configuring the TNC to your particular radio environment. Some other parameters which are related to the timing parameters are discussed here as well.

Amateur radio equipment varies greatly in the time delays required in switching from receive to transmit and from transmit to receive. When two stations are sending packets back and forth, these delays must be allowed for. If data is sent before the transmitter is operating, the packet will not be transmitted properly. Similarly, if the receiving station has not had sufficient time since it stopped transmitting for the receiver to become active, data will be lost. The delay between transmitter keyup and the beginning of data transmission is controlled by the command TXDELAY. Ordinarily, this parameter should be set to the same value by all members of a local packet group, and it should be determined by the slowest pair of stations in the group.

If you are transmitting packets through an audio repeater, you may require a considerably greater keyup delay than is required for direct communications. The command AXDELAY allows you to specify an additional keyup delay to allow the repeater receiver and transmitter to lock up. If the repeater has a long "hang time" and stays up for a while after the transmitting station has been unkeyed, you can make use of this time with the AXHANG command. If the TNC has detected channel activity recently enough that the repeater should still be "up," it will wait only the TXDELAY time before sending data, rather than adding an AXDELAY time as well.

The parameters set by TXDELAY, AXDELAY, and AXHANG are all specified as numbers between Ø and 15. The actual delay in milliseconds is a multiple of the input parameter, 4Ø ms per count for TXDELAY and 12Ø ms per count for AXDELAY and AXHANG. During the time the TNC is keying the transmitter but not sending data, it will transmit a synchronizing signal (flags). Thus, the total keyup delay will only be

Keyup delay = TXDELAY*4Ø + AXDELAY*12Ø

in milliseconds. If channel activity has been heard more recently than AXHANG*12Ø ms ago, the keyup delay will be

Keyup delay = TXDELAY*4Ø

in milliseconds. If your radio is exceptionally slow to key up, you can use AXDELAY to augment the maximum delay available with TXDELAY by setting AXHANG to Ø.

Both AX.25 and VADCG protocols provide for retransmitting packets if no acknowledgment is heard from the connected station within a certain period of time. A packet may not be acknowledged due to channel noise or "collision" with another packet transmission. Since there may be other stations on the channel, the receiving station may not be able to acknowledge the received packet immediately. The time lapse before the originating station retransmits the packet is set by the command FRACK (frame acknowledge time). The maximum number of retransmissions before the originating station terminates the connection is set by the command RETRY. The maximum number of transmissions of a packet is RETRY+1, since the initial transmission does not count as a retransmission.

The frame-acknowledge time is automatically corrected for the additional time required for digipeating. An extra time delay is added for each transmission which must be made after origination of the packet in order to deliver the packet and receive the acknowledgment. The time interval before the TNC retransmits an unacknowledged packet is therefore

Retry interval = FRACK * (2*n + 1)

in seconds, where n is the number of calls in the digipeat field of the address.

Both the AX.25 and VADCG protocols specify that acknowledgments of digipeated packets be made from end to end. This means that intermediate digipeaters do not acknowledge the packets they digipeat. When the destination station receives the packet, it generates an acknowledgment which is sent through the reverse route used by the original packet. If there are several intermediate relays, the chance of either the original packet or the acknowledgment to be lost increases drastically. To help alleviate this problem, an automatic wait time can be imposed on any station not transmitting a digipeated packet. Any station which is ready to transmit a packet immediately after the carrier drops is required to wait for this time interval unless it will be transmitting one or more digipeated packets. This means that the chance of a collision involving a digipeated packet is reduced, since once a transmission begins, other stations will wait for a clear channel. The digipeat wait time is set by the command DWAIT, which specifies 40 ms intervals. If no digipeating is being done by anyone in the local area, this parameter can be set to 0, but in any event it should

be set to the same value by all members of a local packet group.

In order to avoid unnecessary packet retries with the associated channel load, the TNC implements a collision-avoidance strategy which applies to all packets except those which are being relayed. On the second and subsequent transmission of a particular packet, the TNC waits an additional random time after detecting a clear channel before beginning transmission. This strategy is based on the assumption that packets which were not acknowledged suffered collisions with transmissions from other stations. If the random wait is used, repeated collisions of transmissions by the same two stations can be prevented, since eventually they will wait different time periods and one station will capture the frequency. The random time is a multiple (Ø-15) of the TXDELAY time. This is because TXDELAY represents the interval during which a transmitter may have been keyed but can not yet be detected by other stations. The interval, in milliseconds, between the TNC detecting carrier-drop and beginning to transmit is

Wait time = DWAIT * 4Ø

for the first transmission of a packet. For subsequent transmissions of the same packet the interval is

Wait time = (DWAIT + r * TXDELAY) * 4Ø

where r is a random number from Ø to 15.

Both AX.25 and VADCG protocols allow multiple packets to be transmitted before waiting for an acknowledgment. This permits more efficient channel use when large amounts of data are being transfered. The maximum number of packets which the TNC will send before waiting for acknowledgment is specified by MAXFRAME. Of course, the TNC will not wait until MAXFRAME packets have been entered before transmitting -- this parameter is only used to limit the transmission if more than one packet is ready when the TNC begins to transmit. MAXFRAME in combination with PACLEN determines how much information can be sent in a single transmission. The best combination for efficient data transfer is determined partly by the channel quality and partly by the rate at which the terminal can process data. For a 12ØØ baud terminal data rate, you should start with a combination that produces about 3ØØ characters outstanding at one time.

The radio data transmission rate is set by the command

HBAUD. This command selects a baud rate from a table of standard rates similar to the command ABAUD for terminal baud rate. Note that there is no relationship between terminal baud rate and radio baud rate. Also, be aware that the baud rate table is different for HBAUD and ABAUD. A 400 baud option for radio baud rate is included for AMSAT operations, and only rates through 4800 baud are supported (9600 with high-speed clock option). In order to communicate with another packet station you must use the same radio baud rates. The length of time required to send a given amount of information depends inversely on the baud rate, so that it takes four times as long to send a line at 300 baud as at 1200 baud. If you use slow radio baud rates, you should either limit the length of transmissions determined by MAXFRAME and PACLEN so that the hardware watchdog timer does not disrupt your transmissions, or disable or modify the watchdog. The Bell-202 compatible modem is the optimum design only for 1200 baud radio data rate. For HF operation at low baud rates, you should consider configuring the modem for a different set of tones. The modem is not useful at rates higher than 1200 baud, although the TNC will provide data signals at up to 4800 baud with the standard clock; an external modem is required for such operation.

Monitor Functions

Although the AX.25 and VADCG protocols are primarily oriented toward setting up "circuits" between two stations, this is not the way many Amateurs operate. The TNC can also operate in a mode more suitable for a "net" or "round-table" discussion with several participants, although reliable reception of your transmissions by every station can not be guaranteed. This is done by enabling the monitor functions. Monitoring is enabled by the command MONITOR ON, and separate monitor functions are individually enabled. This set of functions allows you to see displayed packets from selected stations or classes of stations. You can list up to ten call signs of stations to monitor with the commands MFROM and MTO. Packets are displayed if any of the call signs specified by MFROM appear in the "from" field of the packet address, or if any call signs specified by MTO appear in the "to" field. If you specify ALL in place of either of these lists, you will see all the packets your TNC receives. If you specify NONE in place of a list, that list will not be used to select monitored packets. Although a list of ten call signs would be too long to store in the TNC's NOVRAM memory, if you have MTO or MFROM set to ALL when you give

the PERM command, this information will be saved; otherwise the NONE choice will be saved.

Monitored packet display is somewhat different from the display of connected packets. Each packet is displayed with the source and destination stations identified:

```
KV7D>NK6K:Go ahead with the file transfer.
```

If a connected packet QSO is taking place on the frequency of your group conversation, you may wish to ignore all connected packets while your group operates in unconnected mode. The command MALL OFF will cause connected packets to be ignored. If you want to be able to monitor packet activity when your station is not connected but have the feature automatically disabled when you connect to someone, you should command MCON OFF. If you have MALL ON and MCON ON and you are monitoring the station you are connected to, packets from that station will be displayed only in the monitor format and not in the usual manner with no station identification.

You can operate a group conversation with some data integrity by having the stations connect in pairs and set MALL ON and MCON ON. This does not insure that every packet is received at every station, but it does insure that a packet involved in a collision will be retried. You may occasionally see duplicate copies of a packet in this mode if the acknowledgment packet is lost. If you have an odd number of stations participating in this sort of conversation, one station can connect to himself via another station as digipeater. This station will have the disadvantage of having to see his own packets re-displayed. For example, WB6YMH, WDØETZ, WAØTTW, W1HDX, and K4NTA wish to carry on a group conversation. In order to make all the transmissions as reliable as possible, the following connections are made.

```
WB6YMH connects to W1HDX
WAØTTW connects to K4NTA
WDØETZ connects to WDØETZ via W1HDX
```

If each station specifies MCON ON, MALL ON, and MTO ALL, each station will see the packets sent by all the others.

HF and OSCAR

When configured in the default mode, the TAPR TNC is optimized for a local VHF FM environment. The modem is

configured for best response at 12ØØ baud. The settings of MAXFRAME and PACLEN provide the possibility of several continuous frames of long data length. The type of data link offered by the average HF or OSCAR 1Ø path is very different. Lower signal to noise ratios require lower baud rates, noise spikes and fades require shorter packet lengths, and a higher rate of false carrier detects lowers the total, usable dynamic range in the audio input. The TNC hardware and software provide many methods to improve throughput in high noise environments. While the best results can be obtained through your own experimentation, some problem areas and hints are given below.

The TNC detects a busy channel through the lock-detect signal from the demodulator. The presence of a lock-detect signal is indicated by the Data Carrier Detect (DCD) LED. For best results, the LED should be off when no data is present. Turn the volume on your receiver down to the point where the LED is on full when data is present and only gives an occasional flicker at other times. In most cases it will not be possible to keep the LED completely off. On a noisy channel, many spurious lock-detect signals may be generated. Each time DCD comes on, the TNC will start another DWAIT interval before it considers the channel to be clear, usually resulting in long periods of silence from your TNC. For this reason, you must set DWAIT to Ø for HF or OSCAR operation. You can disable the random retry wait by setting TXDELAY to Ø and using AXDELAY to force a suitable keyup delay. Note that the units for TXDELAY and AXDELAY are different, however.

If you are operating a full-duplex radio station (simultaneous transmit and receive) such as an OSCAR 1Ø station, you should set the parameter FULLDUP. While the TNC is always capable of full duplex operation, this parameter will cause the protocol to behave slightly differently in acknowledging packets. Also, the state of the DCD line will be ignored. You may be able to improve operation somewhat by disconnecting the DCD line at the modem connector (J5).

Intuition will tell you that lower baud rates will reduce the number of packet retries. In fact, there is usually a small range between too fast and too slow. A slower packet is a larger target for fades and static crashes, and all of a packet must be received correctly. Data rates as high as 12ØØ baud (HBAUD 12ØØ) have been used on both 1Ø meters and through OSCAR 1Ø on a Maryland to New Zealand path using unmodified Beta TNCs with signal to noise ratio of 12-15 db.

You can improve the response of the modem for low baud rates by calibrating the modulator and demodulator tones for a narrow shift. This also permits the use of a narrow filter by the receiving station. You may also wish to move the demodulator center frequency to a lower value. Modem parameters and timing constants can be changed by using different parts on headers U34 and U35. Consult the EXAR notes listed in the Bibliography for the straightforward design procedure. The use of the calibration routines for special tones is discussed in Chapter V.

The audio frequency response of the TNC in combination with a SSB transceiver is likely to be less than optimum. If you will operate your TNC on HF or OSCAR and your transceiver does not require the 56Ø ohm load resistor (R59) you should not install it, as it degrades the low frequency response. The input filtering provided by the MF-1Ø switched capacitor filter can be modified by changing the values of the components on the dip header U3Ø. Various alternate filter configurations are listed from time to time in TAPR's newsletter, PSR.

The onboard modem can be completely bypassed using J5. This allows use of exotic modulation methods and higher baud rates. The interfaces available on J5 are TTL levels and not RS-232C. Be sure to refer to the hardware section for more information.

CHAPTER IV

COMMANDS AND MESSAGES

Command Syntax

There are many variable parameters which are used by the TNC in its operation, such as the user's call sign, terminal type, display preferences, and the characteristics of his radio. In addition, there are several actions the TNC can be commanded to perform, such as connecting to another station to start a conversation, disconnecting at the end of the QSO, saving information in NOVRAM, or displaying information about itself. The user changes parameters and issues instructions to the TNC by typing commands composed of English-like words or word abbreviations, which are called keywords, and variables which are numbers or strings of characters chosen by the user. You will probably never change some of these parameters; however, one of TAPR's goals is to allow each user maximum flexibility to adapt the TNC to his environment.

All commands are listed alphabetically below. If a command has parameters, each parameter is described and the default values are given. The defaults are the values stored in EPROM which may be loaded instead of those in NOVRAM by setting Switch 1 on prior to performing a reset. Those parameters which can be saved in NOVRAM and recalled after power-down are marked with an asterisk at the right margin. Each parameter is described and the possible values are given. A brief discussion of the command is then given. A more detailed discussion of many of the commands and their interrelationships is given in Chapter III. A command is entered to the TNC by typing it when you see the Command-Mode prompt,

```
cmd:
```

The command keywords and parameters are separated by spaces, and the TNC takes action when a carriage return (<cr>) is typed. All keywords may be entered in upper or lower case. Except for the beacon and ID text string, everything entered in Command Mode is translated to upper case before being examined. All commands and alphabetic parameters may be

abbreviated to the shortest unique string. These minimum abbreviations are shown underlined in the commands section of this chapter.

There are several parameter types. A parameter which is denoted as "n" is a number, and can be given either in decimal or in hexadecimal (base 16). When the TNC shows some of these parameters (those which set special characters), they will be given in hex. A hex number is distinguished from a decimal number by preceeding it with a "$" prefix. The "digits" of a hex number represent powers of 16, analogous to the powers of 10 represented by a decimal number. The numbers 10 through 15 are denoted by the hex digits A through F. For example,

$1B = 1*16 + 11 = 27
$120 = 1*16*16 + 2*16 + 0 = 288

The TRACE command parameter is given as a bit-code. This means that several related values are simultaneously set by this command, and the parameter is formed by adding together the numbers corresponding to each value desired. You may find it convenient to think of this number in hexadecimal.

Many parameters are "flags," meaning that they have two possible values, ON and OFF, or YES and NO. All of the commands descriptions show ON and OFF as the options; however YES and NO may be typed instead. A few parameters are really flags, but rather than indicating that something is "on" or "off", they select one of two ways of doing things. Some of these parameters have the values EVERY or AFTER, indicating how a time interval for a repeated action is to be treated. Others are CONVERS or TRANS, indicating operating modes for data transmission.

Several commands require call signs as parameters. While these parameters are normally Amateur call signs, they may actually be any collection of numbers and at least one letter up to six characters; they are used to identify stations sending and receiving packets. A call sign may additionally include an "extension", which is a decimal number from 0 to 15 used to distinguish two or more stations on the air with the same Amateur call (such as a base station and a repeater). The call sign and extension are entered and displayed as call-ext, e.g., K0PFX-3. If the extension is not entered, it is set to -0, and extensions of -0 are not displayed by the TNC.

Several parameters are numerical codes for characters which

perform special functions. The code is simply the ASCII character code for the desired character. These characters have as default values control characters. Control characters are entered by holding down a special control key on the keyboard while typing the indicated key.

There are two commands, BTEXT and IDTEXT, which have as parameters a text string. This string can be any combination of letters, numbers, punctuations, or spaces up to 128 characters. You can even put characters with special meanings, such as carriage returns into the string by preceding them with the "pass" character. The string ends when you type a (non-passed) carriage return.

In the command descriptions below, the keywords are shown in upper case. User-supplied values are shown in lower case. If a parameter must be chosen from one of two values, the choices are separated by a vertical bar. Optional parameters are shown in square brackets. For example,

```
KEYWORD var A|B [C|D]
```

This means that the command KEYWORD requires a user-supplied variable var and either A or B. In addition, the user can optionally specify either C or D.

You can examine the value of any parameter by typing the command which sets this parameter followed by a <cr>. A special command, DISPLAY, allows you to see the values of all parameters or groups of related parameters.

Commands

ABAUD n default: not applicable *

parameters

n selects a baud rate from the list below.

This command sets the baud rate used for input and output through the serial port. The parameter n selects one of the following baud rates:

Ø	Reserved for future use.		
5Ø		18ØØ	
75		24ØØ	
11Ø	#	36ØØ	
135		48ØØ	#
15Ø		72ØØ	*
3ØØ	#	96ØØ	#*
6ØØ		192ØØ	*
12ØØ	#		

- These baud rates are detected by the autobaud routine which operates when the TNC is initialized with the default parameters.

* - The TNC may not be able to perform its other functions while performing input or output at these baud rates. Use of baud rates higher than 48ØØ is not recommended for computer file-transfer applications.

The baud rate change will not take effect until a RESET command is issued. The TNC enters an autobaud routine and attempts to match the terminal baud rate if Switch 1 is used to select default parameters. If the TNC is initialized with default parameters, the autobaud routine will set ABAUD to reflect the baud rate it detects.

ABIT n default: 1 *

parameters

n 1 - 2, specifying number of stop bits.

This value selects the number of stop bits used by the

6551 UART. The number of stop bits will not change until a reset is performed.

AUTOLF ON|OFF default: ON *

parameters

ON A <lf> is sent to the terminal after each <cr>.

OFF A <lf> is not sent to the terminal after each <cr>.

This option should be ON when a terminal is used that does not automatically send a linefeed after a carriage return. If AUTOLF is ON, a <lf> is sent to the terminal after <cr>s in received packets as well as echoed <cr>s received from the terminal. This command only affects what is displayed, not the data sent in packets.

AWLEN n default: 7 *

parameters

n 7 - 8, specifying number of data bits per word.

This value defines the word length used by the 6551 UART. A word length change will not take affect until a reset is performed. Except in Transparent Mode, only the low order 7 bits of a word are kept. To use all 8 data bits in Transparent Mode, AWLEN must be set to 8.

AX25 ON|OFF default: ON *

parameters

ON The TNC operates in AX.25 protocol.

OFF The TNC operates in VADCG protocol.

If ON is specified, the TNC will originate packets and other link operations in AX.25 mode. Packets received in the VADCG protocol when AX25 is ON, or packets

received in the AX.25 protocol when AX25 is OFF will not be interpreted or displayed.

AXDELAY n default: Ø *

parameters

n Ø - 15, specifying 12Ø ms intervals.

This value specifies a period of time to wait in addition to TXDELAY after keying the transmitter before data is sent. This will be used by groups using a standard "voice" repeater to extend the range of the local area net. Repeaters using slow mechanical relays, split sites, or combinations of both require some amount of time to get RF on the air. AXDELAY can also be used when the receiving TNC's rig has a very slow PLL or squelch.

AXHANG n default: Ø *

parameters

n Ø - 15, specifying 12Ø ms intervals.

This value can be used to increase channel utilization when an audio repeater with a hang time greater than 12Ø ms is used. If the repeater squelch tail is long, it is not necessary to wait for AXDELAY after keying the transmitter if the repeater is still transmitting. If the TNC has heard a packet sent within the AXHANG period, it will not add AXDELAY to the keyup time.

BEACON [EVERY|AFTER] n default: EVERY Ø *

parameters

EVERY Send beacon every specified interval.

AFTER Send beacon once after the specified interval with no link activity.

n Ø - 255, specifying 1Ø second intervals.

Beacon mode is turned on by this command. n specifies a time in multiples of 1Ø seconds. A value of Ø for n

turns the beacon off. If the optional keyword EVERY is used, a beacon packet is sent every n*10 seconds. If AFTER is used, a beacon is sent only after n*10 seconds have passed with no activity detected on the RF channel; however, the beacon is sent only once until further activity is detected. This mode can be used to send announcements or test messages only when packet stations are on the air, and avoid adding unnecessary activity on the channel.

A beacon frame consists of the text specified by BTEXT in a packet addressed to "BEACON" and sent via the digipeat addresses specified by the UNPROTO command. These frames can be monitored from other TNCs by setting MONITOR ON and MTO BEACON.

BKONDEL ON|OFF default: ON *

parameters

ON The sequence backspace-space-backspace is echoed when the DELETE character is entered.

OFF The backslash character (\) is echoed when the DELETE character is entered.

This command determines the way the display is updated to reflect a character delete in Command Mode or Converse Mode. The backspace-space-backspace sequence will properly update the screen of a video display.

BTEXT text

parameters

text - any combination of characters and spaces.

BTEXT specifies the content of the data portion of the beacon packet. The default text is:

TAPR/AMSAT AX.25 level 2 protocol software version X.Y

where X.Y represents the software version number. The <cr> character can be included in the text by using the pass character (default is <ctl-V>) preceding <cr>. A maximum of 128 characters can be specified. The beacon text is not stored in NOVRAM.

CALIBRA

The CALIBRA command is used to transfer control to the hardware calibration routine. The commands available from this routine are described in the modem calibration section of Chapter V. Calibration may be performed at any time without altering the current link state.

CANLINE n default: $18 <ctl-X> *

parameters

n Ø - $7F, specifying an ASCII character.

This command is used to change the cancel-line input editing command character.

CANPAC n default: $19 <ctl-Y> *

parameters

n Ø - $7F, specifying an ASCII character.

This command is used to change the cancel packet terminal editing command character.

This character functions as a cancel-output character in Command Mode. Typing the cancel-output character a second time restores normal output.

CMDTIME n default: 1 *

parameters

n Ø - 15, specifying 1 second intervals.

This command sets the Transparent Mode timeout value. In order to allow escape to Command Mode from Transparent Mode while permitting any character to be sent as data, a guard time of CMDTIME seconds is set up. Three COMMAND characters must be entered within CMDTIME of each other, with no intervening characters, after a delay of CMDTIME since the last characters were typed. After a final delay of CMDTIME, the TNC will exit

Transparent Mode and enter Command Mode. You should then see the prompt

cmd:

If CMDTIME is zero, the only exit from Transparent Mode is a hardware reset.

COMMAND n default: $Ø3 <ctl-C> *

parameters

n Ø - $7F, specifying an ASCII character.

This command is used to change the Command Mode entry character. Command Mode is entered from Converse Mode when this character is entered from the terminal. See Chapter III for information on how the command character is used in Transparent Mode.

CONMODE CONVERS|TRANS default: CONVERS *

parameters

CONVERS Connects cause automatic entry to Converse Mode.

TRANS Connects cause automatic entry to Transparent Mode.

CONMODE controls what mode the TNC will be placed in after a connect. The connect may result either from a connect request received over the air or a connect initiated by a CONNECT command. If the optional mode parameter is given with the CONNECT command, the CONMODE parameter will not be used. If the TNC is already in Converse or Transparent Mode when the connection is completed the mode will not be changed. If you have typed part of a command line when the connection is completed, the mode change will not take place until you complete the command or cancel the line.

CONNECT call1 [VIA call2[,call3...,call9]] [CONVERS|TRANS]

parameters

call1 Call sign of TNC to be connected to.

call2 Optional call sign of TNC to be digipeated through. As many as eight digipeat addresses can be specified.

CONVERS Enter Converse Mode upon successful connect.

TRANS Enter Transparent Mode upon successful connect.

Each call sign can have an optional extension designator specified as -n immediately following the call sign, if AX25 is ON. The digipeat fields are specified in the order in which you want them to relay the packets to the destination.

This command does not change NOVRAM values, and it has immediate effect. It initiates a connect request to TNC call1 optionally through digipeaters, and if successful, will enter the specified data transfer mode. An error message is returned if the TNC is in a connected state, or is already attempting to connect or disconnect. If no response to the connect request occurs after RETRY attempts, the command is aborted, a message is typed, and the TNC remains in Command Mode.

If the optional mode parameter is not specified, the mode given in CONMODE in used. If the mode parameter is used, it overrides CONMODE.

If AX25 is ON, the VIA option is used to request digipeating. If AX25 is off, the status of the VRPT parameter controls digipeating. Digipeating by specific stations can not be specified in the VADCG protocol.

Example (AX25 is ON):

CONNECT WA7GXD VIA NØADI-1,WDØETZ CONVERS

This commands the TNC to initiate a connect request to WA7GXD, with the connect packets and subsequent data packets to be digipeated through NØADI-1 followed by

WDØETZ. Packets sent in the opposite direction access the digipeaters in the opposite order. Thus, packets from WA7GXD will first be repeated by WDØETZ, then by NØADI-1.

WARNING: Use of more than one digipeater is a TAPR extension of the AX.25 protocol and may not function properly with TNCs that do not support this enhancement.

CONOK ON|OFF default: ON *

parameters

ON Connect requests from other TNCs will be automatically acknowledged.

OFF Connect requests from other TNCs will not be automatically acknowledged.

This command determines the action taken by the TNC when a connect request for it is received though the radio. ON will result in the request being ACKed, the standard connect message will be output through the terminal, and the data transfer mode specified by CONMode will be entered.

OFF will cause the message "connect request: <call>" to be output to the terminal. The user may then enter Command Mode and issue his own connect command.

Example of OFF:

```
*** connect request: KD4NL

<ctl-C>

CONNECT KD4NL

*** CONNECTED TO KD4NL
```

In this example, the TNC is assumed to be in Converse Mode, but not currently connected to anyone. After receipt of the message, the user enters Command Mode and issues a connect command. If CONOK is ON, only the final connected message will appear.

Connect requests received from another station when the

TNC is already connected or when CONOK is OFF cause the TNC to issue a DM packet (busy-signal). The connect request message will not appear if the TNC is in Transparent Mode.

CONVERS

CONVERS has no options. It is an immediate command, and will cause exit from Command Mode into Converse Mode. Any link connections are not affected.

CPACTIME ON|OFF default: OFF *

parameters

ON PACTIME is used in Converse Mode.

OFF PACTIME is not used in Converse Mode.

When CPACTIME is ON, the PACTIME parameter is used in Converse Mode as well as in Transparent Mode. This mode is normally used when a computer is attached to the TNC on the other end of the link, but full Transparent Mode is not desired. In this mode, characters are sent periodically as in Transparent Mode, but the local editing and echoing features of Converse Mode are enabled.

CR should normally be off in this mode, since otherwise the SENDPAC character is appended at random intervals as the input is packetized by the timer.

CR ON|OFF default: ON *

parameters

ON The SENDPAC character, normally <cr>, is appended to all packets sent in Converse Mode.

OFF The SENDPAC character is not appended to packets.

When CR is ON, all packets sent in Converse Mode will include the SENDPAC character which forces the packet to be sent. Setting CR ON and SENDPAC $ØD results in a

natural conversation mode. Each line is sent when a <cr> is entered, and arrives at its destination with a <cr> at the end of the line. If AUTOLF is on at the other end, no overprinting occurs.

CWID ON|OFF default: ON *

parameters

ON The TNC will send an ID after 9.5 minutes if it sent a packet during the previous 9.5 minute interval.

OFF The TNC will send an ID only when the ID command is entered.

If ON is specified, the TNC will ID after a disconnect operation.

IDTEXT will be used for the ID if it has been entered; otherwise the callsign specified by MYCALL will be used.

DEBUG n default: $Ø5 <ctl-E> *

parameters

n Ø - $7F, specifying an ASCII character.

This command is used to change the debug program entry character. When that character is entered in Command or Converse Mode, the resident debugger is entered. The debugger is described in Appendix E.

DELETE ON|OFF default: ON *

parameters

ON The delete character input editing character is <delete> ($7F).

OFF The delete character input editing character is <backspace> ($Ø8).

This command is used to change the input editing command for character deletion.

DIGIPEAT ON|OFF default: ON *

parameters

ON The TNC will digipeat AX.25 packets if requested.

OFF The TNC will not digipeat AX.25 packets.

When this parameter is turned on, any packet received that has MYCALL in the digipeat list of its address field will be retransmitted. Each station included in the digipeat list relays the packet in the order specified, marking the packet so that it will not accidentally relay it twice (unless so requested), and so that the stations will relay the packet in the correct order. Only the digipeating of AX.25 packets is controlled by this value; VADCG digipeating is controlled by VDIGIPEA. Digipeating takes place concurrently with other TNC operations and does not interfere with normal operation of a packet station.

DISCONNE

This command will immediately initiate a disconnect request with the currently connected station. A successful disconnect results in the display of:

*** DISCONNECTED

Other commands may be entered while the disconnect is taking place, although connects are disallowed until the disconnect is completed.

If the RETRY count is exceeded while waiting for the other side to acknowledge, the TNC moves to the disconnected state. If a disconnect command is entered while the TNC is disconnecting, the retry count is immediately exceeded. In both cases, the disconnect message is preceded by "retry count exceeded".

Disconnect messages are not displayed when the TNC is in Transparent Mode.

DISPLAY [class]

parameters

class optional parameter-class identifier, one of the following: CHARACTE, ID, LINK, MONITOR, TERMINAL, TIMING.

Display will cause all control parameters and their current values to be displayed. Individual parameters can be displayed by entering the parameter name with no options. Groups of related parameters can be displayed by specifying the optional parameter-class.

DWAIT n default: 2 *

parameters

n Ø - 15, specifying 4Ø ms intervals.

This value is used to avoid collisions with digipeated packets. The TNC will wait DWAIT * 4Ø ms after last hearing data on the channel before it begins its own keyup sequence. This value should be agreed on by all members of a local area when digipeaters are used in the area. The best value will be determined by experimentation, but will be a function of the keyup time (TXDELAY) of the digipeater.

This feature is made available to help alleviate the drastic reduction of throughput that occurs on a channel when digipeated packets suffer collisions. It is necessary because digipeated packets are not retried by the digipeater, but must be restarted by the originating station. If all stations specify DWAIT, and the right value of DWAIT is chosen, the digipeater will capture the frequency every time it has data to send, since digipeated packets are sent without this delay.

ECHO ON|OFF default: ON *

parameters

ON Characters received from the terminal are echoed to the terminal by the TNC.

OFF Characters are not echoed.

ECHO controls local echoing by the TNC when it is in Command or Converse Mode. Local echoing is disabled in Transparent Mode.

ESCAPE ON|OFF default: OFF *

parameters

ON The <escape> character ($1B) is output as "$" ($24).

OFF The <escape> character is output as <escape> ($1B).

This command specifies the character which will be output when an <escape> character is to be sent to the terminal. The <escape> translation is disabled in Transparent Mode.

FLOW ON|OFF default: ON *

parameters

ON Input flow control is active.

OFF Input flow control is disabled.

When FLOW is on, any character entered from the terminal will halt output to the terminal until a packet is forced (in Converse Mode) or a line is completed (in Command Mode), PACLEN is exceeded, or the terminal output buffer fills up. Cancelling the current command or packet or typing the redisplay-line character will also cause output to resume. FLOW is ignored in Transparent Mode.

FLOW will keep received data from interfering with data entry. See also the comments on the CR command.

FRACK n default: 4 *

parameters

n 1 - 16, specifying 1 second intervals.

After transmitting a packet requiring acknowledgment,

the TNC will wait FRACK seconds before incrementing the retry counter and sending it again. If the retry count specified by RETRY is exceed, the current operation is aborted. If the packet address includes relay requests, the time between retries will be adjusted to

Retry interval = FRACK * (2*m + 1)

where m is the number of intermediate relay stations.

When the retried packet is sent, a random wait time is added to any other wait times in use. This is to avoid lockups where two TNCs repeatedly collide with each other.

FULLDUP ON|OFF default: OFF *

parameters

ON Full duplex mode is enabled.

OFF Full duplex mode is disabled.

When FULLDUP is OFF, the TNC makes use of the carrier-detect signal from the modem to avoid collisions, and acknowledges multiple packets with a single acknowledgment. When FULLDUP is ON, the TNC ignores the carrier-detect signal and acknowledges packets individually. The latter mode is useful only for full-duplex radio operation, such as through OSCAR 10.

HBAUD n default: 8 (1200 baud) *

parameters

n selects a baud rate from the list below.

This command specifies the baud rate used for radio packet communications. This value has no relationship to the terminal baud rate selected by ABAUD. In order to communicate with other packet stations, the radio baud rates must be the same. Note that this table does not correspond to the ABAUD rate table because of the addition of the special 400 baud rate.

Ø	Reserved for future use.		
50		600	
75		1200	
110		1800	*
135		2400	*
150		3600	*
300		4800	*
400			

* - These baud rates are not supported by the on-board Bell-202 compatible modem.

ID

ID will send the CW id the next time the frequency is clear. If there is an ID request already pending, this command is ignored. This command can be used to send a CW id even if the automatic ID feature is disabled.

IDTEXT will be used for the ID if it has been entered; otherwise the callsign specified by MYCALL will be used.

IDTEXT text

parameters

text - any combination of characters and spaces.

IDTEXT specifies the ID sent by the ID commands. A maximum of 128 characters can be specified. If the first character is set to & or %, the call sign set by MYCALL will be sent. See Appendix A for valid CW characters.

LCOK ON|OFF default: ON *

parameters

ON The terminal is capable of receiving lower case ASCII characters.

OFF The terminal is not capable of receiving lower case ASCII characters.

If LCOK is OFF, lower case characters will be translated to upper case before being output to the terminal. This case translation is disabled in Transparent Mode. Input characters and echoes are not case translated.

LFADD ON|OFF default: OFF *

parameters

ON A <lf> character is added to outgoing packets following each <cr> transmitted in the packet.

OFF No <lf> is added to outgoing packets.

This function is similar to AUTOLF, except that the <lf> characters are added to outgoing packets rather than to text displayed locally. This feature is included in order to maintain compatibility with other packet radio controllers. If the person you are talking to reports overprinting of packets from your station you should set LFADD ON. This character insertion is disabled in Transparent Mode.

MALL ON|OFF default: OFF *

parameters

ON Monitored packets include both "connected" packets and "unconnected" packets.

OFF Monitored packets include only "unconnected" packets.

This command determines the class of packets which are monitored. If MALL is OFF, only otherwise eligible packets (as determined by MTO and MFROM commands) sent by other TNCs in the unconnected mode are displayed. This is the normal manner of operation when this TNC is being used to talk to a group of TNCs all of which are unconnected.

If MALL is ON, all otherwise eligible frames are displayed, including those sent between two other connected TNCs. This mode may be used for diagnostic purposes or "reading the mail."

MAXFRAME n default: 4 *

parameters

n 1 - 7, signifying a number of packet frames.

MAXFRAME sets an upper limit on the number of unacknowledged frames which the TNC can have outstanding at any one time. This is also the maximum number of contiguous frames which can be sent during any given transmission. If some but not all of the outstanding frames are acknowledged, a smaller number may be transmitted the next time, or new frames may be included in the retransmission, so that the total unACKed does not exceed MAXFRAME.

MCON ON|OFF default: OFF *

parameters

ON Monitor mode remains active when TNC is connected.

OFF Monitor mode is off while the TNC is connected.

If MCON is ON, the TNC will observe the MONITOR command while the TNC is connected to another TNC. If MCON is OFF, the display of monitored packets is suspended when a connect occurs, and is resumed when the TNC is disconnected. If MCON is on and the station connected to is selected by MFROM or MTO, you would see only the packets displayed by the monitor function.

MFROM call1[,call2...,call10] default: NONE **

parameters

call Call sign list. Up to ten calls, separated by commas.

MFROM establishes a list of FROM call signs to monitor. If MONITOR is on, any packet heard which has as its FROM address any of the calls in the MFROM list will be displayed.

There are two special calls. If either is used, it

must be the only call in the list. ALL means display all packets heard regardless of their FROM address. NONE means do not display packets based on the contents of the FROM field. A packet is displayed if it appears in either the MTO list or the MFROM list. This means that if either list is ALL then all packets will be displayed.

** If anything other than NONE is specified, ALL will be stored in NOVRAM by the PERM command.

MONITOR ON|OFF default: ON *

parameters

ON Monitor mode is on.

OFF Monitor mode is off.

If monitor mode is on, and the TNC is not in Transparent Mode, packets not addressed to this TNC may be displayed. The addresses in the packet are displayed along with the data portion of the packet, e.g.:

KV7D>W5FD-3: I'm ready to transfer the file now.

The calls are separated by a ">" and the call sign extension field is displayed if it is other than Ø. The MALL, MTO, and MFROM commands determine which packets are to be monitored. The MCON command controls the action of monitor mode when the TNC is connected. All monitor functions are disabled in Transparent Mode. To completely enable monitor mode, a TO or FROM list must be specified using MTO or MFROM.

MTO call1[,call2...,call1Ø] default: ALL **

parameters

call Call sign list. Up to ten calls, separated by commas.

MTO establishes a list of TO call signs to monitor. If MONITOR is on, any packet heard which has as its TO address any of the calls in the MTO list will be displayed. Refer to the discussion under MFROM for

special calls and a discussion of the interaction of the MTO command with other monitor mode commands.

** If anything other than NONE is specified, ALL will be stored in NOVRAM by the PERM command.

MYCALL call[-n] no default *

parameters

call Call sign assigned to this TNC.

n Ø - 15, optionally specified call sign extension.

This command tells the TNC what its call sign is. This call sign will be placed in the FROM address field for all packets originated by it, and it will respond to frames with this call sign in the TO or digipeat fields as appropriate. MYCALL will be used for Morse ID unless another string is specified by IDTEXT.

The call sign in the default parameter list is blanks, and must be changed for proper operation of the protocols. The default for the extension is zero, and is not required to be changed.

MYVADR n default: 31 *

parameters

n Ø - 31, signifying a VADCG address byte.

This command selects the address used when the board is operating in the VADCG mode. The address is translated to the "to be digipeated" range by the VRPT command. Choice of VADCG addresses should be coordinated with other packet operators in your area to avoid duplicate addresses.

NUCR ON|OFF default: OFF *

parameters

ON Nulls are sent to the terminal following <cr> characters.

OFF Nulls are not sent to the terminal following <cr> characters.

This command enables a transmission delay following any <cr> sent to the terminal. The length of the delay is determined by the command NULLS. This delay is required by some hardcopy terminals.

NULF ON|OFF default: ON *

parameters

ON Nulls are sent to the terminal following <lf> characters.

OFF Nulls are not sent to the terminal following <lf> characters.

This command enables a transmission delay following any <lf> sent to the terminal. The length of the delay is determined by the command NULLS. This delay is required by some display terminals.

NULLS n default: Ø *

parameters

n Ø - 3Ø, the (even) number of nulls to send after <cr> or <lf>.

This command specifies the number of nulls to send to the terminal after a <cr> or <lf> is sent. In addition to setting this parameter value, NUCR and NULF must be set to indicate whether nulls are to be sent after <cr>, <lf>, or both. Devices requiring nulls after <cr> are typically hard-copy devices requiring time for carriage movement. Devices requiring nulls after <lf> are typically CRTs which scroll slowly. NULLS is valid only in Converse and Command Modes. If an odd number is specified, the number will be rounded down one.

PACLEN n default: 128 *

parameter

n 1 - 256, the maximum length of the data portion of a packet.

The TNC will automatically transmit a packet when the number of bytes input for a packet reaches PACLEN. This value is used in both Converse and Transparent Modes.

WARNING: Allowing more than 128 characters of data is a TAPR extension of both the AX.25 and VADCG protocols and may not function properly when used to communicate with TNCs that do not support this enhancement.

PACTIME [EVERY|AFTER] n default AFTER 4 *

parameters

n Ø - 15, specifies 1/4 second intervals.

EVERY Timeout occurs every n seconds.

AFTER Timeout occurs after n seconds with no other input.

This parameter is always used in Transparent Mode, and will also be used in Converse Mode if CPACTIME ON is specified. When EVERY is specified, input bytes are packaged and queued for transmission every n/4 seconds. When AFTER is specified, bytes are packaged when input from the terminal stops for n seconds. In neither case is a zero length packet produced, and the timer is not started until a new byte is entered. If EVERY or AFTER is not given, the current state is retained.

PARITY n default: 3 (space parity) *

parameters

n Ø - 4, selecting a parity option from the table below.

This command sets the parity mode for the terminal output according to the following table:

n	Parity
Ø	odd
1	even
2	mark
3	space
4	none

If PARITY choices Ø-3 are chosen together with AWLEN=8, only one stop bit is available and ABIT will be automatically set to 1. The parity bit is automatically stripped on input and not checked. If your terminal transmits 7 data bits and a parity bit, all 8 bits can be transmitted in Transparent Mode by setting AWLEN 8 and PARITY 4.

PASS n default $16 <ctl-V> *

parameter

n Ø - $7F, specifying an ASCII character.

This command selects the ASCII character used for the pass input editing command.

PERM

PERM is an immediate command. It causes any NOVRAM values changed since the last PERM command to be made permanent; all values are burned into the NOVRAM. As this process can not be undone by turning the TNC off, care should be taken to see that the correct values have been selected.

Each bit of the NOVRAM chip can be reversed by the burning procedure a minimum of 10,000 times. According to our information, giving the PERM command when no values have changed does not affect the life-expectancy of the chip.

PROGRAM

PROGRAM is an immediate command. It enters the EPROM Programmer routine. This routine supports the TAPR

EPROM Programmer attachment which allows programming of EPROMS through the parallel I/O port. This routine is documented with the EPROM Programmer kit. Should you accidentally enter this routine, type <cr> until you see the Option: prompt. Exit by typing R followed by a <cr>. Then, following the next prompt, enter any character.

REDISPLA n default: $12 <ctl-R> *

parameters

n Ø - $7F, specifying an ASCII character.

This command is used to change the redisplay-line input editing character.

RESET

This command is used to perform a soft reset. Any NOVRAM parameters changed but not PERMed are retained if Switch 1 is OFF. PERMed values are restored via a hardware reset (toggling DIP switch 3) or power off/on.

RETRY n default: 1Ø *

parameter

n Ø - 15, specifying number of packet retries.

The AX.25 and VADCG protocols allow for retries, i.e., retransmission of frames that are not acknowledged. Frames are re-transmitted RETRY times before the operation is aborted. The time between retries is specified by FRACK. A value of zero specifies an infinite number of retries. If the number of retries is exceeded, the TNC goes to the disconnected state (with an informative message if not in Transparent Mode). See also the FRACK command.

SCREENL n default: 80 *

parameters

n Ø - 255, specifying the screen or platen width of the terminal.

This value is used to properly format terminal output. A <cr> <lf> sequence is sent to the terminal at the end of a line in Command and Converse Modes when n characters have been printed. A value of zero inhibits this action.

SENDPAC n default: $ØD <cr> *

parameters

n Ø - $7F, specifying an ASCII character.

Selects the character that will force a packet to be sent when entered in Converse Mode.

START n default: $11 <ctl-Q> *

parameters

n Ø - $7F, specifying an ASCII character.

Selects the character used to restart output from the TNC to the terminal. Output is stopped with the STOP character.

STOP n default: $13 <ctl-S> *

parameters

n Ø - $7F, specifying an ASCII character.

Selects the character used to stop output from the TNC to the terminal. Output is restarted with the START character.

TRACE n default: $1000

parameters

n a 16 bit value.

TRACE is used to set protocol debugging functions. Each bit enables a different option; the bit values are described here. If the bit in the position indicated below is set, the option is enabled. The frame dump output is described in Appendix E.

2xxx Dump data as well as header.

1xxx Dump input and output frames in case of FRMR condition.

x8xx Dump all outgoing frames.

x4xx Dump incoming frames that look useful.

x2xx Show "before" and "after" states when the link state changes.

x1xx Dump all incoming frames.

xx8x Never send Final bit.

xx4x Dump any digipeated frames that don't get monitored.

xx2x Allow digipeated frames to get monitored.

All other bits are currently undefined.

TRANS

This command causes immediate exit from Command Mode into Transparent Mode. The current link state is not affected.

TXDELAY n default: 4 *

parameters

n 1 - 16, specifying 40 ms intervals.

This value tells the TNC how long to wait after keying up the transmitter before sending data. Some startup time is required by all transmitters to put a signal on the air; some need more, some need less. In general, crystal controlled rigs with diode antenna switching don't need much time, synthesized rigs need time for PLL lockup, and rigs with mechanical T/R relays will need time for physical movement. The correct value for a particular rig should be determined by experimentation. The proper setting of this value may also be effected by the requirements of the station you are communicating with. This parameter should be locally agreed upon.

TXFLOW ON|OFF default: OFF *

parameters

ON The XFLOW parameter is used in Transparent Mode.

OFF The XFLOW parameter is ignored in Transparent Mode.

When ON, XFLOW is used to determine the type of flow control used during Transparent Mode. When OFF, software flow control is not used, i.e., XFLOW is treated as OFF. If TXFLOW and XFLOW are both ON, the TNC will use the XON and XOFF characters to control input from the terminal. However, only hardware flow control is available to the terminal to control output from the TNC, and all input from it remains fully transparent.

UNPROTO call1 [VIA call2[,call3...,call9]] default: CQ

parameters

call1 Call sign to be placed in the TO address field.

call2 Optional digipeater call.

This command is used to set the RPT and TO address fields of packets sent in the unconnected (unprotocol) mode. Unconnected packets are sent as unsequenced I frames with TO and RPT fields taken from UNPROTO call1

through call9 options. A special call, NONE is interpreted as "no one in particular", i.e., CQ. When NONE is specified, unconnected packets are sent to CQ. These packets sent from other TNCs can be monitored by setting MONITOR ON and MTO CQ or MTO ALL. As in the case of the CONNECT command, up to eight digipeater calls may be specified. See also the BEACON command.

VDIGIPEA ON|OFF default: OFF *

parameters

ON This TNC is a VADCG digipeater.

OFF This TNC is not a VADCG digipeater.

When ON, VDIGIPEA causes this TNC to retransmit any VADCG frames it receives that have an address byte value in the digipeat range. Digipeating occurs concurrently with other operations of the TNC. Only one TNC in an area should be a VADCG repeater.

VRPT ON|OFF default: OFF *

parameters

ON VADCG address is translated into the digipeat range.

OFF VADCG address is not modified.

This parameter is used to request that packets originated by this TNC in the VADCG mode be digipeated. It has no effect on operation in AX.25 mode.

XFLOW ON|OFF default: ON *

parameters

ON XON/XOFF flow control enabled.

OFF XON/XOFF flow control disabled; hardware flow control is enabled.

When XFLOW is on, the device connected to the terminal port is assumed to respond to flow control characters

XON and XOFF. When XFLOW is off the TNC will only respond to hardware flow control (CTS) and will communicate flow control commands via RTS.

XMITOK ON|OFF default: ON *

parameters

ON Transmit functions are enabled.

OFF Transmit functions are disabled.

When XMITOK is off, transmitting is inhibited. All other functions of the board remain the same.

XOFF n default: $13 <ctl-S> *

parameters

n Ø to $7F, specifying an ASCII character.

This command selects the character sent by the TNC to the terminal to stop input from that device.

XON n default: $11 <ctl-Q> *

parameters

n Ø to $7F, specifying an ASCII character.

This command selects the character sent by the TNC to the terminal to restart input from that device.

Messages

The first message you should see on your screen after issuing a RESET command or performing a hardware reset is a sign-on message.

Tucson Amateur Packet Radio Corporation
TAPR/AMSAT AX.25 level 2 version X.Y

This message appears when you have performed a reset with DIP switch 1 set on. It indicates that the TNC has been initialized with the default parameters stored in ROM. X.Y refers to the software version number.

TAPR packet radio

This sign-on message appears when a reset has been performed with DIP switch 1 set off. It indicates that the TNC has been initialized with the parameter values stored in NOVRAM.

RAM size is nnnn

This message appears after the sign-on message and indicates that the RAM has been successfully verified and found to be the indicated (hex number) length in bytes. If you have an 8k RAM in the U7 socket, the RAM size should be 2000.

High RAM size is nnnn

This message is printed if you have a RAM chip in the U8 socket.

HDLC can't init

This is a hardware diagnostic that will appear at the time of reset if the HDLC (U17) chip can not be commanded. It indicates difficulties with either the WD-1933 chip or the 6522.

PIA can't init

This is a hardware diagnostic that will appear at the time of reset if the 6520 (U13) PIA chip can not be commanded.

UART can't init

This message doesn't ever appear -- if the UART can't init, there is nothing to type on. If you see LEDs D1 and D2 blinking every couple seconds when the TNC should be signing on, this indicates problems with the 6551 (U14) UART.

Messages displayed in response to Command Mode input are discussed below. The message as it appears on terminals with lower case displays is given at the left margin. Messages which are responses to input errors in commands from the user will try to point out the problem by typing a $ under that part of the line. If your input line is messy because of deleted characters echoed as \ or incoming packets, the pointer will not line up properly.

cmd:

This is the Command Mode's prompt for input. Any characters entered after the TNC prints "cmd:" will be used as command input and not packet data.

EH?

This is the TNC's generalized "I don't understand" message. A dollar sign ($) is used to point to the offending character. It will also appear if a required input item is missing, e.g.:

```
C KV7B V
        $
EH?
```

In this example, the required call sign after the VIA option is missing. Most commands that receive an EH? error are ignored. In a few cases, part of the command may be accepted and acted upon, as described under the message "Input ignored".

Value out of range

If the syntax of the command is legal, but the value specified is too large or too small for this command, the value out of range message is used. A $ is used to point to the bad value.

Input ignored

Since the command parser was kept small and simple, it will sometimes change parameters before it completes parsing some of the more involved commands. In some cases, options at the beginning of the command will have been acted on before a syntax error near the end of the line is reached. When this occurs, "Input ignored" is used to show what part of the line was ignored. The dollar sign points to the boundary: characters to the left were used; the character pointed to and those to the right were not, i.e., the line was parsed as if a <cr> was entered at the $. Example:

```
MTO QST,WB9FLW K9NG
               $
Input ignored
```

The command is parsed as if it were MTO QST,WB9FLW and the K9NG is ignored.

was

Whenever one of the NOVRAM values is changed, the previous value is displayed. Example:

```
AX25 OFF
was ON
```

Not while connected

The AX25 parameter can not be changed if the TNC is connected to another TNC. This message is printed if an attempt is made.

too many packets pending

You have given a CONVERS or TRANS command after exiting Transparent Mode but before all packets have been acknowledged. Wait until the TNC quits retrying packets then enter the command again.

Link state is:

This message is output in response to the CONNECT and DISCONNECT commands if the state of the link does not permit the requested action. It is prefaced by either "Can't CONNECT" or "Can't DISCONNECT" as appropriate.

A CONNECT command with no options will display the current link state. The states are:

DISCONNECTED

No connection exists. Connects are legal; disconnects are not.

CONNECT in progress

Connect request has been issued. Another connect is illegal; a disconnect will abort the attempt.

CONNECTED to <CALLSIGN>

The TNC is connected. Connects are illegal, disconnects start the disconnect process.

FRMR in progress

The TNC is connected but a protocol error has occurred. This should never happen when two TAPR TNCs are connected. An improper implementation of the AX.25 protocol could cause this state to be entered. The TNC will attempt to re-synchronize frame numbers with the TNC on the other end although a disconnect may result. Connects are not legal in this state, and a disconnect will start the disconnect process.

DISCONNECT in progress

A disconnect has already been issued. Connects are not legal in this state, and a second disconnect will cause a "retry count exceeded" condition.

CHAPTER V

HARDWARE

Overview

This discussion of the TNC hardware is intended to show the functional interrelationships of the major components of the TNC. Please refer to the schematic and block diagrams in Appendix A for the following discussion.

For the purpose of this discussion, the TNC will be considered as eight distinct subsystems. These are the Central Processing Unit (CPU), the system clock, serial Input-Output (I/O), memory (including Random Access Memory (RAM), Read Only Memory (ROM), and NOnVolatile Random Access Memory (NOVRAM)), the High level Data Link Controller (HDLC), the MOdulator/DEModulator (MODEM), the Versatile Interface Adapter (VIA), and finally, the parallel user port.

TNC Specifications

Processor 68Ø9

Clocks Processor master clock input frequency: 3.6864 MHz jumper selectable to 7.3728 MHz (requires higher speed parts). If you wish to use this option, contact TAPR for software alterations.

UART clock: 1.8432 MHz.

MF-1Ø switched-capacitor filter clock: 115.2 kHz.

System E and Q clocks: 921.6 kHz standard, 1.8432 MHz with high speed option.

Memory All memory in industry-standard Byte-Wide sockets.

Standard complement of ROM: 32k = 4 x 2764

Standard complement of RAM: 8k = 1 x 6264

One spare socket for use with ROM or RAM, jumper configurable for 2k to 16k parts.

Custom address decoder will permit use of 27128 EPROMS and almost any mix of EPROM and RAM, making full use of the 64k address space of the 6809 processor.

Serial Port

6551 UART plus 1488/1489 TTL <--> RS-232C signal interface chip set.

Baud rates supported: 50, 75, 110, 135, 300, 600, 1200, 2400, 3600, 4800, 7200, 9600 and 19200. Usage at 9600 and 19200 baud requires 7.3728 MHz master processor clock option if sustained operation at these speeds is desired.

Autobaud operation whenever DIP switch 1 is ON and the terminal is at 110, 300, 1200, 4800, or 9600 baud.

Standard female DB-25S (DCE) RS-232C connector.

Parallel Port

6520 parallel port serves 3 different functions:

I. TNC status and command channel. Gives link state information and accepts single bit commands for use by control computers.

II. Diagnostic signal port. Presents various signals to permit diagnosing problems and verifying operation.

III. PROM programmer control port.

Modem

Input demodulation filter: MF-10 switched-capacitor filter permits use with unmodified FM transceivers.

Demodulator: EXAR 2211 demodulator circuit plus related components to receive up to 1200 baud 1200 Hz/2200 Hz FSK modulation.

Typical dynamic range is nominally 51 db in the presence of typical FM noise.

Modulator: EXAR 2206 modulator circuit plus related components to produce up to 1200 baud 1200 Hz/2200 Hz coherent FSK.

All important passive modem components mounted on DIP headers for ease in substitution and modification.

Built-in modem calibration system requires no additional equipment.

An external modem may be attached via a single connector which completely bypasses the on-board modem.

Support for an external modem tuning indicator is provided via a separate connector.

NOVRAM Xicor XD2212

This electrically erasable, programmable memory is configured as two switch selectable banks of 64 bytes each, enabling two independent TNC configurations to be stored.

Protocols AX.25 and VADCG (Vancouver Amateur Digital Communications Group) with full duplex radio data link operation.

Operating Modes

Command mode: accepts commands via digital port.

Conversation mode: accepts digital data, transmits and receives packets, permits terminal editing features (character delete, line delete, input packet delete, output packet delete, redisplay input) and escape to command mode via special characters trapped by the TNC. Optional use of packet completion timer as in Transparent mode.

Transparent mode: accepts digital data, transmits packets via packet completion timer only, and receives packets. No local editing features permitted. Escape to command mode via specially timed character sequence.

Functional Description of TNC

CPU

The CPU is the heart of the TNC. It manages the operation of all the other system components. It does this by issuing commands on the control bus and addresses on the address bus so that it can send and receive data between itself and the various other system components via the data bus. It also retrieves its own instructions from ROM in the memory area in this manner. These instructions stored in ROM form the program which enables the TNC to implement the communications protocol and to communicate with the user through an I/O port.

The CPU consists of four main components. First, the "brains" of the outfit is U5, the 68Ø9 microprocessor. This is the device that executes the instructions of the program. Second, a 74S288 programmable ROM (PROM) (U4) is the address decoder. It generates chip select signals to enable the proper device on the data bus at the proper time. Third, logic gates U3 and U36 generate the proper read, write, and memory refresh timing signals. Finally, sections D and E of inverter U1 buffer the system "E" clock for the other users of this clock signal in the system.

System Clock

The system clock generates all the proper timing signals for the CPU and its peripheral devices (serial I/O, HDLC, MODEM). The system clock consists of U1A, B, and F which make up the clock oscillator, and U2 which divides the clock oscillator frequency to generate the various different frequencies required by the clock users mentioned above.

Serial Input/Output

The serial I/O section consists of U14, a 6551 Universal Asynchronous Receiver Transmitter (UART), along with U14 and U15, which are level translators. The UART receives data in parallel format from the CPU and sends it in serial format to the terminal (or computer) for communication with the user of the TNC. The UART also receives serial data from the terminal and sends it in parallel format to the CPU on the system data bus. The level translators convert the signals from the TTL levels used by the TNC logic circuitry to the +/-12 volt levels compatible with the RS-232C interface standard.

Memory

Memory is the component of the system in which instructions and data are stored. EPROM is used for program instructions and data which need not be changed during the operation of the system or lost when power is off.

RAM is used for data which will change, such as incoming and outgoing packets and system scratch variables. The TNC has a third type of memory, the NOVRAM. It is used for storage of infrequently changed parameters such as callsign, terminal baud rate, timing parameters, etc.

HDLC

The HDLC controller, U17, is a very important and complex device which performs several useful functions for the TNC. First, it helps format data received from the CPU into packets for transmission over the air. Second, it generates and checks the Frame Check Sequence (FCS) which is used for error detection. Third, it processes formatted packets received off the air to check for errors and ship the required data to the CPU for further processing under the communications protocol. This chip unloads a major portion of the work involved in implementing the lower layers of the protocol from the CPU, and thus allows the CPU to spend its time implementing the higher levels of the protocol.

Modem

The modem consists of two main parts. The MOdulator converts the serial binary data from the HDLC controller to two tones (normally 1200 & 2200 Hz) which are used to modulate the radio carrier when the packet is actually sent out over the air. The DEModulator converts the audio tones received off the air to serial binary data for the HDLC. The modulator consists of an EXAR 2206 frequency shift keyed signal generator (U19) and an output buffer U31A. The demodulator consists of U18, U20, U28, and U31B. U18 is an EXAR 2211 phase-locked-loop FSK demodulator. U20A and U28 filter and frequency compensate the received audio for use by U18. U20B provides a clean signal to the HDLC controller for use during calibration of the modem. U31B is used to drive the LEDs which indicate proper audio level for the demodulator.

VIA

The 6522 Versatile Interface Adapter (VIA) performs several different functions. First, it is used to communicate with

the NOVRAM. Second, the VIA provides a means by which the CPU can read the status of DIP switch 1. Third, it provides a programmable baud clock for the HDLC controller. Fourth, it is used to key the modem tones for the Morse code identification function. Finally, it is used to provide a system software real-time clock, used by the software in timing channel operations and other functions.

Parallel Port

Finally, we come to the parallel port. The parallel port (as you might suspect) passes parallel data between the CPU and external devices which require a parallel data format. It is currently used to interface the TAPR EPROM programmer to the TNC and to output diagnostic and informative signals from the TNC. The parallel port consists of U13, a 6520 Peripheral Interface Adapter (PIA).

NOTE: We recommend that you have the schematic diagram available for reference during the following discussions. We have used the convention of denoting a negative-true logic level by a trailing "*" (e.g., IRQ* means IRQ is true at logic 0). Further note that TTL logic levels are defined as greater than +2.4 volts but less than +5.25 volts for a "1" and less than +0.8 volts but greater than -0.4 volts for a "0".

Clock and System Timing

The clock and system timing section is concerned with the generation of signals needed for clocking the CPU and serial user port baud rate, generating the power-on reset signal for on-board Large Scale Integrated circuits (LSI), address decoding for chip selection by the CPU, and conversion of CPU control bus signals to formats required by the non-family peripheral devices (memory bank and HDLC controller). This circuitry is Shottky-TTL based.

The primary system clock is crystal-controlled at 7.3728 Mhz by Y1, using U1A and U1F (74LS04) biased into linear operation by R12 and R13. The clock is buffered by U1B and passed to divider U2 (74LS393), a dual 4-stage binary counter. U2 provides a 1.8432 MHz clock for UART U14 (6551), and 115.2 kHz for U28 (MF-10), the modem input filter. Two taps are provided to U5 (6809) via jumper JP7 for quadrature clock generation at the bus frequency. The standard tap

provides a clock of 3.6864 MHz, yielding a bus frequency of 921.6 kHz. The 7.3728 MHz tap provides a bus frequency of 1.8432 MHz when using optional high-speed components to support radio link data rates in excess of 4800 baud.

The power-on reset generator timing is controlled by RC network R8-C12. After power is applied, a delay of some 50 to 200 ms occurs before the Schmitt trigger (composed of U1C, U3A, R87 and R88) fires. The positive-going output from pin 3 of U3A (74LS00) and applied to the CPU (6809) as well as being distributed by the system control bus as RES* (reset).

A system clock on the control bus is provided by buffering the E phase output from U5 (6809) pin 34 through U1E and U1D (74LS14).

The HDLC controller and system memory require a separate RD* (read) and WR* (write) control signal not directly provided by the 6809. RD* is generated by NANDing R/W* and E from U5 (6809) through gate U36B (74LS10). Similarly, WR* is generated by NANDing E with R*/W (by inverting R/W* with U3D) via U36A (74LS10). The additional input to these RD* and WR* gates is tied to system RES* to drive RD* and WR* high during reset as may be required by certain RAMs used at U7 (see memory bank, below).

Address decoding is provided by U4 (74S288), a high-speed Shottky bipolar PROM. The memory map programmed into U4 is:

Address (HEX)		Line	Size	Function	Chips Selected
0000	1FFF	CS0*	8k	RAM	U7
2000	27FF	CS2*	2k	I/O	U6,U13,U14
2800	2FFF	CS3*	2k	HDLC	U17
4000	7FFF	CS1*	16k	RAM/ROM	U8
8000	9FFF	CS4*	8k	(EP)ROM	U9
A000	BFFF	CS5*	8k	(EP)ROM	U10
C000	DFFF	CS6*	8k	(EP)ROM	U11
E000	FFFF	CS7*	8k	(EP)ROM	U12

The system used is called incomplete address decoding, which means that an IC that requires only a few bytes of address space is allocated more than it needs, which makes it accessible at multiple addresses. The primary addresses for the various I/O chips (which are the addresses actually used in the software routines) are:

IC	Description	Address Range (Hex)
U6	6522 VIA	2Ø4Ø-2Ø4F
U13	652Ø PIA	2Ø2Ø-2Ø23
U14	6551 UART	2Ø1Ø-2Ø13
U17	1933 HDLC	28Ø0-28Ø7

In addition, certain logic has been provided for use with so-called "smart RAMs" which use dynamic RAM technology to achieve high density memory at lower cost than static RAM of comparable capacity. Specifically, many such memories require a pulse on pin 1 telling them when it is "safe" to perform an internal refresh operation without potential conflict with access requests by the CPU (below). This line is called refresh enable (REFEN*).

REFEN* is asserted (made true -- a logic "Ø" in this case) when RES* is false, CSØ* is false and the E clock is high. This only occurs when the TNC is not in a hardware reset cycle, the chip (U7) is not selected, and the microprocessor is far enough into its current cycle to ensure that no glitches will occur on the CSØ* line.

The REFEN* circuitry design was based on preliminary (limited) information and may not work with "smart RAMs" of any particular manufacturer.

Central Processing Unit

A 68Ø9 microprocessor acts as the system CPU. This part is readily available, multiply-sourced and widely accepted for application in dedicated-function controllers as well as general purpose low-end computing devices. It executes the logical algorithms located in system EPROM.

The processor has an internal 2-phase clock generator and provides control, address and data bus input/output for family peripheral devices. The control lines are modified for non-family devices as explained above.

The 68Ø9 has capabilities for position-independent code and is designed to support multiple stacks, making it very efficient for executing block structured high level languages (such as Pascal, which is used to implement the VADCG and AX.25 protocols running on your TNC). Information on the 68Ø9 instruction set and addressing modes is available from the manufacturers.

Serial User Port

The serial user port is designed to provide a full-duplex RS-232C interface for the user's terminal or personal computer. Full baud rate selection from 50 baud to 19.2 kilobaud is supported by the port. EIA RS-232C levels and transition rates are implemented as well. The on-board connector is a DB-25S to conform with the RS-232C physical interface connector recommendations for modems (or Data Communications Equipment - DCE).

The serial I/O port is controlled by U14 (6551), a complete LSI Universal Asynchronous Receiver-Transmitter (UART) with an internal, software-controlled baud-rate generator. The transmitter and receiver are doubly-buffered and capable of interrupt-driven operation. The 6551 supports hardware flow control via its CTS* (Clear To Send - for transmit) and RTS* (Request To Send - for receive) pins.

The TTL I/O levels of the 6551 are translated to and from RS-232C levels and sense by U15 (1488) and U16 (1489A) buffers. U15's output transition rate of change is limited by 330 pf capacitors (C9-C11) to ensure RS-232C compliance. D17 and D18 are incorporated to prevent power supply problems when abnormal conditions exist on the RS-232C lines.

All input lines to the TNC's RS-232C port are pulled to proper levels by 33k resistors (R1, R3 and R4). This sets the correct defaults to allow the TNC to work with so-called "2-wire" serial data ports or other RS-232C subset implementations on the attached terminal. R1 asserts CTS* so the TNC will send data to the terminal. R2 (6.8 k - also an output) asserts DCD* to allow the TNC to accept user data.

Parallel User Port

The parallel user port is designed as a general-purpose I/O port. It is currently used to output status information and debugging information (see Chapter V and Appendix E). In addition, it is designed to support accessories such as the TAPR EPROM programmer.

U13 (6520/6820/6821) is a 6809 family parallel interface chip that provides two TTL-level, 8-bit handshaking ports. It allows for easy interface to computers, ASCII keyboards, and other devices that may not support RS-232C serial I/O. Due to the lack of universal standards for such interfaces,

the I/O levels are left as TTL, and the user is required to make such level changes as may be necessary for his equipment.

System Port, Timers, Indicators and NOVRAM

The system port consists of a dedicated-function parallel I/O device which communicates with the NOVRAM used for system configuration, the DIP switches, the HDLC controller RESET, and the indicator driver interface for various LED-monitored system functions. It also provides timers for HDLC baud rate generation, software timing functions, Morse ID, and calibration of the modem frequencies. This section also includes the LED drivers.

The primary component in the system interface is U6 (6522), a very powerful 68Ø9 family LSI part. The 6522 incorporates a pair of 8-bit programmable I/O ports, four control lines (for handshaking or other use), two 16-bit programmable timer/counters and an 8-bit shift register.

The A port of the 6522 is used to read DIP switch S1 at PAØ during initial system configuration. The remaining lines are used for 4-bit data bus I/O to and from the NOVRAM XD2212 (U27), the high-order address line to U27 (A6 via PA1) and as the WE* (write) and STORE* (store) control lines to U27.

The 6522 B port is used to control the address bus (AØ through A5) of U27 and to provide output from Timer 1 (normally the HDLC baud rate generator) at PB6, and input to Timer 2 at PB7 during modem calibration.

The control lines are used as follows:

CA1	not used
CA2	HDLC reset and LED driver for modem calibration
CB1	not used
CB2	Morse code ID output and LED driver for modem calibration

U27 is a non-volatile RAM. It is used to store system parameters that are normally not changed such as call sign,

terminal attributes and timing parameters, but which remain user alterable. This allows configuration changes for a given session only, or on a "permanent" basis.

The NOVRAM is a special type of integrated circuit (IC) memory that has two modes, volatile (temporary) and non-volatile (permanent) storage.

In the volatile mode, NOVRAM looks to the TNC like any other kind of temporary data storage memory. If you turn the TNC off, then back on again, the data values stored in the volatile (RAM) portion of the NOVRAM chip are completely random.

In the non-volatile mode, NOVRAM still looks like ordinary RAM except that, when a power-off-then-on cycle occurs, the data in the NOVRAM is initialized from a form of permanent memory (called electrically erasable programmable read only memory -- EEPROM). This EEPROM, a sort of "shadow memory" for the RAM portion of the NOVRAM, is where you normally save your call sign and other parameters by setting the values, then performing a PERM command.

Note that, while the EEPROM portion of the NOVRAM chip is called "permanent" you can still change it as many times as you like (a minimum of 10,000 times) via the PERM command provided on the TNC.

The NOVRAM interface consists of the following:

WE* write-enable: store data to RAM function (volatile)

STORE* store: save data to ROM (non-volatile) 10,000 cycles minimum

CS* chip enable: always true

Q0-Q3 data bus: 4-bit parallel interface

A0-A6 address bus: 128 4-bit locations (512 bits)

ARRAY RECALL* overrides STORE* if Vcc drops below 3 volts: transfers contents of ROM to RAM (no cycle limit)

U21 (7406) consists of 6 inverters with open-collector NPN transistors capable of sinking sufficient current to drive the LED indicators.

HDLC and Modem

The WD1933B controller is used to implement the HDLC standard bit oriented protocol including CRC check sum and zero bit insertion. The modem consists of a phase-coherent AFSK modulator operating with 1200 and 2200 hz tones (for Bell 202 compatibility), a PLL demodulator, and the necessary analog circuitry for interface with most common Amateur communications equipment. A 60-second hardware "watchdog" timer is inserted in series with the transmitter activation line to prevent accidental RF channel lockout.

U17 (WD1933B) is an LSI HDLC protocol controller with onboard PLL for clock recovery from NRZI-encoded data. This chip is interfaced to the system data and address busses directly, and to the control bus signals described above. Note that all data is inverted on transfer to or from the CPU, and the address assignments likewise reflect the inverted status of the address bus. The DRQO, DRQI and INTRQ interrupts are inverted via open-collector inverters U25A, B and C (7406) for interface to the 6809 IRQ* input. U17 is wired for selection of NRZI format and PLL clock recovery.

The transmit control signal from the HDLC chip MISCOT* pin is fed through U25F and C13 to U26 (NE555), a monostable multivibrator with an approximate 1-minute time period established by R28 (on header U35) and C14. The output from U26 drives Q1 (VN10KM), a power field-effect transistor (VFET) which in turn keys the transmitter via J3 pin 4.

The modem is based on the EXAR 2206/2211 chip set for FSK operation. U19 (XR2206) provides phase-coherent low distortion sine wave output at 1200 hz and 2200 hz for a 1200-baud Bell 202 compatible modulator. The output signal is buffered by U31A (LM1458) and sent to the transceiver interface connector. R33 is used to adjust the AC output level of the tone from a few millivolts to a few volts peak-to-peak. R36 sets the 2200 Hz frequency and R38 the 1200 Hz. Input sources are selected by push-on, pull-off jumpers to allow HDLC data (JP4) or a fixed logic 1 or 0 (JP5). The fixed levels are for calibration of the modem tone frequencies.

Morse code ID is accomplished by use of FSK tones. When not transmitting, the tone output is nulled. R31 is adjusted to provide a null in the 2206 output when pin 11 of U25 (7406) is in the low, or non-transmitting state. This allows voice override without disconnecting the TNC from the transmitter's microphone input.

The demodulator is based on the EXAR 2211 PLL demodulator which provides both data and a lock-detect output. R43 is used to set the unlocked frequency of the device for best demodulation of the input frequencies used. To satisfy the requirement for input tone balance to the 2211, a switched-capacitor filter based on the MF-1Ø (U28) is provided. The passband of the filter is tailored for typical 2-meter FM radios. The filter characteristics are determined by a resistor network plugged into a 16-pin header (U3Ø) to allow other filter characteristics to be selected easily. The input level to the filter is monitored by LEDs D7 and D8 at the output of buffer U31B (LM1458).

Jumpers are provided to allow for calibration of the modem (JP1 through JP5) and for disconnecting the modem from the HDLC chip (via J5). This flexibility allows the use of external modems for specialized applications and experimenting, as well as using the on-board modem for other purposes. Finally, there is a modem tuning indicator connector (J7).

Memory Bank

The memory bank consists of six JEDEC-standard 28-pin "Byte-wide" sockets. One site (U7) is mapped for an 8k static RAM and four (U9-U12) are mapped as up to 16k EPROM or static RAM sites. The remaining site may be configured, via jumpers JP6 and JP8, to support a 2k through 16k EPROM, EEPROM or RAM. The configuration supports 2716, 2732, 2764 and 27128-type EPROM devices, but not 2532 or 2564-type EPROMs.

The JEDEC standard chosen (there are two) incorporates two-line control of the memory chip (CE* and OE*). This simplifies the controller timing circuitry and allows multiple-sourced memory chips to be used. The TNC is address-mapped for one 8k-by-8 static RAM chip, U7 (6264) to occupy low memory (ØØØØ-1FFF hex) and four 8k-by-8 chips (U9-12) to occupy high memory (8ØØØ through FFFF hex). U8 is mapped into the 16k space from 4ØØØ through 7FFF hex. A replacement of the address decoder (U4) will allow other memory mappings to be used with no board changes.

Power Supply

The power supply provides the needed voltages at the required current levels for proper TNC operation. Specifically, +12, -12 and -5 volts are supplied for the modem and

RS-232C circuitry, and +5 volts is sent to all circuitry. The primary 5 volt regulator (U24) IC is usually mounted on board on a heat sink. The custom power transformer is the only required off-board component on the TAPR TNC. The power supply consists of two subsections, the main +5 volt section, and the +/-12, -5 volt section.

The primary +5 volt section utilizes a bridge rectifier (D13-16) charging a large capacitor (C1). This raw DC is fed to regulator U24 (LM309K). The output of U24 is decoupled by numerous 0.1-uF capacitors and distributed across the board to the digital logic.

The +/-12, -5 volt section incorporates a full-wave bridge (D9-12) fed from a center-tapped winding. The split DC obtained is filtered (C3 for +12 and C5 for -12 volts) and fed to on-board regulators U22 (7812) for +12 and U23 (7912) for -12 volts. A portion of -12 volts output is further regulated to -5 volts by U29 (79L05), while a portion of +12 volts is further regulated by U33 (78L05) to +5 for the modem section only. The resulting outputs are decoupled and distributed.

A user wire-wrap area is provided, primarily for custom interface to the user's transceiver or for level shifting the parallel interface from TTL levels if required. For convenience, the wire-wrap area has power supply busses provided for +5, +12, -12 volts and ground.

Jumper Placement

Several jumper pins are present on the board. Some of these jumper pins are used during the modem calibration procedure. Others are provided for flexibility in satisfying individual configuration requirements. Jumper placement for normal operation is discussed below.

JP1 This jumper (normally on) is removed to unlock the XR2211 VCO (U18) in order to allow measurement of its free-running center frequency during modem calibration.

JP2 This jumper (normally off) is connected to allow the on-board counter to measure the tone frequencies generated by the XR2206 modulator (U19) during modem calibration.

JP3 This jumper (normally off) is connected to allow the on-board counter to measure the voltage-controlled

oscillator (VCO) frequency of the XR2211 phase-locked loop (PLL) tone decoder (U18) during modem calibration.

JP4 This jumper (normally on) is connected to allow the on-board modem to accept data from the HDLC controller chip (U17). It is removed during modem calibration.

JP5 This jumper (normally off) is used during the modem calibration procedure to force a low (jumper on) or high (jumper off) control signal to the modulator during the modem calibration.

JP6 This jumper allows socket U8 (normally empty) to accept a 2k byte RAM chip (jumper to the left) or to accept an 8k byte RAM chip (jumper to the right).

JP7 This jumper selects the system clock speed. In the right position (normal) it selects a clock input to the microprocessor of 3.6864 MHz and a system clock speed of 921.6 kHz, which allows the TNC to operate with standard 1 MHz microprocessor and peripherals and slower access memories. In the left position, it selects a clock input to the processor of 7.3728 MHz and a system clock speed of 1.8432 kHz. In order to operate at this speed, the TNC requires high speed parts and will support higher baud rates.

JP8 This jumper allows socket U8 (normally empty) to accept a 2k byte to an 8k byte memory IC (jumper to the left) or a 16k byte memory IC (jumper to the right).

In addition to the numbered jumpers listed above there are eight jumpers normally installed in the modem disconnect, J5 (just to the right of U17). (The second and eighth pin pairs, counting from the top of the connector, are unoccupied.) These jumpers are necessary when using the on-board modem for packet communications and are removed to connect an off-board modem. One may also remove these jumpers to experiment with the on-board modem for other uses. If you want to use an off-board modem, remove these jumpers and use J5 to connect a cable to your external modem. A detailed description of this connector is given later in this chapter.

DIP Switches

The functions of the DIP switches are detailed below. The ON position is that closest to the right edge of the board.

Note that the function of switch 1 is defined by software and may be changed in future software releases.

All commands referred to in this section are explained in detail in Chapter IV of this manual.

Switch 1 [DEFAULT SELECT] (normally OFF)

This switch is read by the system software at the time a reset is performed.

ON instructs the software to initialize the operating parameters from the defaults stored in the system EPROM. This switch must be in the ON position when the TNC is first turned on until you have stored your operating parameters in NOVRAM. See the PERM command in Chapter IV. This switch also activates the autobaud routine (see Chapter III).

OFF instructs the software to initialize the operating parameters from the values stored in NOVRAM. Following a "soft" reset (via the RESET command), the values read will be the volatile, non-permanent ones. Following a "hard" reset (using the reset switch) the values may be the volatile, non-permanent ones, or the non-volatile values, depending on the setting of Switch 4. The non-volatile memory will be read following a power off/on cycle.

Switch 2 [NOVRAM BANK] (normally ON)

This switch is used to choose one of two sets of NOVRAM-based parameters. This allows you to select from two different sets of operating parameters allowing rapid reconfiguration for different applications such as ragchewing, mailbox systems, or two users.

Switch 3 [RESET] (normally OFF)

This is the system reset switch. To perform a "hard" reset, toggle this switch ON, then OFF. It must be in the OFF position for the TNC to operate.

Switch 4 [NOVRAM DISCONNECT] (normally ON)

This switch is used to disconnect the NOVRAM from the system reset line when performing a hardware reset via Switch 3. This feature may be used when you have a

special set of parameters stored in NOVRAM that you wish to retain during a reset operation but have not saved via the PERM command. This switch is normally left in the ON position, and the TNC probably will not power up properly if it is left in the OFF position.

Terminal Interfacing

The TAPR TNC has two data ports for connection to the station terminal or computer: a serial port and a parallel port. The serial port is the primary terminal I/O port and is selected as the I/O port by the default operating parameters. In this mode, diagnostic and link information is provided on the parallel port. The parallel port may also be used to control the TAPR EPROM programmer, and may be used for terminal I/O at a future date.

Serial Port Operation

The Electronic Industries Association (EIA) RS-232C Standard defines a widely used serial data interface. A subset of the RS-232C standard was used to design the TAPR TNC serial port.

The serial port is interfaced via J2, a DB-25S style connector wired as Data Communications Equipment (DCE). (This is the same configuration a modem would have.) This allows connection of a standard terminal to the TNC through a pin-for-pin cable interface. For packet stations using a standard RS-232C compatible terminal, such a pin-for-pin cable (utilizing pins 1 through 8 and 20) is all that will be required for attaching to the TNC.

Unfortunately, not all manufacturers provide standard RS-232C interfaces for serial I/O ports. The following information may be useful if you have a non-standard device.

According to the standard, a logical "0", "spacing" or "on" state exists when a voltage between +3 volts and +25 volts is present on a line. Similarly, a logical "1", "marking" or "off" state exists when a line has a voltage between -25 volts and -3 volts. An important point to note here is that some so-called RS-232C interfaces use standard TTL logic levels (0 and +5 volts) to interface to a serial port. If your attached terminal or computer uses any voltage between +3 volts and -3 volts as an output level on its serial port, it may not work with the TAPR TNC!

RS-232C defines 20 signal lines on the 25-pin connector. The TAPR TNC, like most other devices using the RS-232C interface scheme, implements only a subset of these signal lines. Specifically, the following lines are used:

	Terminal (DTE)		TNC/Modem (DCE)
Description	Pin	Direction	Pin
Protective Ground	1	= = =	1
Transmit Data	2	= = =>	2
Receive Data	3	<= = =	3
Request To Send	4	= = =>	4
Clear To Send	5	<= = =	5
Data Set Ready	6	<= = =	6
Signal Ground	7	= = =	7
Data Carrier Detect	8	<= = =>	8
Data Terminal Ready	20	= = =>	20

The function of these lines is explained below.

Protective Ground: This line should be electrically bonded to the machine or interface frame. It may also be tied to signal common.

Signal Ground: This line establishes the common ground reference potential for all circuits except Protective Ground.

Transmit Data (TXD): This line is the serially transmitted data from the terminal to the TNC. It is held at the marking level (less than -3 volts) by the terminal when no data is being sent.

Receive Data (RXD): This line is the serially transmitted data from the TNC to the terminal. It is held at the marking level (less than -3 volts) by the TNC when no data is being sent.

Clear To Send (CTS): This line tells the terminal whether or not it may send data to the TNC. An "on" level tells the terminal it may send data while an "off" level tells it to stop sending data.

Request To Send (RTS): This line tells the TNC that the terminal is ready to send data.

Note that when the attached terminal or computer tells the TNC to stop sending data via the RTS line, the hardware

buffer in the UART (6551, U14) may still send as many as two characters.

Data Set Ready (DSR): This line from the TNC indicates that the TNC is ready or available for use. It does not mean that a connection has been established as defined by RS-232C. The current TNC software simply asserts this line during initialization and holds it in the "on" state.

Data Terminal Ready (DTR): This line tells the TNC that the attached terminal or computer is ready or available. The current TNC software ignores the state of this line.

Data Carrier Detect (DCD): This line is held high by a pull-up resistor on the TNC, and any DCD signal generated by the terminal is ignored. Thus, this line can function as an output to a terminal requiring a DCD input signal.

RTS/CTS Handshaking

The RTS/CTS and DTR/DSR lines are used for hardware "handshaking" protocol to control the flow of data between the terminal (DTE) and the TNC (DCE). The terminal indicates that it is on line by asserting DTR to the TNC, then waits for the TNC to indicate that it is on line by asserting DSR to the terminal. Note that the current TNC software always asserts DSR and ignores the state of DTR.

The TNC implements a hardware "handshaking" protocol with the attached DTE via the Request To Send/Clear To Send (RTS/CTS) pair for transferring data between the TNC and the terminal. The TNC will assert CTS when it is ready to accept data from the terminal, and negate (make false) this line when it is not ready to accept data from the attached DTE. Thus, if you find the TNC won't send data to your terminal, one of the first things to do is to verify that the RTS line at pin 4 of J2 is not being held low. If the software flow control option is disabled, the TNC will not send data to the DTE unless RTS is asserted. If the terminal does not implement the RTS/CTS protocol, the RTS/CTS lines (pins 4 and 5 on J2) should remain unconnected.

Many simple serial I/O ports do not implement RTS/CTS or DSR/DTR handshaking. If these pins are not connected at the terminal end, they will be pulled up (and thus asserted) by resistors on the TNC end. However a non-standard serial connector may use some pins for other purposes, such as supplying power to a peripheral device, so be sure that your

system either implements the RTS/CTS and DSR/DTR handshake or has no connections to these pins of J2 whatsoever. Note that reference to RS-232C "compatibility" or the presence of a DB-25 type connector does not guarantee that you have a full RS-232C serial port!

The TAPR TNC supports all standard baud rates from 50 through 19,200. However, it is recommended that the terminal port be configured to run at a data rate of 4800 baud or less if attached to a computer. In addition, the port supports all standard parity options (odd, even, mark, space and none) as well as 7- or 8-bit character lengths. (These terminal parameters are discussed in the operations chapter.)

If you want to interface your TNC with a device configured as DCE (such as a telephone modem), a so-called "null modem" cable may be constructed to interchange the data and handshake signals. Basically, a null modem cable is an otherwise pin-for-pin RS-232C cable that has pins 2&3, 4&5, and 6&20 interchanged on one end. See for example Byte, February, 1981 page 198.

Modem Calibration

The TAPR TNC contains a versatile, user configurable FSK modem that is suitable for both 1200 baud "Bell 202" packet use as well as lower data rate use, such as 200 Hz shift 300 baud use on hf links. These directions will be for the default 1200 baud mode.

Calibration of the modem is usually only required when the unit is initially put into operation or when you want to change the modem characteristics. You may want to check the calibration if you find you have difficulty connecting to other packet stations, but recalibration is normally not necessary.

After powering up your TNC and getting the sign-on message, type the command CALIBRA. The following display will appear on your terminal.

TAPR TNC Calibration and Checkout

```
Menu:
   1  Set low tone (1200 hz) - 1st pot
   2  Set high tone (2200 hz) - 2nd pot
   3  Set demodulator (1700 hz) - 5th pot
   4  Set audio level - 4th pot
      and tone null - 3rd pot
   5  ROM checksum
   6  End calibration and reset

cal:
```

The prompt "cal:" means the TNC is in the calibration mode and is awaiting your command. To select any menu item, enter the number of the item in response to the prompt, followed by a carriage return. The TNC will then display a message describing the jumper configuration and adjustments to be made. The trimpots are counted from left to right with the TNC in normal orientation. For example, to calibrate the 1200 Hz tone:

```
cal:1<cr>
```

The following display will appear.

```
   1 Set low tone (1200 hz) - 1st pot
Remove jumpers JP1,JP3,JP4
Place jumpers JP2,JP5
Adjust R38
Type any key to continue
```

Using a small screwdriver, adjust the indicated pot. Be patient, as these trimmers are 20-turn units and you may have to turn the adjustment screw several times.

The LEDs D2 - CWID and D1 - RESET are your frequency setting indicators. D2, when lighted, indicates the frequency is too low and the trimmer needs to be adjusted counterclockwise, while D1, when lighted, indicates the frequency is too high and the trimmer needs to be adjusted clockwise. When both LEDs are on, or flickering about equally, the setting is just right.

When you are satisfied with the LED indications, press a key on your terminal. The calibration routine will display something like:

```
Error is 0001 counts

cal:
```

An error count no worse than 2 or 3 is satisfactory. If you are unable to get this close to the desired count you may want to verify the frequency with a standard (external) frequency counter. If you still have trouble, consult the troubleshooting section of this manual before proceeding.

The calibration for the 2200 Hz and 1700 Hz tones is similar to that for the 1200 Hz tone. Simply respond to the "cal:" prompt with "2<cr>" for 2200 Hz or "3<cr>" for the 1700 Hz tone calibration.

At this point, all the modem frequencies are calibrated. The next part of the modem calibration procedure involves setting the transmitter (microphone) drive level and no-tone null for best operation with your radio.

Respond to the "cal:" prompt with "4<cr>".

Your terminal will then display the following:

```
      4 Set audio level - 4th pot
        and tone null - 3rd pot
Remove jumpers JP2,JP3,JP5
Place jumpers JP1,JP4
Adjust R31 and R33
Type 'Q' to quit, 'K' to key/unkey transmitter,
any other key to switch tone.
```

Set the jumpers as instructed.

In the following step you will set the mic drive level to your transmitter. The voltage at TP1, located above U31, will be about 30 times the voltage appearing at the mic output from the TNC. If you don't have a manufacturer's recommended drive level, use a value of 20 mV. This corresponds to 0.6 volts p-p or 0.2 volts RMS at TP1.

Now adjust trimpot R31 (center trimpot) 20 turns clockwise.

Monitor test point TP1 with an oscilloscope or ac voltmeter. Strike the "K" (or "k") key on your terminal until LED D4 (PTT) is on, then adjust R33 (4th trimpot from the left) clockwise to increase level, counterclockwise to reduce

level, for an amplitude of about Ø.6 volts peak-to-peak or Ø.2 volts RMS at TP1 (or 3Ø times the recommended drive for your transmitter). The waveform should be a clean sine wave in appearance.

Now strike the "K" key again so that LED D4 (PTT) is off. Adjust R31 (center trimpot) (starting counter-clockwise) for a null (minimum level) in the mic drive output. The null will be fairly sharp and very deep. After you have set the null, strike the "K" key again a few times and note that the audio drive switches between the preset level and the null. Verify that the "PTT" LED (D4) glows when the tone is present and is off when the tone is nulled.

Now strike the "Q" (or "q") key.

This completes the modem calibration. The next step will verify that the EPROMs are correct before exiting the calibration software.

Type:

```
cal:5<cr>
```

The screen on your terminal will then display the following:

```
   5 ROM checksum
8ØØØ nnnn
AØØØ nnnn
CØØØ nnnn
EØØØ nnnn
```

The software determines these checksum values by adding all the bytes in each EPROM according to a certain industry standard algorithm. The software release notes (if any) accompanying your software may also contain a list of these checksums for verification that your EPROMs are valid.

The value "nnnn" above simply means a 4-digit hexadecimal number. A hexadecimal numeral can be any value of Ø-9 or A, B, C, D, E or F. The use of 'nnnn' should not be taken to mean that the checksums for all four EPROMs are the same; this is very unlikely!

After verifying that the checksums are correct, strike any key, then type:

```
cal:6<cr>
```

The TNC will respond with:

```
6  End calibration and reset

Remove jumpers JP2,JP3,JP5
Place jumpers JP1,JP4
Type any key to continue
```

Set the jumpers as instructed. Your TNC is now ready for operation! The next keystroke will return you to command mode.

Calibration for Non-Default Tones

The TNC calibration system also allows you to set tones other than the defaults in case you wish to experiment. A typical example might be to set up for 170 Hz shift for HF use at 300 baud and slower. Of course, you may want to modify the passband characteristics of the MF-10 modem filter to optimize performance at a shift different from the default. It may also be necessary to alter the values of some of the timing and filtering components in the modulator, demodulator and/or watchdog timer to use other data rates successfully. It is for these reasons that many components of the modem are mounted on DIP headers U30, U34, and U35 instead of simply being soldered to the TNC PC board.

The calibration system measures a tone's frequency by counting the number of system clock cycles which occur in two oscillations of the waveform being measured. Since the system clock runs at 921.6 kHz, it "ticks" once each 1.085 usec. This count is compared with a 16-bit counter value, and the software then commands the LEDs to indicate whether the period just measured was too long or too short. The error-count message is the residual count "overflow" on the last period measured. Thus, an error of "0001 counts" means the measured period was 1.085 usecs too long, corresponding to a frequency about 3/4 Hz low in the case of the 1200 Hz tone.

In order to calibrate the modem to a different tone pair, you must first calculate twice the period of each frequency to be measured. The period is, of course, the reciprocal of the frequency (1/f). For example, a frequency of 2975 Hz,

widely used in HF AFSK work, has a period of 1/2975 = 336.1 usec. Our measuring interval is twice this, or 2 * 336.1 = 672.2 usec.

The next step is to determine how many CPU clock cycles occur in the measuring interval. With a CPU clock frequency of 921.6 kHz, each clock cycle is 1.085 usec long. Again using 2975 Hz as the reference (which we will continue to use throughout this discussion), there are 672.2/1.085 = 619.5 clock cycles in the measurement interval. Since the counter used can only work in terms of integers, we round this result to the nearest integer, giving a value of 620.

How do you use this count information? When you enter the CALIBRAte command and then select option 2, you must enter the new count value you want the software to use. In our example, the value 620 (you may drop leading "0"s if you wish) is entered as follows:

```
cal:2/620
```

The "/" tells the software to look for the new number you are supplying in place of its normal one.

In a similar manner, values for the modulator low tone and the free-running VCO frequency of the XR2211 demodulator may be calculated. Note that the 2211's VCO frequency should be the average of the high and low tones for best operation (the default 1700 Hz is midway between 1200 Hz and 2200 Hz).

If the TNC won't calibrate to your new tones, but calibrates to its defaults properly, it probably means that you must change a component value on the DIP header (U34 or U35) associated with the adjustment you are trying to make. To reduce the frequency limits of an adjustment, increase the value of the resistor in series with the trimpot you are adjusting, or lower the value to increase the frequency limits. Note that 1% temperature stable resistors are used along with (expensive!) zero drift capacitors in your TNC modem. Don't use typical 5% carbon resistors or other than "COG" (NPO) coefficient capacitors in the modem frequency determining components or you may experience some very annoying calibration drift.

TNC Modem Disconnect

The TAPR TNC design includes provision to completely bypass the on-board modem. This allows the TNC to be used with

higher-speed or special purpose modems, experimentation with modem techniques and so forth. The following information is primarily for those who wish to interface external modems to the TNC. Familiarity with modem and serial data channel terms is assumed.

20-pin connector J5 is available for disconnecting the on-board modem and allows connection of an external modem at TTL interface levels. A TTL high is greater than 2.4 volts but less than 5.25 volts while a TTL low level is greater than -0.4 volts but less than 0.8 volts. DO NOT connect an RS-232C level modem directly to J5!

Normally, jumpers are installed to connect pins 1-2, 5-6, 7-8, 9-10, 11-12, 13-14, 17-18 and 19-20. If the on-board modem is to be used, all of these jumpers must be installed.

The connector pinouts will now be described.

Pin 1 Carrier Detect In

This pin tells the HDLC controller (U17) that a valid data carrier has been detected. It should be pulled high when no carrier is detected and low when a carrier is present. This line must be implemented to use the software in the TNC unless the software release notes indicate otherwise.

Pin 2 Carrier Detect Out

This pin is an output from the on-board modem and meets the requirements outlined for pin 1, above. It is normally jumpered to pin 1 when the on-board modem is used.

Pin 3 CD1*

This pin is normally tied to ground via pull-down resistor R76, and tells the HDLC controller to interrupt the microprocessor (uP) when a negative-going edge is applied to Carrier Detect In, pin 1. Tying this pin high will disable this edge. This pin will normally be left unconnected.

Pin 4 CD0*

This pin is normally tied to ground via pull-down resistor R77 and tells the HDLC controller to interrupt the uP when a positive-going edge is applied to Carrier Detect

In, pin 1. Tying this pin high will disable this edge. This pin will normally be left unconnected.

Pin 5 MSCOT*

This line is an output from the HDLC controller. Unless otherwise indicated by the software release notes, it is used to key the attached transmitter and must be connected for proper operation of the radio link. This pin is high when the transmitter is commanded off and low when the transmitter is to be keyed.

Pin 6 Xmtr Key In

This pin is an input to the on-board modem and conforms to the specifications outlined above for pin 5. The on-board modem features a hardware "watchdog" timer to protect the packet channel from a runaway TNC that always tries to key the transmitter. The time constant is about 1 minute, but can be changed by selection of R28 on header U35.

Pin 7 DSR*

This pin is an input to the HDLC controller and is used to tell the TNC that the attached modem is ready for operation. The on-board modem has no initialization time and simply returns this line to pin 8, described below. This pin must be satisfied for the TNC to operate properly, unless the software release notes indicate otherwise.

Pin 8 DTR*

This pin is an output from the HDLC controller to the modem telling it that the HDLC port is ready for operation. If the modem has no use for this line, it should be returned to pin 7 as mentioned above.

Pin 9 RTS*

This pin is an output from the HDLC controller and tells the attached modem that the HDLC port has data to send. It is used as a handshake with CTS*, pin 1Ø (below) to synchronize the sending of data from the TNC when the modem requires it. The line will be high when the HDLC controller has nothing to send to the modem and low when it has data.

Some external modems may use this line; the on-board

modem does not and simply returns it to pin 10.

Pin 10 CTS*

This pin is an input to the HDLC controller and tells it that the modem is ready to accept data. It must be connected to allow proper operation of the HDLC controller, unless the software release notes indicate otherwise. The pin must be high to indicate the modem is not ready for data, and low to indicate the modem is ready to accept data.

The on-board modem simply returns this line to pin 9, above.

Pin 11 TC*

This line is the transmitter clock. While various duty cycle widths are acceptable, a square-wave clock is preferred. If the DPLL is enabled (pin 15, below), the clock frequency must be at 32x the data rate (38.4 kHz for 1200 baud); otherwise the clock must be a 1x the data rate (1200 Hz for 1200 baud).

Pin 12 Clock Out

This line is tied to the on-board HDLC clock generator. It runs at 32x the data rate for the HDLC port and provides a square-wave signal under software rate control.

Pin 13 RC*

This pin is an input to the HDLC controller of the receive data rate clock. The same restrictions apply to this pin as apply to pin 11, above.

Pin 14 Clock Out

This pin is physically tied to pin 12, described above.

Pin 15 32X*

This pin is an input to the HDLC controller and is used to select the on-chip digital phase-locked loop (DPLL) for data clock recovery. When the pin is held low, the DPLL is selected and the supplied TC* and RC* clocks must be at 32x the desired data rate. Pull-down resistor R79 is provided to set the default value of this pin to

enable the DPLL. When this line is high, the supplied clocks must be at 1x the desired data rate AND the receive clock must be synchronous with and in a certain time relationship to the received data. See the Western Digital data sheet listed in the Bibliography for details.

Pin 16 NRZI*

This pin is an input to the HDLC controller and tells it to format its output, and decode its input data stream, as NRZI (non-return to zero, inverted) when low and as NRZ when high. Normal packet radio usage to date has used the NRZI format for data as standard and pull-down resistor R78 is provided to configure the default state of this pin.

Pin 17 RXD

This pin is the received data input to the HDLC controller from the modem.

Pin 18 Receive Data Out

This pin provides receive data from the on-board modem to the HDLC controller.

Pin 19 TXD and MISCIN*

The TXD pin is the HDLC controller's transmitted data output to the modem. The format will be NRZ or NRZI depending on the state of that control line (see pin 16, above).

The MISCIN* input pin is used in conjunction with the on-board modem and control logic on the TNC to ensure the FSK ID is "right side up" when sent.

Pin 20 Tx Data Input

This input line accepts data to be transmitted by the modem.

In addition to the modem disconnect, three other lines are made available to the user from the HDLC controller. These lines are RI*, RI1* and RI0* and are normally disabled by pullup resistors R73, R74 and R75 respectively.

These lines are used to program interrupt response by the HDLC controller to a "ringing" signal supplied by an external modem. Since these lines are not needed in a radio application, they have been disabled and the software ignores them. For further details, refer to the manufacturer's data sheet for the HDLC controller.

If you elect to use an off-board modem, be sure to properly shield the connecting cables, etc., as the TNC may be susceptible to RFI.

Tuning Indicator Interface

In order to facilitate communications on HF and OSCAR, the TAPR TNC includes a connector for attaching a tuning indicator. The attached unit may range from an oscilloscope to a specialized LED-style unit. Please refer to the Exar Application Note referenced in the Bibliography for details on functions of the XR2211 signals available on this connector.

The connector pinouts will now be described.

Pin 1 Ground

This pin is the TNC's analog ground reference. It should not be used to sink appreciable current or the modem weak signal performance may be compromised.

Pin 2 Loop Data Filter Output

This pin is connected to the output of the XR2211 PLL data filter. It is a high-impedance source, and care should be exercised to ensure that no extraneous signals or low-impedance loads are attached.

Pin 3 Demodulator Reference Voltage

The internal XR2211 data comparator reference voltage is available on this pin. By comparing this value with the signal on pin 2, correct tuning may be accomplished. As above, this pin must be carefully shielded from noise, and has a high internal impedance.

Pin 4 Data Carrier Detect

This pin is an open-collector output that goes near ground when valid data is not present.

Pin 5 +12 Volts

This pin is a source of +12 volts dc. It should not be used to source more than a few milliamperes of current or degradation of the on-board modem's weak-signal performance may result.

CHAPTER VI

TROUBLESHOOTING

Troubleshooting Hints

General

WARNING: Never remove or insert an IC with power on!

The TAPR TNC is a complex piece of electronic equipment. Servicing must be approached in a logical manner. While it is not possible to present all possible problems, symptoms and probable cures, this section of the manual will give direction to troubleshooting based on our experience.

In most cases we have found that careful visual inspection combined with simple measurements generally reveals the problem. The most useful single instrument for troubleshooting is a good DVM that can read AC and DC volts, and can non-destructively test resistance while the ICs are still in their sockets.

While a number of checks may be made without the aid of an oscilloscope, you will need one to check signals at various points on the board if you fail to locate the problem by visual means or with a meter. Be very careful about shorting pins on ICs when applying meter or scope probes to the board. It is a good idea to attach a secure ground lead to the meter or scope, one that won't accidentally short across components on the board.

The first thing to check in any malfunction is the power supply. First check the power supply levels on the wire wrap area at the upper left. Are they close to their nominal values? Do all the ICs in the suspected area have the proper voltage on their power pins? Is there excessive ripple in any of the DC voltage lines? If so, check the regulator and associated components, working backwards toward the transformer. If the voltage is low, in conjunction with a hot regulator, suspect a short circuit on the board. (The large +5 volt regulator normally runs quite warm, but should not be near or above the boiling point of water. The voltage from it should be within about 300 mV of 5 volts.)

Step two is to note any unusual physical symptoms. Have you installed any ICs the wrong way? This is almost guaranteed to ruin the IC and produce a high current through it, detectable by the IC's high temperature. Are any components discolored? Does something smell like a burning TNC? Do any of the parts seems excessively warm? If you have never had your fingers on operating digital integrated circuits before you may erroneously conclude they are too hot when they are actually operating normally. This is especially true of the larger integrated circuits. In general their normal temperature will be well below the boiling point of water, but you may not want to keep your finger on them very long.

The third step is to carefully inspect the pc board and component installation. Are any cold solder joints present? (See Assembly soldering instructions in Appendix F.) Is a mounting screw shorting to the board anywhere? Are all ICs firmly seated in their sockets? Are any IC leads tucked under the chip or otherwise bent in such a manner that they aren't making proper contact with the IC socket? (This is a VERY common error!)

The fourth step is to inspect the diodes and electrolytic capacitors for proper installation. Are the diode cathodes pointing the correct way? Are the negative ends of the electrolytic capacitors pointing the correct way?

The fifth step is to inspect the interconnection cabling. Does it work on another TNC? Has the radio and/or terminal been successfully used on packet with this or another TNC? Are all the connections tight? Has the cable frayed or broken?

While these steps may seem obvious, careful inspection often will point to the problem or give significant clues as to the probable area of the TNC most suspect.

After the above inspection has been completed and apparent problems dealt with, it is time to proceed to more specific analysis.

Symptom: TNC appears dead

If the TNC powers up with D1 and D6 lit, followed by D2 lighting a second or so later, the processor is working and the software is probably working correctly. You should suspect the RS-232C port at this point. Check all connections

and verify the logic levels according to the RS-232C troubleshooting guide in this Chapter. If you see D1 and D2 blinking every second or so, this is a message indicating that the processor is having trouble initializing U14, the UART.

If no LEDs wink during the reset cycle the problem may be more serious. Check to see that the crystal oscillator is working and that a clock signal is coming from the uP, U5. The crystal oscillator input to the processor is pin 38, and the clock output from the processor is pin 34. Both of these should show (possibly distorted) square wave signals, with that at pin 34 running four times slower than the one at pin 38. Verify that the clock input at pin 38 of U5 is running at the correct frequency.

Remember that all the logic circuits operate at standard TTL levels, and all digital inputs and outputs switch between these two levels. Thus, if you see logic signals switching between Ø and 1 volt, say, you can be sure there is a problem (usually a short). On the other hand, do not mistake switching transients on digital logic lines for improper operation -- these show up as ringing and other distortions.

Next, verify that there is activity on the control bus READ and WRITE lines, the 8 CHIP-SELECT lines, and the IRQ line. Each of these lines should show activity, and if any is quiet this is a sign of trouble.

Logic lines that show no activity may often be traced to a short on the pc board, probably due to a solder splash or bridge.

Address and/or data line shorts may also show up as lack of activity on the control bus lines, especially the chip selects. Check each of the 16 address and 8 data lines for activity. Any lines showing a lack of activity are not operating properly.

If you suspect problems with address or data lines, try removing all the memory chips. Each address and data line will now show a distinct pattern. The address lines should be (possibly distorted) square waves whose periods increase by a factor of two on successive lines as you step line by line from AØ to A15.

If you decide to use an ohmmeter to check for shorted lines, use a low voltage/low current test instrument. If in doubt,

remove any ICs connected to the lines you are measuring. If you suspect a short, check the high density areas of the pc board for the problem. In most cases it will be found there.

It is very unlikely that the pc board itself will have a short as every board shipped by TAPR has been electrically tested for shorts and opens on a commercial "bed-of-nails" board tester prior to acceptance by TAPR.

Symptom: Modem won't calibrate or key transmitter.

Double check the placement of parts on each DIP header assembly, measuring resistor values and checking for shorted capacitors.

Calibration of the demodulator's 1700 Hz tone and the modulator's 1200 Hz and 2200 Hz tones is done in software by setting the specified device to generate the frequency in question and routing the signal to pins 16 and 17 of U6 where it is counted by the 6522. The calibration routine then examines this count and sets the LEDs appropriately. This signal also goes to U17, pins 26 and 31, where it should also be observable. In the case of the 1700 Hz tone, it is first passed through U20, acting as a Schmitt trigger. The input signal to the Schmitt trigger is a sawtooth wave. In the case of the modulator, the signal presented to U6 comes directly from pin 11, and should be a reasonable square wave.

Troubleshooting improper calibration amounts to checking for proper signals at U6 and following up any improper signal. If the calibration signal is present, but you cannot reduce the error count to zero, you may have an out-of-spec frequency determining component. Check the values of the appropriate passive components. Also, check the placement of jumpers! As a last resort, check the signal frequency with a frequency counter.

If the transmitter doesn't key and the PTT LED illuminates, the problem may be in the watchdog timer, U26. Check especially for an open timing capacitor C14 or a bad solder connection associated with R28 in header U35.

Symptom: Uncopyable transmitted or received packets

If no one seems to be able to decode your packet transmissions, it is often the case that your transmitter is being

overdriven. The solution is to reduce the drive level via trimpot R33.

If you manually key the transmitter and a strong tone is heard, your tone null isn't properly set and should be adjusted via R31 for a minimum background or residual tone level.

If you are having problems hearing other stations, the demodulator circuitry associated with U18 may be at fault. Check the center frequency of the VCO in U18 via the 1700 Hz calibration procedure. Working in the direction of flow of the input signal, verify that it is being passed through to pin 2 of U18, the input pin. The signal there should be above 100 mV and below 3 V for proper operation of the demodulator. It should be relatively clean, although a few millivolts of noise is normal.

RS-232C Troubleshooting

If you can't get the TNC to sign on and accept data from your terminal or computer, the problem may be in the RS-232C interface. The troubleshooting guide below is provided as an aid to help in resolving problems that may be related to the RS-232C port.

Symptom: TNC won't sign on to the terminal.

If you find the TNC won't send data to your terminal, one of the first things to do is to verify that the RTS line at pin 4 of J2 is not being held low. If the software flow control option is disabled, the TNC will not send data to the DTE unless its RTS is asserted. If the terminal does not implement the RTS/CTS protocol, the RTS/CTS lines (pins 4 and 5 on J2) should remain unconnected.

Verify that the voltages on the TNC are correct. If the TNC is in otherwise good condition, check the following pins on the UART, U14 (6551). Pin 9 should be between 0 and +0.8 volts. If this is incorrect, check U16 pin 4 and verify that the voltage is greater than +3 volts. If this is correct, U16 or the traces around it may be bad. If this is not the problem, disconnect the terminal and check it again. If this doesn't help, U16 or R1 may be at fault.

If the above checks are ok, observe pin 10 of U14 with an oscilloscope and cycle the reset switch (switch 3) on the TNC. Transitions on this pin shortly after reset indicate

that the TNC is sending data. Verify that transitions are also present on U15 pin 3. If these tests fail, the fault could be with U15, C11, U14, J2, the attached cable or faulty soldering (shorts, cold joints, etc.)

Symptom: The TNC appears to be signing on but only gibberish is printed on the terminal.

This indicates that some combination of the data rate (baud), parity option, or number of start and stop bits are not set the same at the TNC and terminal ends of the RS-232C cable. If possible, set your terminal to one of 110, 300, 1200, 4800, or 9600 baud. These are the rates automatically detected by the autobaud routine on the board. Also verify that the terminal is set for seven data bits, space parity, and 1 stop bit. These are the default settings stored in EPROM. Make sure that DIP switch 1 is set ON (to the right), and perform a hard reset by toggling DIP switch 3 ON then OFF (right, then left). Wait until the gibberish stops, then enter up to 3 or 4 asterisks (*). If the TNC prints a readable message during this process, do a PERM to save the serial port parameters.

If the TNC still prints gibberish, set the terminal to 300 baud and do a power off then on cycle on both the TNC and terminal. The sign on message defaults to 300 baud when using switch 1 in the ON position, so this should fix it. If the message still fails to appear try troubleshooting with an oscilloscope, looking first at the TXD pin of U14 (6551).

Symptom: The TNC signs on OK but won't accept commands.

After the TNC signs on, try giving it a command such as MYCALL or any other command. If the default settings are in effect, it will attempt to "echo" each character you type back to the screen. If it doesn't echo, be sure that U14 pin 16 has a voltage level between 0 and +0.8 volts on it. The voltage on U16 pin 10 should be greater than +3 volts. If these voltages aren't correct, the fault could be in U16, U14, J2, R2, soldering or the interconnecting cable.

If the above checks are OK, use an oscilloscope to verify that data is present on U14 pin 12 and U16 pin 1 when you strike a key. If not, the data isn't getting from your terminal to the TNC. Check J2, the cable and U16 again. Finally, be sure that your terminal actually uses levels less than -3 volts and greater than +3 volts for signal levels. 0 and +5 volts may not work!

Diagnostic and Status Signals

During normal operation, the parallel user port (J1) is used to output diagnostic and status signals. The diagnostic signals may be of use during troubleshooting. The status signals are provided for use by bulletin board programs or other computer-based applications. This function is automatically disabled if the EPROM Programmer routine is entered.

The diagnostic signals represent interrupts to the microprocessor from its various peripheral chips. The signals are updated as the processor actually services the interrupts rather than as they are generated by the hardware. The signal in each case is toggled from low to high or high to low upon servicing the interrupt. Thus, for example, if you look at the timer interrupt signal with an oscilloscope, you should see a square wave with a period of 20 ms, since the timer interrupt is generated at 10 ms intervals.

The diagnostic signals present are:

J1 Pin Number	Designation	Interrupt Signal
19	PB0	UART (terminal) input
6	PB1	UART (terminal) output
20	PB2	6522 timer
7	PB3	1933 carrier-detect change
12	PB4	1933 XEOM with error
11	PB5	1933 XEOM without error
10	PB6	1933 REOM with error
9	PB7	1933 REOM without error

The five WD-1933 interrupt signals correspond to interrupts on pin 6 of the WD-1933. XEOM means "transmitted end of message". The only error possible for a transmitted message is an underrun condition which could occur if the data rate is too high for the processor or if many other interrupt requests are present. The XEOM-with-error condition should never occur during proper operation of the TNC. REOM means "received end of message". The errors possible for received messages are: overrun, which, like the underrun condition, should never occur; aborted or invalid frame, which is frequently present if there is random noise on the channel; and frame-check sequence error, which occurs if a genuine packet is received in error.

There are two interrupt signals from the WD-1933 which are not represented in the list above. These signals are DRQI

(data request for input) and DRQO (data request for output). These signals are individually measurable at pins 18 and 19 respectively of the WD-1933

The status signals are provided for possible use by a computer attached to the TNC. The signals provided are shown below.

J1 Pin Number	Designation	Status Signal
15	PAØ	\
2	PA1	Link state
16	PA2	/
4	PA5	Operating mode
18	PA6	Active packet buffer
5	PA7	Carrier Detect

The link state is represented by a number from 1 to 5 in binary appearing on pins 15, 2, and 16. The possible link states are:

Pin 16	Pin 2	Pin 15	Link State
Ø	Ø	1	Disconnected
Ø	1	Ø	Connect attempt in progress
Ø	1	1	FRMR condition detected
1	Ø	Ø	Disconnect in progress
1	Ø	1	Connected

The significance of these link states is described in Chapter VII and appendices B and C. Note that you may regard pin 16 as a connect indication, similar to the modem carrier-detect that a telephone bulletin-board program might use.

Pin 4 indicates that the TNC is in Command Mode (1) or data mode (Ø). Pin 18 indicates that the radio transmit buffer is active, meaning that not all packets which have been generated have been transmitted. Pin 5 is a copy of the state of the audio carrier detect, which is also indicated by LED D5.

CHAPTER VII

PROTOCOLS

Explanation of Protocol

The material in this chapter is intended to supply an overview of the protocols used to transmit data by the TAPR software. References are given to more detailed information required by those wishing to implement these protocols on other hardware. The material presented below is somewhat tutorial in nature for those who have not had previous exposure to layered network protocols, but it presumes some knowledge of general communications hardware and software. Persons already well versed in networking will want to skip this chapter and refer to Appendix B, AX.25 Protocol Specifications and Appendix C, VADCG Protocol Specifications.

The TAPR TNC hardware and software architecture is organized in accordance with the International Standards Organization (ISO) layered network model. The model describes seven levels and is officially known as the ISO Reference Model of Open Systems Interconnection, or simply the ISO Model. The model and many other interesting topics are discussed in Computer Networks by Andrew S. Tanenbaum.

The ISO model provides for layered processes, each supplying a set of services to a higher level process. The TAPR TNC currently implements the first two layers, the Physical layer and the Data Link layer.

Physical Layer

The duty of the Physical Layer, layer one, is to provide for the transmission and reception of data at the bit level. It is concerned only with how each bit is physically transmitted, i.e., voltages on a hardwire line or modem tones on phone or RF links.

The physical layer of the TAPR TNC is described in Chapter V. It is compatible with the VADCG TNC. The actual modem interface is compatible with the Bell 202 standard which is similar to the CCITT V.23 standard. Any other hardware device which is compatible with the Bell 202 standard will be

compatible with the TAPR TNC, at least at level one of the ISO reference model.

Data Link Layer

The duty of the Data Link layer is to supply an error free stream of data to higher levels. Since level one simply passes any bits received to level two and is unaware of the content or overlying structure of the data, transmission errors are not detectable at level one. Level two carries the responsibility of detecting and rejecting bad data, retransmitting rejected data, and detecting the reception of duplicate data.

Level two accomplishes this task by partitioning data to be transferred by level one into individual frames, each with its own error detection field and frame identification fields. The TAPR TNC supports two level-two layers, the VADCG and AX.25 protocols. Each of these protocols is based on HDLC, the High Level Data Link Control protocol defined by the ISO.

HDLC Frames

Exact knowledge of the format of HDLC frames has been made largely unnecessary by the advent of LSI and VLSI communications chips which interface directly with the level one hardware. The level two software need only supply data to fill in various fields and the chip takes care of the rest. For completeness however, an HDLC frame looks like this:

| FLAG | ADDRESS | CONTROL | DATA | FCS | FLAG |

FLAG — A unique bit sequence used to detect frame boundaries. A technique called "bit stuffing" is used to keep data from looking like a flag.

ADDRESS — A field normally specifying the destination address. The VADCG protocol uses a one byte destination address; AX.25 uses 14 or 21 bytes containing the actual call signs of the source, destination, and optionally a digipeater.

CONTROL — A byte which identifies the frame type. In both the VADCG and AX.25 protocols, the

control field may include frame numbers in one or two 3-bit fields.

DATA — This field contains the actual information to be transferred. This field need not be present. Most frames used for link control only do not have data fields.

FCS — Frame Check Sequence, a 16-bit error detection field.

The communications chip recognizes the opening and closing flags and passes the address, control, and data fields to the software. The FCS field is a Frame Check Sequence computed by the transmitting chip and sent with the frame. The receiving chip recomputes the FCS based on the data received and rejects any frames where the received FCS does not match the computed FCS. This satisfies the level two task of bad data detection.

The communications chip used on the TAPR TNC is a Western Digital 1933 running in NRZI mode. NRZI mode is a way of encoding bits so that a logic level transition is guaranteed to occur at least every 5 bit times. This allows two chips to synchronize clocks when data is transmitted, and is required when sending bit stream data asynchronously as is done by the TAPR TNC. Other chips which are compatible with the 1933 are the Intel 8273 (used on the VADCG TNC) and the Zilog 8530.

The HDLC format supplied by the communications chip is common between the VADCG and AX.25 protocols. There are several other Layer Two concerns that are not handled by the chip. These items are duplicate frame detection, connection and disconnection of the level two layers on different TNCs, and buffer overrun avoidance. AX.25 and VADCG solve these problems in similar ways. The AX.25 protocol will be discussed first, and then the areas in which VADCG differs will be presented.

AX.25 Level Two

AX.25 is based on the Balanced Link Access Procedure (LAPB) of the CCITT X.25 standard. LAPB in turn conforms to the HDLC standard. Two extensions are made to LAPB in AX.25. These are the extended address field, and the unnumbered information (UI) frame. In LAPB, addresses are limited to

eight bits, while AX.25 uses either 112 or 168 bits, containing the originator's call sign, the destination call sign and an optional digipeater (simplex digital repeater) call sign.

The UI frame is used to send information bypassing the normal flow control and acknowledgment protocol. This data is unacknowledgeable but can be transmitted by layer two at any time without fear of disturbing higher layers. It is used by the TAPR TNC for beacon frames and for sending information frames when the TNC is not connected to another TNC, i.e., CQ and QST activities.

The exact specifications for AX.25 are supplied in Appendix B, AX.25 Protocol Specification. The TAPR implementation makes three deviations from that specification. These deviations are detailed below.

The DM Frame

The DM frame is sent whenever a non-SABM frame is received when the TNC is in the disconnected state. TAPR has expanded this definition. A TAPR TNC will send a DM frame in the following additional cases:

1) When an SABM frame is received and the TNC is in the disconnect state and the CONOK flag is off.

2) When the TNC is connected and an SABM frame is received from a third TNC. The DM is sent to the third user.

If a DM frame is received by a TNC in response to an SABM frame sent by it, that TNC will print

*** <call> busy

The Address Field

The address field has a single digipeater call sign slot specified. TAPR has extended the address field to allow up to eight digipeater call signs. Only as many digipeater subfields as needed are sent. Only the final byte of the final digipeater subfield has its "E" bit set. The meaning of the "H" bit is extended from "This frame has been repeated" to "The frame has been repeated by this digipeater". Thus, when a frame is received, the digipeater list is scanned beginning with the

subfield closest to the start of the frame, looking for the first digipeater address with H set to Ø. If that subfield is the current TNC, the frame is repeated, first setting the H bit in that subfield to 1. If all digipeater "H" bits are on, then the frame has been completely repeated and the destination address can be searched. The destination TNC will reverse the digipeater list when sending packets in the other direction.

If the VIA option of the CONNECT command is limited to one call by the user, the TAPR TNC will generate address fields in compliance with the current specification.

The Poll/Final Bit

The handling of the Poll/Final bit is an area of controversy. X.25, from which AX.25 was taken defines the uses of the P/F bit. The AX.25 environment is not quite the same, and the P/F bit becomes harder to pin down. In fact, some Amateurs have pointed out the need for a second bit in addition to the P/F bit to make error recovery work in all cases. The TAPR TNC code does not use the P/F bit to perform its error recovery; all packet retries are based on the timers, RR, and REJ frames already defined by AX.25. The TAPR TNC will never generate a frame with the P/F bit set. For compatibility with other software which may be using the P/F bit as specified, the TAPR TNC will generate an RR frame with the final bit set in response to a frame with the poll bit set.

All of the above items are invisible to the TAPR TNC user and are mentioned only for the benefit of those who may be writing software for other TNCs.

The following paragraphs will list the frame types used by AX.25 and describe their purpose. The material is intended for persons desiring a general idea of what transpires on an AX.25 link. Implementers are again referred to Appendix B. The control field contents are given as they appear in memory after data is received, i.e., the high order bit is at the left and the low order bit is at the right. Some texts choose to list the bits in the order in which they are transmitted, which is low order bit first. The AX.25 specification document reproduced in Appendix B uses this format. The data is presented in the "as in memory" form here because that is how it appears in the trace dump format enabled by the TRACE command.

The control bytes listed below are presented in hex with the "x" character used to signify four bits which may be any value, depending on what ACK functions the packet is performing. Usually "x" is a frame number. Frame numbers fit in three bits and are used to ensure that frames are received in order and that no frames are missed. Since only three bits are available, the frame number is counted modulo 8. This is why the MAXFRAME parameter has a ceiling of 7: no more than seven frames can be "in flight" (transmitted but unacknowledged) at one time. A short description of the use of the frames is given after the table.

x1	RR - Receive Ready
x5	RNR - Receive Not Ready
x9	REJ - Reject
Ø3	UI - Unnumbered Information
ØF	DM - Disconnected mode
2F	SABM - Connect request
43	DISC - Disconnect request
63	UA - Unnumbered Acknowledge
87	FRMR - Frame reject
even	I - Any frame ending in an even number (including A, C, and E) is an information frame.

I This and UI frames are the only frame types containing user data. The control byte contains this frame's number and the number of the next frame expected to be received from the other end of the link.

RR Usually used to acknowledge receipt of an I frame. The RR function can also be performed by sending an I frame with an updated "expected next frame number" field.

RNR Used when the buffer space on the receiving side is full.

REJ Used to request retransmission of frames starting from "x". Missed frames are detected by receiving a frame number larger than that expected.

DM Sent in response to any frame received other than a connect request (SABM) when the TNC is disconnected. Sent in response to an SABM whenever the TNC is on the air but can't connect to the requesting user, e.g., if the TNC is already connected to someone else or if CONOK is off.

SABM Set Asynchronous Balanced Mode - initiates a connect.

DISC Initiates a disconnect.

UA Sent to acknowledge receipt of an SABM or DISC.

FRMR Sent when an abnormal condition occurs, i.e., the control byte received is undefined or not proper protocol at the time received.

UI An I frame without a frame number. It is not acknowledged.

VADCG Level Two

The VADCG level two protocol is also based on HDLC and is therefore similar to AX.25. VADCG is not based on LAPB however, so many procedures are different, most notably in the connect and disconnect sequences. VADCG does not define a REJ frame type. VADCG frame types and control bytes are listed below in the same format as for AX.25 above. The detailed VADCG protocol description appears in Appendix C.

x1	RR - Receive Ready
x5	RNR - Receive Not Ready
Ø3	UI - Unnumbered information
17	Connect request
Ø7	Connect Acknowledge
53	Disconnect Request
43	Disconnect Acknowledge
even	I - Any frame ending in an even number (including A, C, and E) is an information frame.

Frame use is as in the AX.25 protocol except that connect and disconnect request frames have the P/F (bit 4) set, and are acknowledged by the same control byte with the P/F bit turned off.

Channel Use and Timing Functions

The following discussions mention timing parameters which are set by various commands. The time values selected are discussed in Chapter III.

An important part of any packet radio protocol is the means

by which many stations make efficient use of an RF channel, achieving maximum throughput with minimum interference. The basis for this time domain multiplexing is Carrier-Sensed Multiple Access (CSMA) with collision detection and collision avoidance.

CSMA means simply that (as every Amateur knows) no station will transmit if the frequency is in use. The TNC continually monitors for the presence of an audio data carrier on the frequency and transmits only if there is no carrier. (Notice that the RF carrier is not detected.) In order to make detection of a busy channel more reliable, the TNC sends an audio signal (continuous flags) any time the transmitter is keyed and a packet is not being sent, as during the transmitter keyup delay (TXDELAY), or while a slow audio repeater is being keyed (AXDELAY).

By itself, CSMA is not enough to insure a minimum, or even low, interference rate, due to the likelihood of simultaneous keyup by two or more stations. This is where collision detection and collision avoidance come in. The TNC detects a collision by the absence of an ACK from the station it is sending to. The receiving station does not acknowledge the frame that suffered the collision, since either the FCS was incorrect or the packet was not heard. There are other possible reasons for non-receipt of the packet, but the TNC's response is based on the assumption of a collision.

After transmitting a packet, the TNC waits a "reasonable" length of time (frame acknowledge -- FRACK) for an acknowledgment. "Reasonable" is determined by the link activity, packet length, whether the packet is being digipeated, and other time-related factors. If no ACK is received, the packet must be re-sent. If the unACKed frame was lost because of a collision, the presumption is that there is at least one other packet station out there that also lost a frame and will probably have exactly the same criterion for deciding when to retry the transmission as this station is using.

In order to avoid a second collision, the collision avoidance protocol calls for the stations retrying transmissions to wait a random time interval after hearing the frequency become clear before they key their transmitters. There must be enough different random wait times to provide a reasonable chance of two or more stations selecting different values. In addition, the difference between adjacent time

values must be similar to the keyup time delay of typical stations on the frequency. This is the time lapse after a station keys its transmitter before other stations detect its presence on the channel, and is a function of the keying circuitry of the transmitter and the signal detection circuitry of the receiver. We have chosen the random time to be a multiple (0-15) of the transmitting station's keyup delay (TXDELAY). This is reasonable if one's own keyup delay is similar to that of other stations on the channel.

One other factor must be taken into consideration in optimizing data throughput. The currently implemented link protocols provide for relaying (digipeating) of packets. The acknowledgment procedure for such packets is that the relay station simply repeats packets without acknowledgment to the sending station. The receiving station sends its ACK back through the same digipeaters to the originating station. Since the digipeated packets are not acknowledged to the digipeater, an unsuccessful transmission must be retried from scratch by the originating station. In order to help alleviate the congestion of the frequency that tends to result when digipeated packets suffer collisions, the digipeater is given first shot at the frequency every time it becomes clear. Other stations, instead of transmitting as soon as they hear the channel clear, must wait a short time (DWAIT). This restriction applies to all stations except the digipeater, which is permitted to transmit relayed packets immediately. This prevents digipeated packets from suffering collisions except on transmission by the originating station.

APPENDIX A

ILLUSTRATIONS

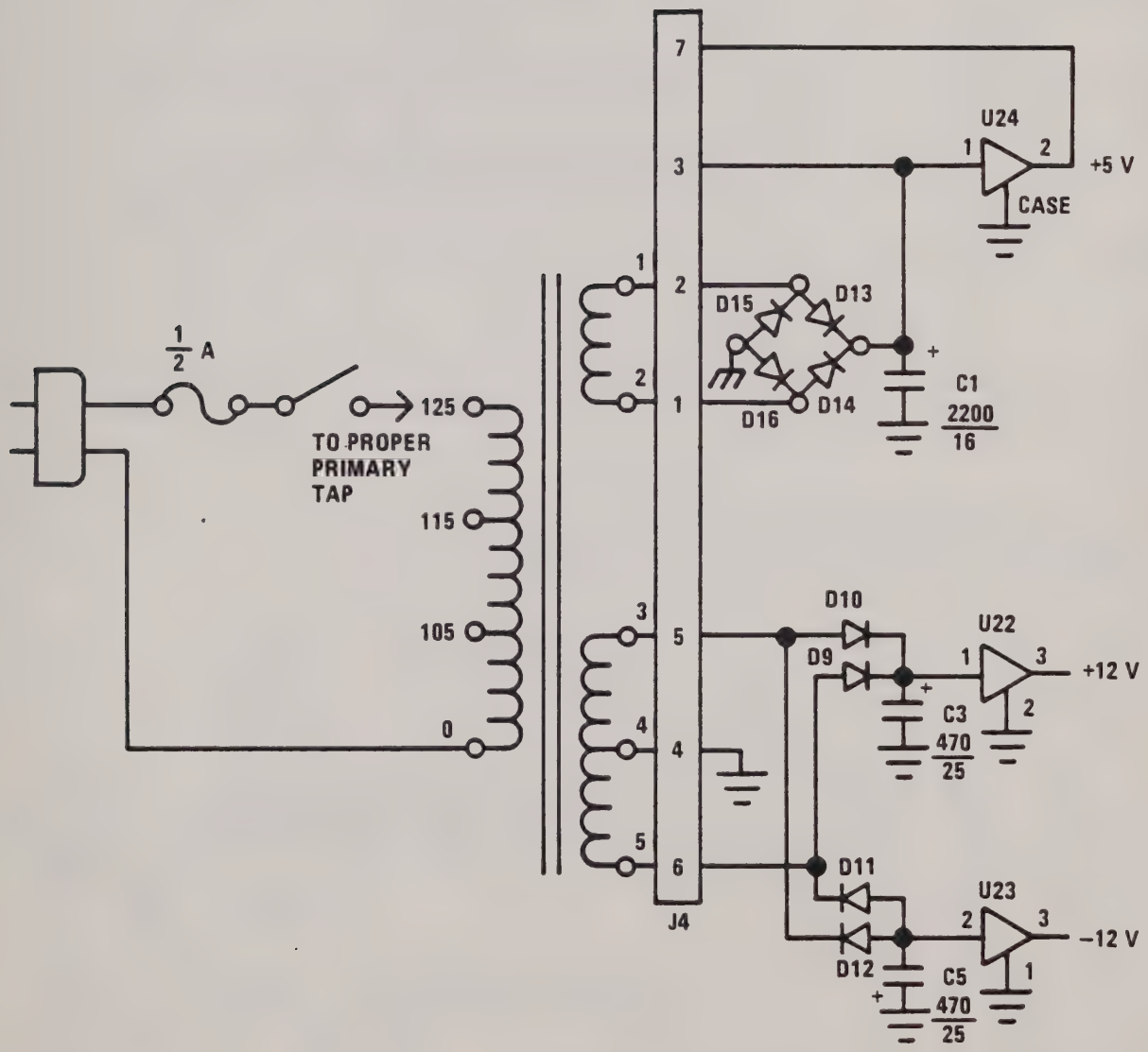


Fig. A.1 Transformer Wiring

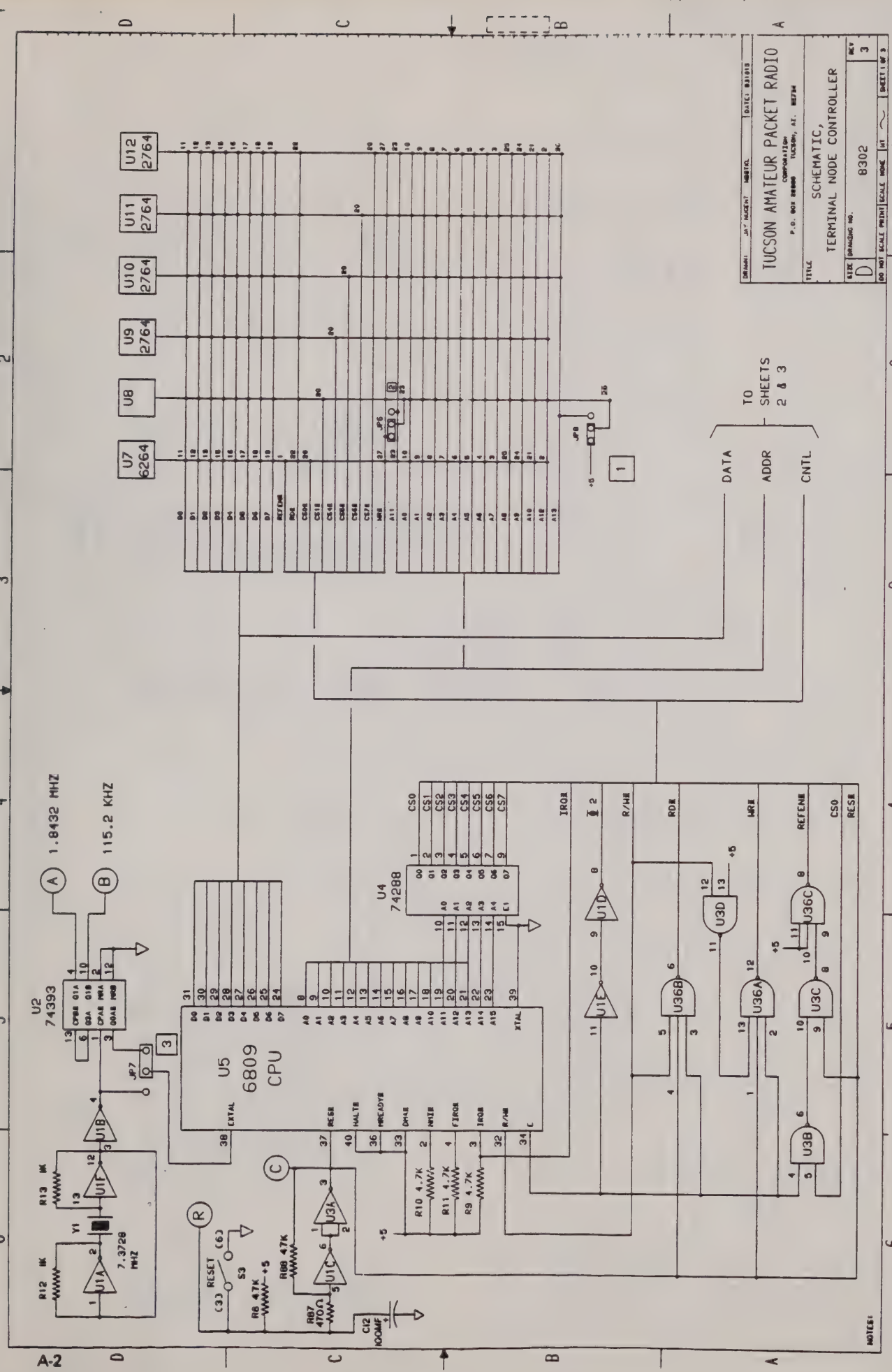


Fig. A.2 TNC Schematic — part 1 of 3

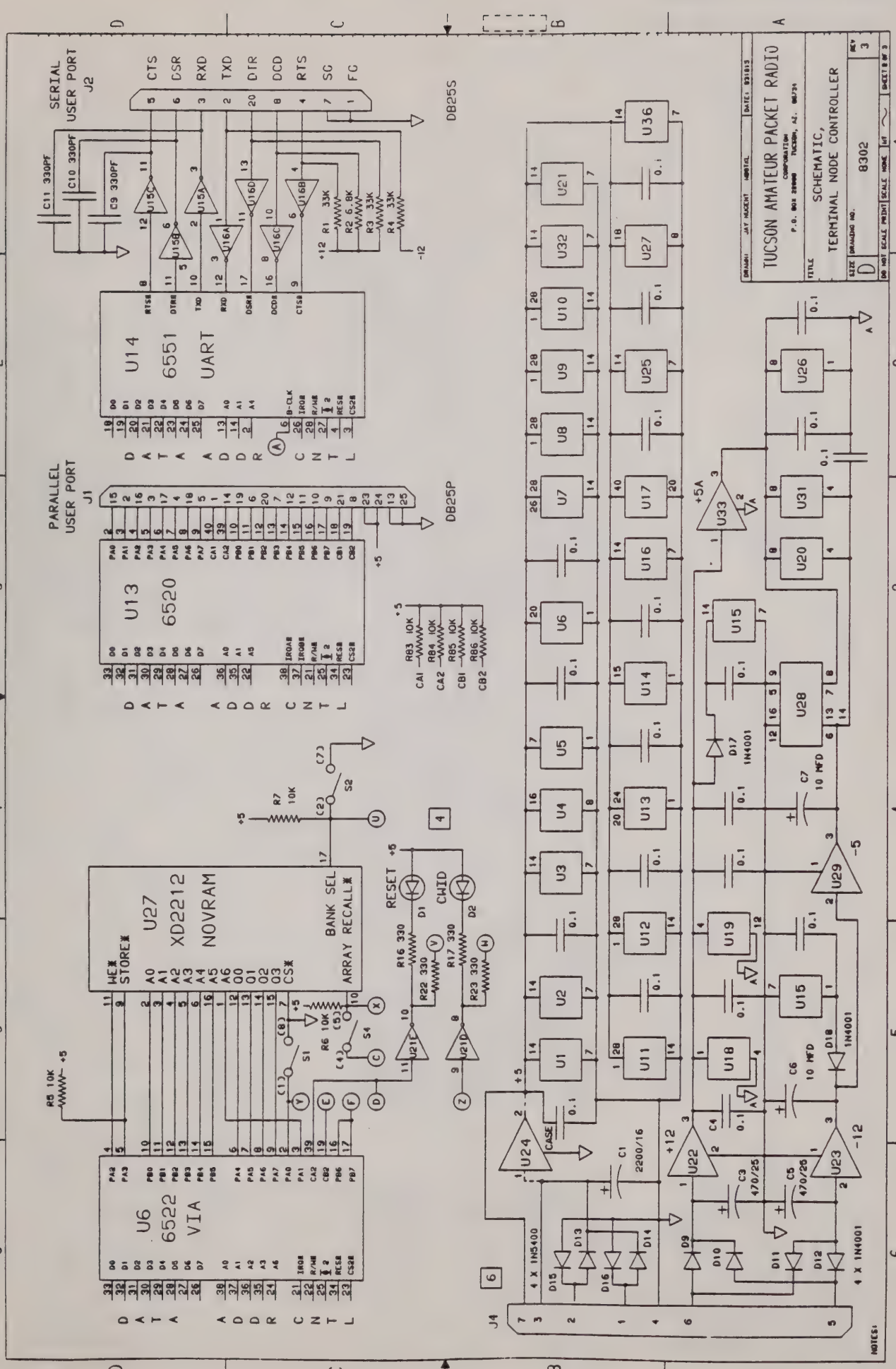
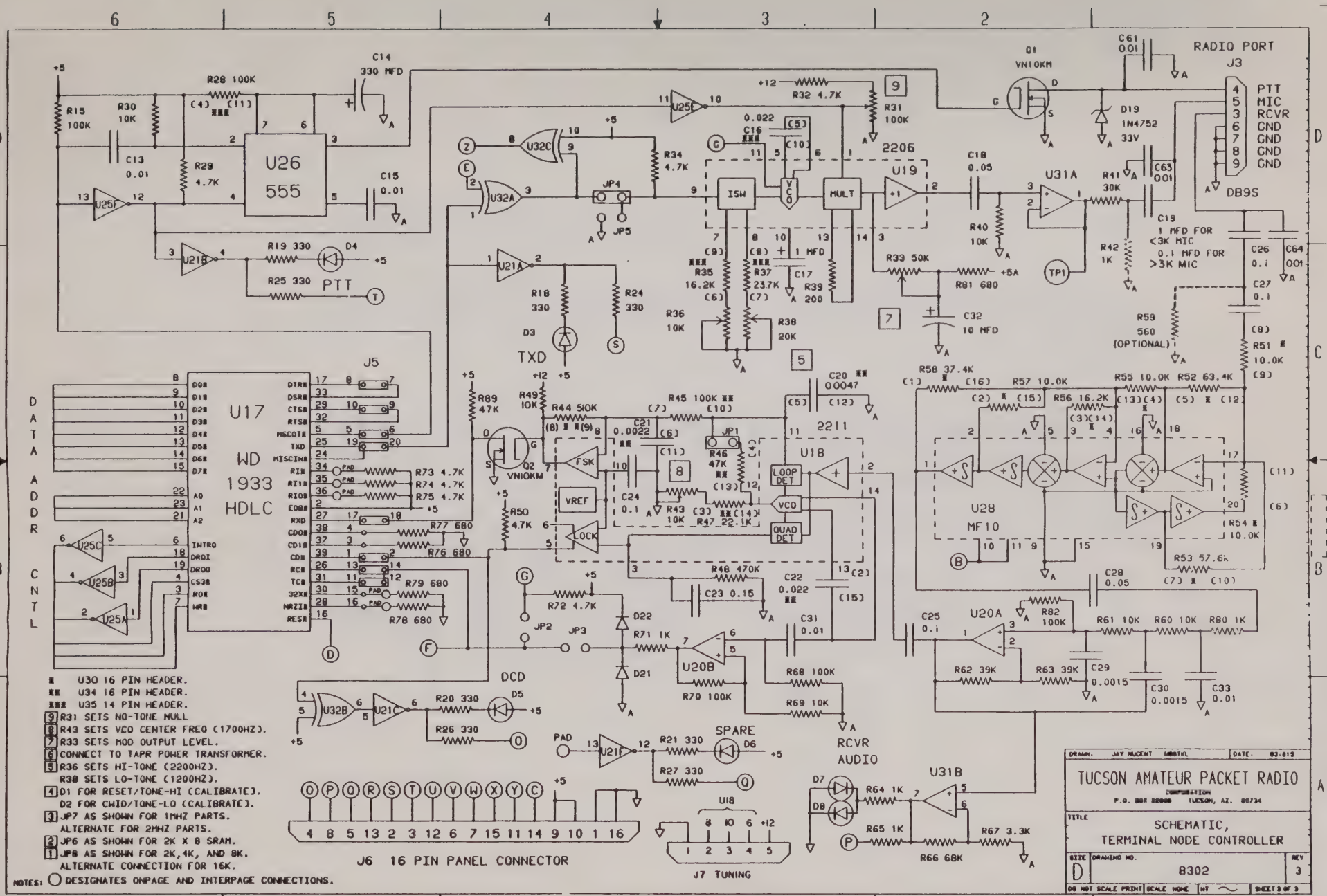


Fig. A.3 TNC Schematic – part 2 of 3



∎ U30 16 PIN HEADER.
∎∎ U34 16 PIN HEADER.
∎∎∎ U35 14 PIN HEADER.

[9] R31 SETS NO-TONE NULL
[8] R43 SETS VCO CENTER FREQ (1700HZ).
[7] R33 SETS MOD OUTPUT LEVEL.
[6] CONNECT TO TAPR POWER TRANSFORMER.
[5] R36 SETS HI-TONE (2200HZ).
R38 SETS LO-TONE (1200HZ).
[4] D1 FOR RESET/TONE-HI (CALIBRATE).
D2 FOR CWID/TONE-LO (CALIBRATE).
[3] JP7 AS SHOWN FOR 1MHZ PARTS.
ALTERNATE FOR 2MHZ PARTS.
[2] JP6 AS SHOWN FOR 2K X 8 SRAM.
[1] JP8 AS SHOWN FOR 2K,4K, AND 8K.
ALTERNATE CONNECTION FOR 16K.

NOTES: ○ DESIGNATES ONPAGE AND INTERPAGE CONNECTIONS.

Fig. A-4 TNC Schematic, Part 3 of 3

U1	74LSØ4	Hex Inverter
U2	74LS393	Dual 4-bit Counter
U3	74LSØØ	Quad NAND Gate
U4	74S288	32 x 8 Shottky PROM
U5	68Ø9	Microprocessor
U6	6522	Parallel Port w/Timers
U7	6264	8k x 8 Static RAM
U8	----	(spare memory site)
U9	2764	"8" 8k x 8 EPROM
U1Ø	2764	"A" 8k x 8 EPROM
U11	2764	"C" 8k x 8 EPROM
U12	2764	"E" 8k x 8 EPROM
U13	652Ø	(or 6821) Parallel Port
U14	6551	Serial Port (UART)
U15	1488	TTL --> RS232 Driver
U16	1489	RS232 --> TTL Receiver
U17	1933	HDLC Controller
U18	2211	FSK Demodulator
U19	22Ø6	FSK Modulator
U2Ø	1458	Dual Op Amp
U21	74Ø6	Hex O.C. Inverter
U22	7812	+12 Volt Regulator
U23	7912	-12 Volt Regulator
U24	3Ø9	+5 Volt Regulator
U25	74Ø6	Hex O.C. Inverter
U26	555	Timer
U27	2212	1k bit NOVRAM
U28	MF-1Ø	Four-Pole Filter
U29	79L05	-5 Volt Regulator
U3Ø	----	16-pin DIP Header
U31	1458	Dual Op Amp
U32	74LS86	Quad Exclusive-OR Gate
U33	78L05	+5 Volt Regulator
U34	----	16-pin DIP Header
U35	----	14-pin DIP Header
U36	74LS1Ø	Triple NAND Gate

Fig. A.5 IC List

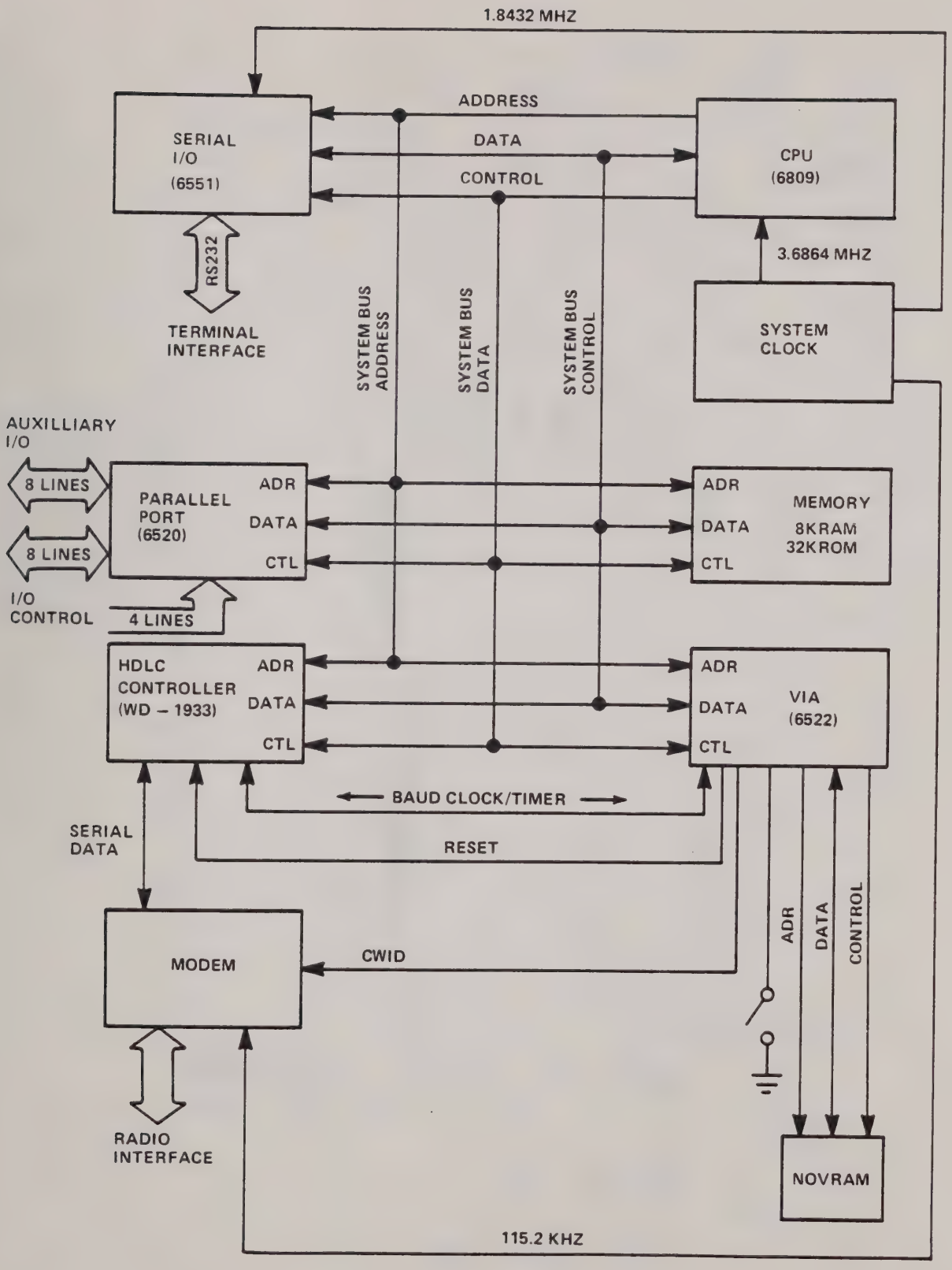


Fig. A.6 System Block Diagram

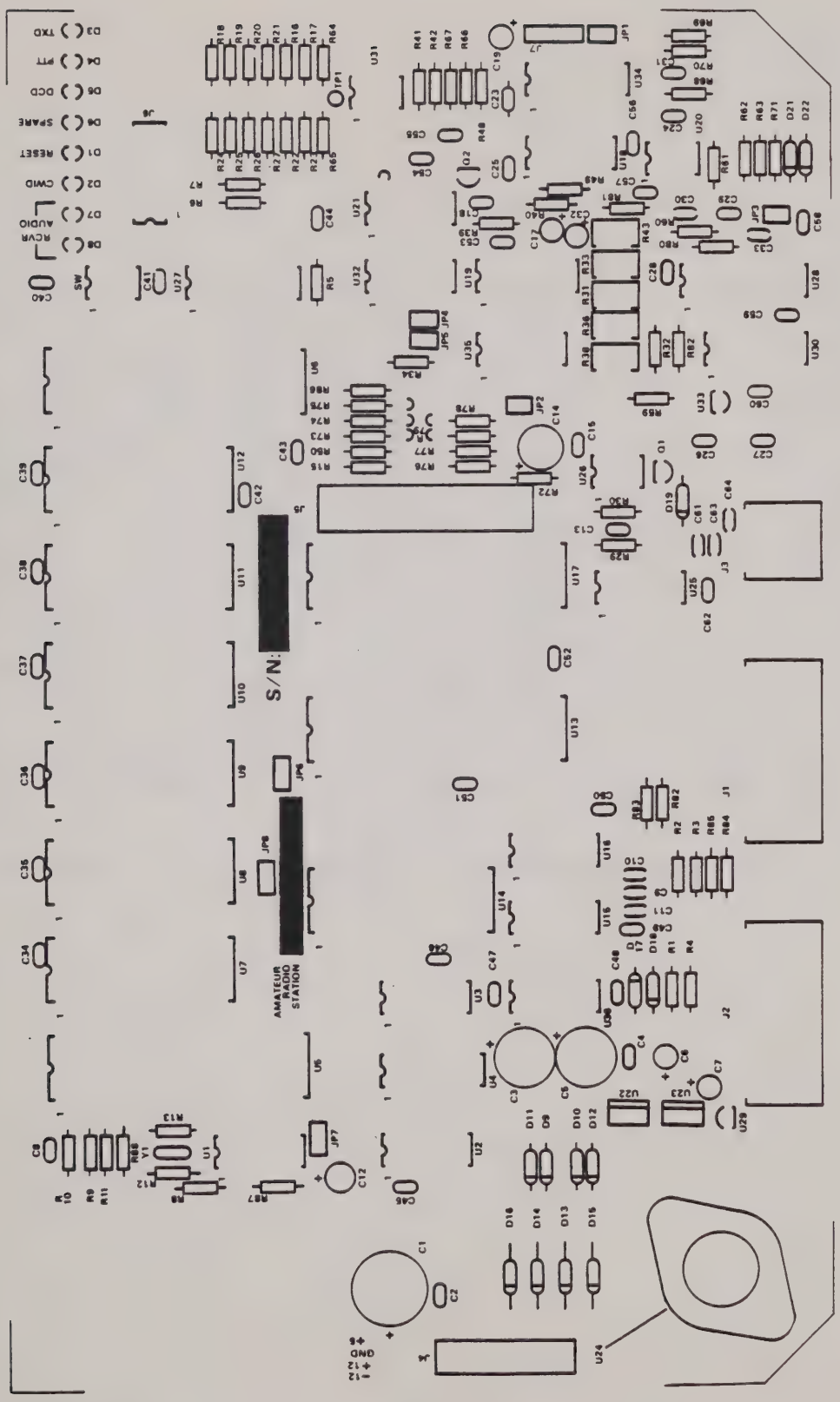


Fig. A.7 Board Layout

2Ø			3Ø	Ø	-----	4Ø	@	-...- (BT)	5Ø	P	.--.
21	!	---.	31	1	.----	41	A	.-	51	Q	--.-
22	"	.-..-.	32	2	..---	42	B	-...	52	R	.-.
23	#	.-.-. (AR)	33	3	...--	43	C	-.-.	53	S	...
24	$	...-.- (SK)	34	4	....-	44	D	-..	54	T	-
25	%	invalid	35	5	.....	45	E	.	55	U	..-
26	&	invalid	36	6	-....	46	F	..-.	56	V	...-
27	'	.----.	37	7	--...	47	G	--.	57	W	.--
28	(	-.--.	38	8	---..	48	H	....	58	X	-..-
29	)	-.--.-	39	9	----.	49	I	..	59	Y	-.--
2A	*	-.-.-- (KW)	3A	:	---...	4A	J	.---	5A	Z	--..
2B	+	-...- (BT)	3B	;	-.-.-.	4B	K	-.-			
2C	,	--..--	3C	<	invalid	4C	L	.-..			
2D	-	-....-	3D	=	invalid	4D	M	--			
2E	.	.-.-.-	3E	>	invalid	4E	N	-.			
2F	/	-..-.	3F	?	..--..	4F	O	---			

Note: All lower case characters are sent as upper case.

Fig. A.8 Morse Code Characters Available

APPENDIX D

HOOKUP FOR SPECIFIC RADIOS

This appendix contains interfacing information for the IC-2AT and the FT-2Ø8R, along with a table listing hookup information for several commonly used Amateur transceivers. All the data in the table is derived from information received from participants in our Beta test. While we hope there are no errors in this information, we urge caution in making use of it, in view of the potentially grave results some errors might produce. Please DO NOT use this information without independently checking its accuracy!

Each entry includes the source being quoted, the connector type, as described by that source, and the pin-out for the various functions. We also list the PTT open-circuit voltage as well as the current measured while keyed. In some cases more than one source described the same transceiver and we've tried to include all variations reported. In the case of reported variations we have simply listed them as described. They may be due to the use of alternative connectors where more than one exists on the unit.

Simple electrical tests with a VOM should be enough to determine the accuracy of the information listed. The primary situations to watch out for are accidently connecting the radio positive supply to the TNC's PTT line or audio input line (the latter is bad only if a polarized capacitor is used or the capacitor rating is exceeded.)

Interfacing the IC-2AT

The IC-2AT keys the transmitter by completing the ground connection on the microphone. The cleanest way to interface to this combination is to use an audio transformer, connecting the primary side to the TNC audio out and return lines (pins 5 and 6-9, respectively), and connecting one side of the secondary to the microphone input of the IC-2AT and the other to the PTT line on the TNC (pin 4). Almost any transformer should work, provided the turns ratio is not too great.

Interfacing the Yaesu FT-208R

The FT-208R must be interfaced carefully (presumably these peculiarities exist in other Yaesu equipment) in order to avoid damaging either the TNC or the radio. Using Yaesu's numbering of pins on the external connector, attach radio pin 1 to TNC pin 5, radio pin 2 to TNC pin 3, radio pin 3 to TNC pin 4 and radio pin 4 to TNC pins 6-9. On no account connect radio pin 6 to any pins on the TNC radio connector! The radio pins mentioned are the four closest to the FRONT panel of the HT, with pin 1 to the left as you hold it normally.

Note: Unless described to the contrary, all model numbers beginning with "TR" or "TS" in the table below refer to transceivers made by Kenwood.

SOURCE	RADIO	CONNECTOR TYPE	SP AUD PIN	PTT PIN	MIC AUD PIN	COMMON PIN	PTT OCV/ CURR.
KF4LG	TR-7600	5-pin		2	1	4,5	4.39V/30mA
		1/8" phone	tip			sleeve	
N6MA	GENAVE	1/4" phone	tip			sleeve	12V/22mA
	GTX-100	1/4" phone		tip	ring	sleeve	
W4CQI	KDK FM-2025A/E	4-pin		2	4	3	13V/1.0mA
	Mark II	mini-phone	tip			sleeve	
WB9HBH	KDK FM-2025	5-pin	1	5	2	case	12V/40 mA
	(NOTE: this arrangement via rear connector, J2)						
W1BEL	TS-700A	9-pin	1	6	3	2,4	17.3V/0.4mA
	(Note: W1BEL used rear aux. conn., tied pins 8 and 9 together)						
W1BEL	AZDEN PCS-3000	EC-80		9	12	2,11	9V/16mA
		mini phone	tip			sleeve	
WB0IBZ	TR-9130	6-pin	?	2	1	6	8.6V/49mA
W6SZX	ICOM 22-S	4-pin mike		3	1	4	13.8V/60mA
		small audio	tip			sleeve	
	(NOTE: W5FD used pin 2 as PTT)						
WB6HHV	Drake UV-3	Molex, under chassis. Added speaker audio to an unused pin, others per Drake. Changed R35 to 2.2k to help with p.s. "noise."					11.6V/30mA
WD6FPY	FT-227R	5-pin	1	3	2	case	?
WA6CFM	FT-230R	7-pin	5	3	2	1	4V/40mA
N6TE	ICOM 211	24-pin Molex Waldom 1625-24P	4	3	5	8	?
WB6UUT	ICOM 22U	Amphenol CBC-8		5	1	7	9.5V/25mA
		mini phone	tip			sleeve	
	(Note: WB9SVM used pin 6 instead of 7 for common)						
WD9DBJ	WILSON MARK II	WILSON P1016	J3-2	J3-3	J3-1	J3-4	12V/?
W7KB	TR 7950	6-pin		2	1	6	9.5V/16mA
		mini phone	tip			sleeve	
N8ANJ	HW-2036	RS 274-204/205		1	2	4	0.8V/2.2mA
		phono	tip			sleeve	
NØDVS	TR 7400 A	4-pin		2	1	3,4	?
		mini phone	tip			sleeve	
	(NOTE: KB7XP measured 12V/70mA)						
W8KOX	TR 2200	4-pin	3*	2	1	4	?V/#<10mA
	(*NOTE: pin 3 was rewired as speaker out rather than ground)						
WB9HBH	Drake TR-7	4-pin	*	2	4	3	5V/1.0mA
	(*Note: audio out from speaker line)						
NØENN	WILSON WE800	?	4	3	5	1	12.7V/50mA
AI6C	TR 9000	6-pin		2	1	6	8V/26mA
		mini phone	tip			sleeve	
	(NOTE: TouchTone jack was used for mike/PTT)						
KØHOA	TR 7800	6-pin		2	1	6	0.1V/8.2mA
		mini phone	tip			sleeve	
WDØFHG	ICOM 290A	9-pin	7	2	9	4	13.4V/13mA
	(NOTE: pin 7 was connected to ext. speaker jack, pin 9 to tone input pin 1)						

TAPR wishes to thank all those participants in Beta test who submitted radio interface data and returned software questionnaires. Along with Amateurs mentioned above, we thank W3VS, W6LLE, N9ATM, WB9BWN, WB9CNE, W9TD, WA9WXC, WA9YKK, NØCCZ, KØNN, and WØQKA for their radio interface information.

APPENDIX E

SPECIAL FUNCTIONS

Trace Function

The trace function is a protocol debugging tool. It allows the user to examine the frame structure of sent and received packets. If you report difficulties with the software, you may wish to include output from the trace function with such a report. Trace options are individually enabled with the TRACE command.

Frame dumping options are: dump frames sent, dump frames used (frames addressed to this TNC), dump all frames read, dump digipeated frames, and dump FRMR frames. Selecting all options will result in some frames being dumped more than once. Be aware that enabling all trace functions on a TNC whose terminal baud rate is no faster than the HDLC baud rate will quickly fill up the terminal output buffer. Refer to the section on Editing Commands in Chapter III for fast relief from this condition.

The frame dump output contains three sections and will be properly formatted if the screen width is at least 80. Unless the option to dump the entire frame is enabled, the trace dump will show the header only.

The first section (leftmost column) contains the hex representation of the actual bytes transmitted or received. (If you examine the I/O buffers used by the transmitting and receiving routines you will notice no apparent resemblance to this data. The data is complemented prior to being stored in the buffer to compensate for the reversed logic of the WD-1933 chip. Convert 0s to 1s and vice versa.)

The second section (middle column) of the dump displays the packet contents as ASCII characters, after right-shifting each byte by one bit. You should be able to easily identify the structure of the addresses used from this section; AX.25 protocol calls for address fields consisting of ASCII call signs left-shifted by one bit.

The third section (right column) displays the packet as ASCII characters. The header information in this section

will look funny, but you should be able to read the message, if any, from this section.

Debug Program

This program was written as a debugging tool for the authors of the software. People wishing to modify TNC software or examine hardware I/O addresses may find the commands described here useful. In addition, you may wish to use this program to examine memory locations as supplementary information to accompany reports of software bugs (we don't really expect any, but it's always wise to be prepared...).

The debug program is entered by typing the character specified by the DEBUG command, by default <ctl-E>. The same character is used to exit the debug program. Following the exit command the message "Bye" will be displayed.

When debug mode is entered, a prompt symbol (:) will be sent to the terminal. This prompt will also be sent to the terminal after completion of a debug mode command except the exit command.

> NOTE: All numbers input to the debug program are in hex and the hex prefix ($) is optional. This is different from the convention employed in command mode. The program does not parse numbers to insure that they are within appropriate bounds. If you type a number which is too large, the low-order part of the number will be used and no warning will be given.

The debug program operates with a very rudimentary and intolerant parser. Instructions which are not recognized do not produce warnings or error messages. They simply aren't executed. Parsing of numeric input stops when an invalid (non-numeric) character is encountered.

Following is a summary of debug mode commands. All commands are started with a special command character and terminated by a <cr>. The command characters are @, #, >, T, L, and Q. User-supplied variables are identified here in lower case and explained in the following text. Material enclosed in square brackets is explanatory.

Display Storage

@address

where address is the address whose contents you want displayed.

The program will respond with

address content [no <cr>]

Content is the value read by the processor when the specified location is addressed. (Some peripheral chips address different registers on read and write access of the same location.) Typing a <cr> will cause the program to display the next address and contents. This mode is terminated by typing "Q".

Example:

```
:@AC00
AC00 56
AC01 41
AC02 4C Q
:
```

Following the prompt symbol, the user typed the characters "@AC00" followed by three <cr>s. This requested the contents of three bytes starting at hex AC00. The storage asked for contained the ASCII characters "VAL" which was sent to the terminal as 6 ASCII characters showing the hex representation of the storage content. The display function was terminated by typing "Q" and a final <cr>.

There is no restriction as to what address you specify. You can specify RAM, ROM, some hardware device, or a non-existent address. The result of asking for the contents of a non-existent address is not predictable and probably not very useful. You should be aware that reading some I/O locations may affect hardware status. Not all of the address lines are used in addressing the I/O chips, so a given location may be accessed by more than one address. The memory socket for U8 is mapped for 16k of RAM or ROM. If you use this socket for a memory smaller than 16k, each location will be accessed by more than one address.

Write to Storage

@address=data

where address is the address of the location you want to put the data into, and data is the hex representation of one byte of data. The program will respond by prompting you with the next address and an "=" sign:

nextaddress=[no <cr>]

Subsequent addresses can be loaded by typing only the data to be stored. Typing a <cr> only will enter Ø into the location. This mode is terminated by typing "Q".

If you try to write to a non-existent address or to a read-only address, no warning will be given. If you write to a hardware control register you may affect the behavior of the TNC in an unexpected way.

Example:

```
:@1FØØ=48
1FØ1=49
1FØ2=Q
:
```

The storage area starting with 1FØØ has been filled with the ASCII characters for "HI".

Registers

Contents of registers as they appeared just prior to entering debug mode can be examined and changed. To examine the register contents:

#register

Here, register is a register code as follows: C (condition code), A, B, D (A|B), G (direct page), X, Y, U, S, P (program counter). The contents of D, X, Y, U, S, and P will be displayed as double-byte quantities. The program will respond

```
#register content
:
```

To alter register contents:

#register=data

where data is the single-byte quantity to be stored in registers C, A, B, or G, or a double-byte quantity to be stored in registers D, X, Y, U, S, or P. The program will respond with a prompt symbol. The values set in this procedure will be moved into the actual registers upon exit from the debug program. Altering register contents can produce bizarre results.

Start Execution at Address

>address

address is the location in storage which contains the next instruction you want executed. A call (JSR) to that location will be executed. If the instruction sequence executed terminates with a RTS instruction, the execution will return to the debug program and the prompt symbol will be typed. If the routine starting at address requires initialization of registers prior to entry, you should supply a routine to do so. The register contents on entry to the debugger are not loaded for the subroutine call, and are not affected by the call.

If you wish to load test routines into RAM for execution using this command, you should be careful not to write over areas used by the TNC operating system. For the version 3.0 release, the RAM area from $1800 to $2000 is unused, as well as any RAM occupying the U8 socket.

Copy

Tsource destination

where source is the source address range expressed in one of the following ways:

address
address!length
address:endsource

and destination is the beginning of the destination address range. The source and destination fields are separated by a space.

List

Lsource

will copy addresses and contents from the address(es) specified by source to the terminal. The format of source is the same as for the copy command.

APPENDIX F

ASSEMBLY

TAPR TNC - Parts List

Please check the enclosed parts with this list. Check off each item in the space ( ) provided. The number following the check-off space is the quantity required.

The TAPR part numbers referenced below are not necessarily marked on the parts. Rather, they are provided for reference when ordering replacement parts from TAPR.

Parts Sorting

As you sort the parts in the parts list, you may find it convenient to place them in a muffin tin, egg carton or other compartmentalized container for ready access.

Resistors

1/4 watt, 5%

				TAPR P/N
( )	1	200 ohm	(red-black-brown-gold)	CFR1/4-201
( )	12	330 ohm	(orange-orange-brown-gold)	CFR1/4-331
( )	1	470 ohm	(yellow-violet-brown-gold)	CFR1/4-471
( )	1	560 ohm	(green-blue-brown-gold)	CFR1/4-561
( )	5	680 ohm	(blue-grey-brown-gold)	CFR1/4-681
( )	7	1k ohm	(brown-black-red-gold)	CFR1/4-102
( )	1	3.3k ohm	(orange-orange-red-gold)	CFR1/4-332
( )	14	4.7k ohm	(yellow-violet-red-gold)	CFR1/4-472
( )	1	6.8k ohm	(blue-grey-red-gold)	CFR1/4-682
( )	13	10k ohm	(brown-black-orange-gold)	CFR1/4-103
( )	1	30k ohm	(orange-black-orange-gold)	CFR1/4-303
( )	3	33k ohm	(orange-orange-orange-gold)	CFR1/4-333
( )	2	39k ohm	(orange-white-orange-gold)	CFR1/4-393
( )	1	68k ohm	(blue-grey-orange-gold)	CFR1/4-683
( )	5	100k ohm	(brown-black-yellow-gold)	CFR1/4-104
( )	1	470k ohm	(yellow-violet-yellow-gold)	CFR1/4-474
( )	1	510k ohm	(green-brown-yellow-gold)	CFR1/4-514

1/8 watt, 1%			TAPR P/N
( ) 4	10.0k ohm	(brown-black-black-red-brown)	CFR1/8-1002
( ) 2	16.2k ohm	(brown-blue-red-red-brown)	CFR1/8-1622
( ) 1	22.1k ohm	(red-red-brown-red-brown)	CFR1/8-2212
( ) 1	23.7k ohm	(red-orange-violet-red-brown)	CFR1/8-2372
( ) 1	37.4k ohm	(orange-violet-yellow-red-brown)	CFR1/8-3742
( ) 1	47.5k ohm	(yellow-violet-green-red-brown)	CFR1/8-4752
( ) 1	57.6k ohm	(green-violet-blue-red-brown)	CFR1/8-5762
( ) 1	63.4k ohm	(blue-orange-yellow-red-brown)	CFR1/8-6342
( ) 1	100.k ohm	(brown-black-black-orange-brown)	CFR1/8-1003

Trimpots

( ) 2	10k ohm	(68WR-10K)	68WR-103
( ) 1	20k ohm	(68WR-20K)	68WR-203
( ) 1	50k ohm	(68WR-50K)	68WR-503
( ) 1	100k ohm	(68WR-100K)	68WR-104

Capacitors

Ceramic Disc

Capacitors may be marked in various ways. The typical markings are given but may vary. Find all that match the typical markings given and the remaining ones, if any, should become apparent by elimination.

			TAPR P/N
( ) 3	330 pF	(330 or 331)	DISC-331
( ) 2	1500 pF	(.0015 or 152)	DISC-152
( ) 7	0.01 uF	(.01 or 103)	DISC-103
( ) 2	0.05 uF	(.05 or 503)	DISC-503
( ) 36	0.1 uF	(.1 or 104)	DISC-104
( ) 1	0.15 uF	(.15 or 154)	DISC-154

Mylar or Monolithic

( ) 1	2200 pF	(.0022 or 222)	MONO-222
( ) 1	0.0047 uF	(.0047 or 472)	MONO-472
( ) 2	0.022 uF	(.022 or 223)	MONO-223

Electrolytic

The electrolytic capacitors may have voltage ratings equal to or greater than those listed below, but not less.

			TAPR P/N
( )	2	1 uF, 16 volt	RAD16V-1Ø5
( )	3	1Ø uF, 16 volt	RAD16V-1Ø6
( )	1	1ØØ uF, 6 volt	RADØ6V-1Ø7
( )	1	33Ø uF, 6 volt	RADØ6V-337
( )	2	47Ø uF, 25 volt	RAD25V-477
( )	1	22ØØ uF, 16 volt	RAD16V-228

Diodes

Diodes are marked with the value directly on the body of the part. Some diodes are very small, so a magnifying glass may be helpful!

Note that the banded or tapered end is the cathode on standard diodes and the flat side is the cathode on the LEDs. The LEDs may not have a part number on them.

			TAPR P/N
( )	6	1N4ØØ1	1N4ØØ1
( )	2	1N4148 (these are the smallest diodes)	1N4148
( )	1	1N4752 Zener	1N4752
( )	4	1N54ØØ (these are the largest diodes)	1N54ØØ
( )	8	LED, red	SR-5Ø3D

Transistor and Voltage Regulators

( )	2	VN1ØKM or VN1ØKMA Power FET	VN1ØKM
( )	1	3Ø9 or 34Ø-5 or 78Ø5 +5 volt regulator	LM3Ø9K
( )	1	78LØ5 +5 volt regulator	UA78LØ5
( )	1	7812 +12 volt regulator	UA7812
( )	1	79LØ5 or LM32Ø -5 volt regulator	UA79LØ5
( )	1	7912 -12 volt regulator	UA7912

Integrated Circuits

Integrated circuits may come from various manufacturers and may have differing prefixes and/or suffixes. For example, if the part is listed as a 74LSØØ, it may be marked SN74LSØØN or MC74LSØØP or DM74LSØØN or F74LSØØP or a similar variation. The key is that the sequence 74LSØØ appears in the part number. A four-digit number, such as 83Ø4, indicates the year and week of manufacture and should not be confused with being a part number.

WARNING: Do not handle the ICs at this time! Carefully remove the black foam carrier with ICs from the anti-static bag and verify the ICs against this list. Then return the foam with ICs to the anti-static bag. Do not touch any of the ICs. This precaution cannot be overemphasized!

			TAPR P/N
( )	1	74LSØØ TTL NAND gate	74LSØØ
( )	1	74LSØ4 TTL Hex inverter	74LSØ4
( )	2	74Ø6 TTL OC inverter	74Ø6
( )	1	74LS1Ø TTL NAND gate	74LS1Ø
( )	1	74LS86 EX-OR gate	74LS86
( )	1	74LS393 TTL divider	74LS393
( )	1	1488 TTL to RS232 driver	MC1488
( )	1	1489 or 1489A RS232 to TTL receiver	MC1489
( )	1	82S123, 74S288 or 76Ø3 Bipolar PROM	74S288
( )	1	555 Timer	NE555V
( )	2	1458 dual Op Amp	LM1458
( )	1	2Ø6 FSK Modulator	XR2Ø6
( )	1	2211 FSK Demodulator	XR2211
( )	1	2212 NOVRAM	XD2212
( )	1	MF-1Ø CMOS filter	MF-1Ø
( )	1	68Ø9 NMOS microprocessor	MC68Ø9
( )	1	652Ø or 6821 NMOS parallel port	MC6821
( )	1	6522 NMOS parallel port/timer	SY6522
( )	1	6551 NMOS Uart	SY6551
( )	1	1933 or 1935 NMOS HDLC controller	WD1933
( )	1	6264 (or equiv.) MOS 8k by 8 RAM	8KRAM
( )	4	2764 (labelled 8, A, C, E)	2764-( )

Sockets		TAPR P/N
( ) 3	8-pin DIP	DIPS-Ø8
( ) 11	14-pin DIP	DIPS-14
( ) 5	16-pin DIP	DIPS-16
( ) 1	18-pin DIP	DIPS-18
( ) 1	2Ø-pin DIP	DIPS-2Ø
( ) 7	28-pin DIP	DIPS-28
( ) 4	4Ø-pin DIP	DIPS-4Ø

Connectors		
( ) 5	2-pin male header	HM-Ø2
( ) 3	3-pin male header	HM-Ø3
( ) 1	5-pin male header with "wall"	HMW-Ø5
( ) 13	Jumpers, "push-on"	JMP-Ø2
( ) 1	2Ø-pin male header with ejector/latch	HMSEL-2Ø
( ) 1	DE9S 9-pin female PC right angle	DEØ9PCR-S
( ) 1	DB25P 25-pin male PC right angle	DB25PCR-P
( ) 1	DB25S 25-pin female PC right angle	DB25PCR-S
( ) 1	7-pin male "MOLEX" power	MOLPWR-Ø7P
( ) 1	14-pin male DIP header	DIPH-14
( ) 2	16-pin male DIP header	DIPH-16

Miscellaneous		
( ) 1	Crystal, 7.3728 MHz (NDK, M-tron or equal)	XTAL-Ø73
( ) 1	4-position DIP switch (CTS 2Ø6-4)	DIPSW-Ø2
( ) 2	6-32 x 3/8 screw	MS632-3/8
( ) 2	6-32 x 5/8 screw	MS632-5/8
( ) 4	6-32 hex nut	MN632
( ) 4	#6 flat washer	MW632-FLAT
( ) 2	#6 lock washer	MW632-LOCK
( ) 1	MOLEX Power connector housing	MOLPWR-Ø7S
( ) 7	MOLEX Power connector pins	MOLPWR-PINS
( ) 1	DE9P 9-pin male connector	DEØ9-P
( ) 1	3 foot length of stranded hookup wire	W2ØS-36

The following components are packed separately

( ) 1	Heat sink	HSTO3-1
( ) 1	PC board, TNC rev 3	TNCPC-R3
( ) 1	Power Transformer - 115 v 6Ø Hz (4111)	XFMR-TNC-2
	or	
	Power Transformer - 23Ø v 5Ø Hz (4425)	XFMR-TNC-3
( ) 1	Backshell for DE9P	DEØ9-BACK

TAPR TNC - Assembly Directions

This section will proceed in a step-by-step fashion. Please mount and solder the components to the board in the order given. It has been tested on numerous units and found to be a very efficient order for assembly.

When mounting axial lead discrete components, such as resistors and certain diodes, grasp the body of the part in one hand and bend the leads of the device with the other, such that the leads will pass through the holes provided on the board. Note that all axial lead components except 1N5400 diodes are on 0.5 inch lead centers. Place the part so the body is parallel to, and flat against, the circuit board unless instructed otherwise. Next, bend the leads on the bottom of the board slightly to secure the part. Then proceed to mount the next component. Do not solder the part until directed. (See Fig. F.1.)

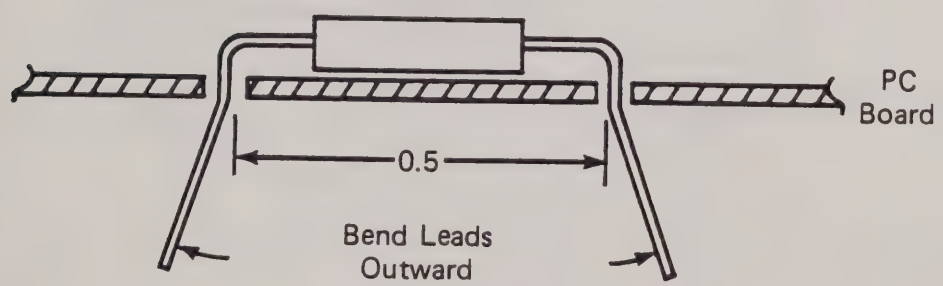


Fig. F.1 Axial Lead Component Mounting

Soldering

When soldering to the printed circuit board, use only a very small tipped controlled temperature (700 degrees F. maximum) soldering iron and fine (.050 inch) rosin-core solder. 63/37 (eutectic) solder is preferred, but 60/40 is acceptable.

To ensure the joint is properly soldered, the iron should be placed so that it contacts the pad on the board and the lead to be soldered. Solder should then be applied to the pad and the opposite side of the lead from the iron. Thus, the iron must heat both the pad and the lead to cause the solder to melt. This helps prevent cold solder joints. (See Fig. F.2.) Also, keep a wet sponge handy and wipe the soldering iron tip on it frequently to keep it (and your connections) clean.

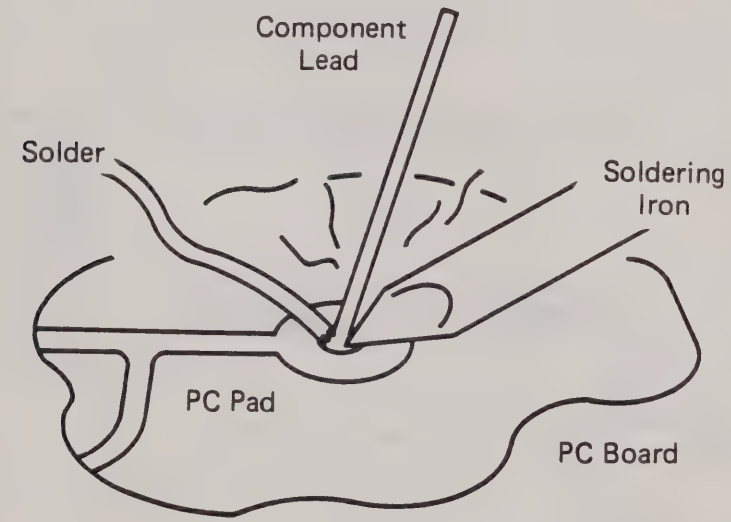


Fig. F.2 Soldering

Care must be taken to ensure that the pad is not overheated, or it may lift from the PC board! Apply heat only when ready to solder. Do not apply heat longer than necessary to complete a good joint, but be sure to heat adequately for the solder to flow completely.

NOTE: All modern components are designed for soldering and you should not worry about 3-5 seconds of heating.

Board Mapping

Place the circuit board on a clean working surface. Orient the board so the lettering is in normal reading position. For purposes of description, the board consists of four sectors. The top of the board is the edge nearest the memories (U7-U12). The bottom, left and right follow in natural order. The upper left is quadrant one, the upper right is quadrant two, the lower left is quadrant three and the lower right is quadrant four. The board assembly will proceed by quadrants. (See Fig. F.3.)

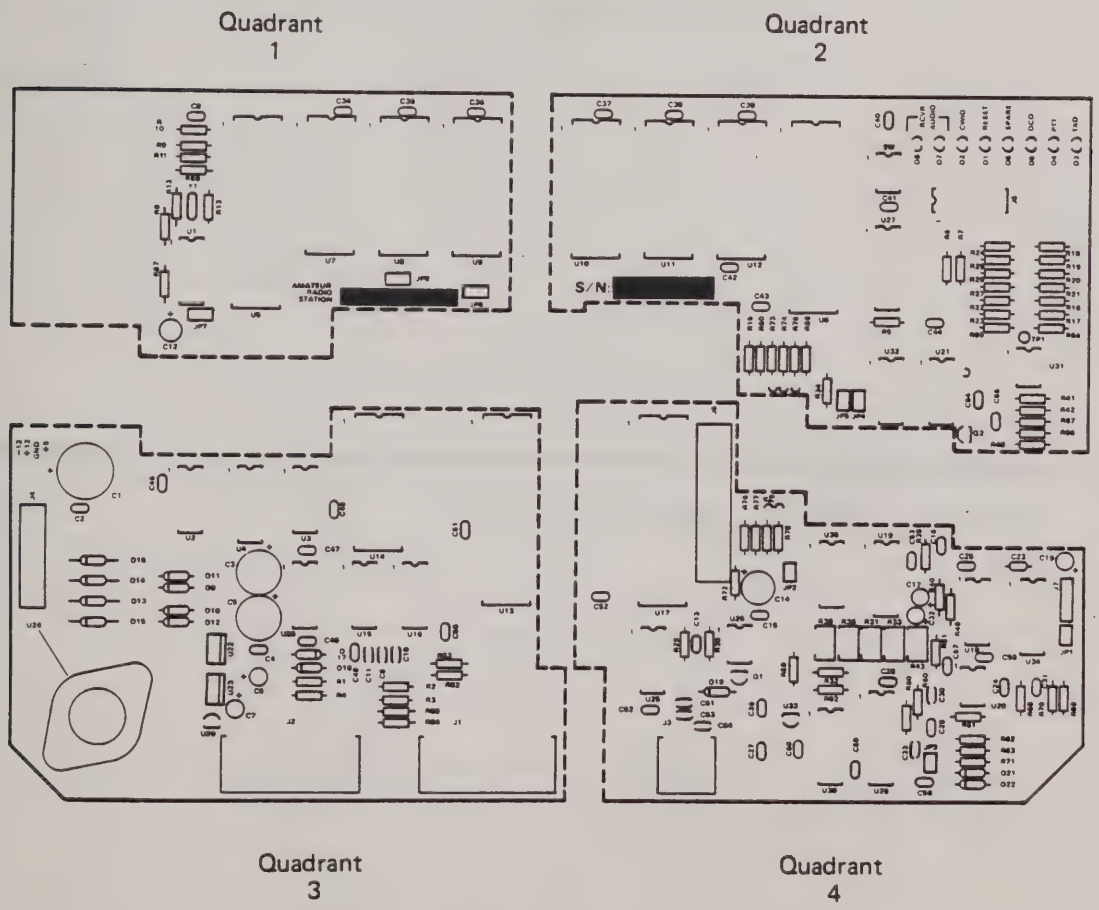


Fig. F.3 Board Mapping

IC Sockets

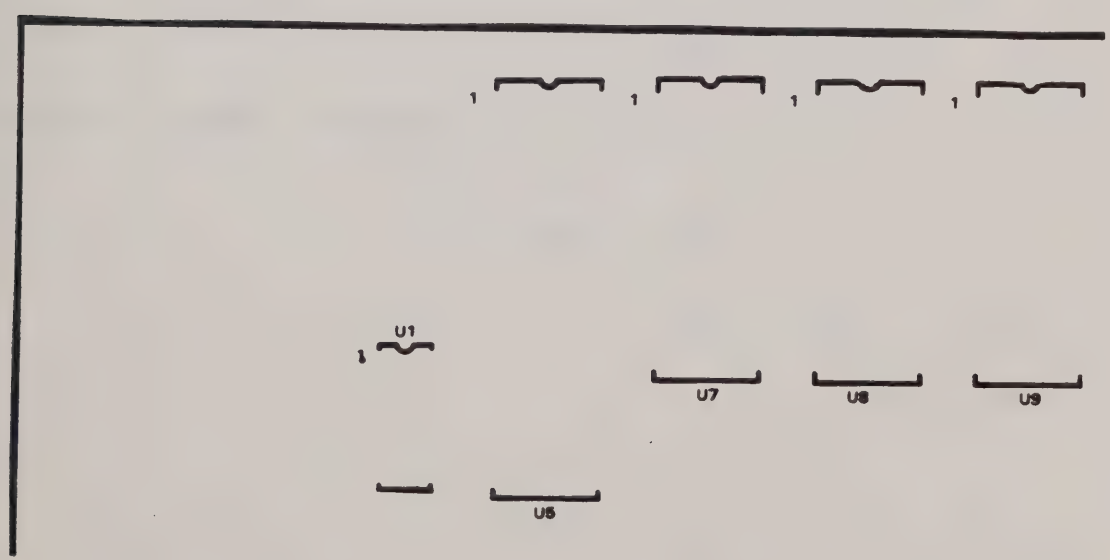
NOTE: If any socket pins are bent, carefully straighten them with a pair of long-nose pliers.

When installing IC sockets, tack-solder two diagonally opposite corners first (such as pin 1 and pin 8 on a 14-pin socket). Double check to ensure that the socket is seated properly against the board with the notch, bevelled corner or "1" nearest pin 1 (pin 1 is the upper left corner when viewed from the top of the board on all socketed IC positions) and that all IC socket pins are showing on the solder side of the board. Then solder the remaining pins of that socket before proceeding to the next one.

NOTE: Take care now to avoid solder bridges!

(quadrant 1)

( ) U1 14-pin
( ) U5 40-pin
( ) U7 28-pin
( ) U8 28-pin
( ) U9 28-pin

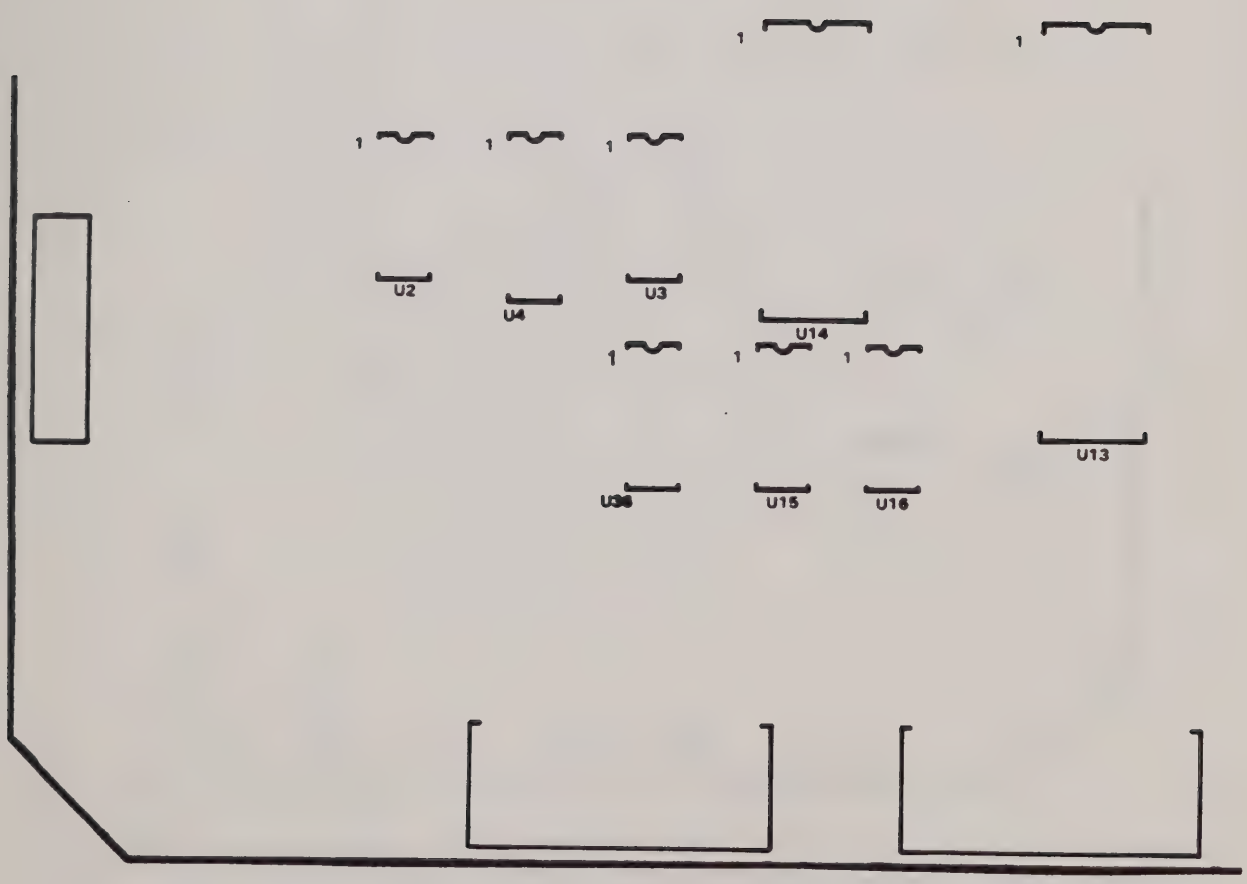


(quadrant 2)

( ) U10 28-pin
( ) U11 28-pin
( ) U12 28-pin
( ) U6 40-pin
( ) U27 18-pin
( ) J6 16-pin
( ) U32 14-pin
( ) U21 14-pin
( ) U31 8-pin

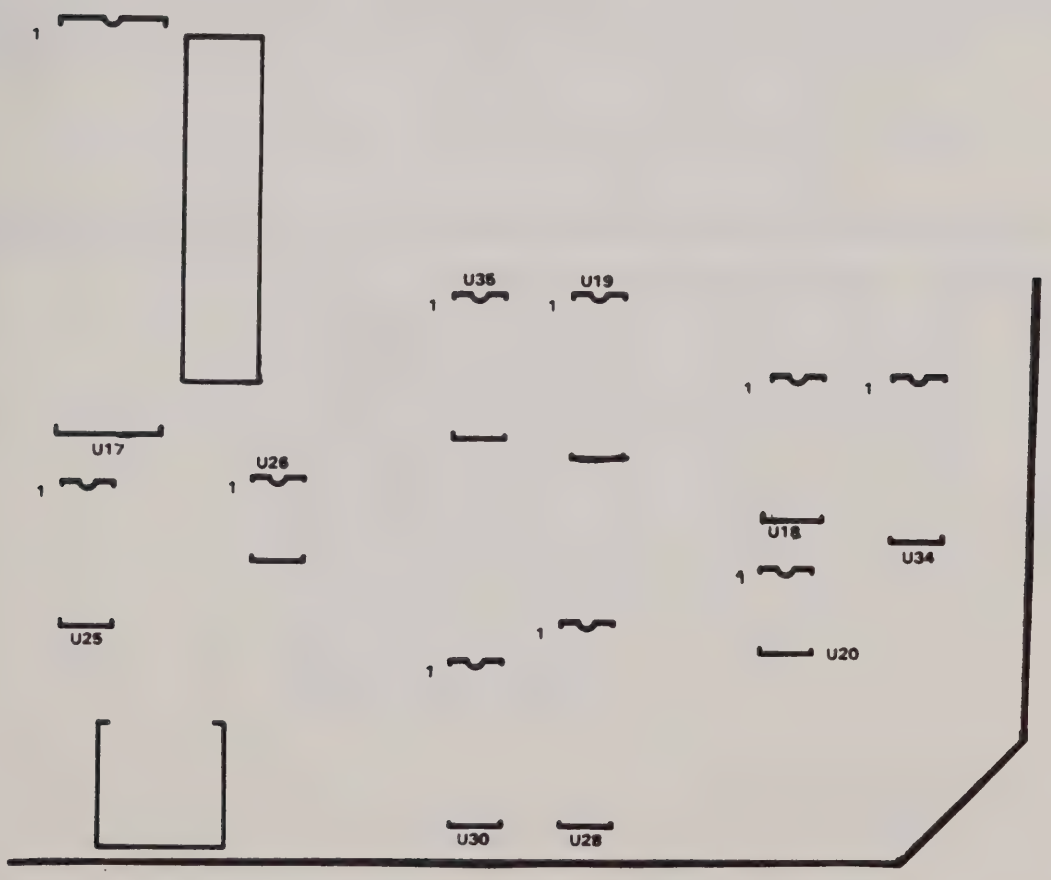
(quadrant 3)

( )	U2	14-pin
( )	U4	16-pin
( )	U3	14-pin
( )	U14	28-pin
( )	U13	40-pin
( )	U36	14-pin
( )	U15	14-pin
( )	U16	14-pin



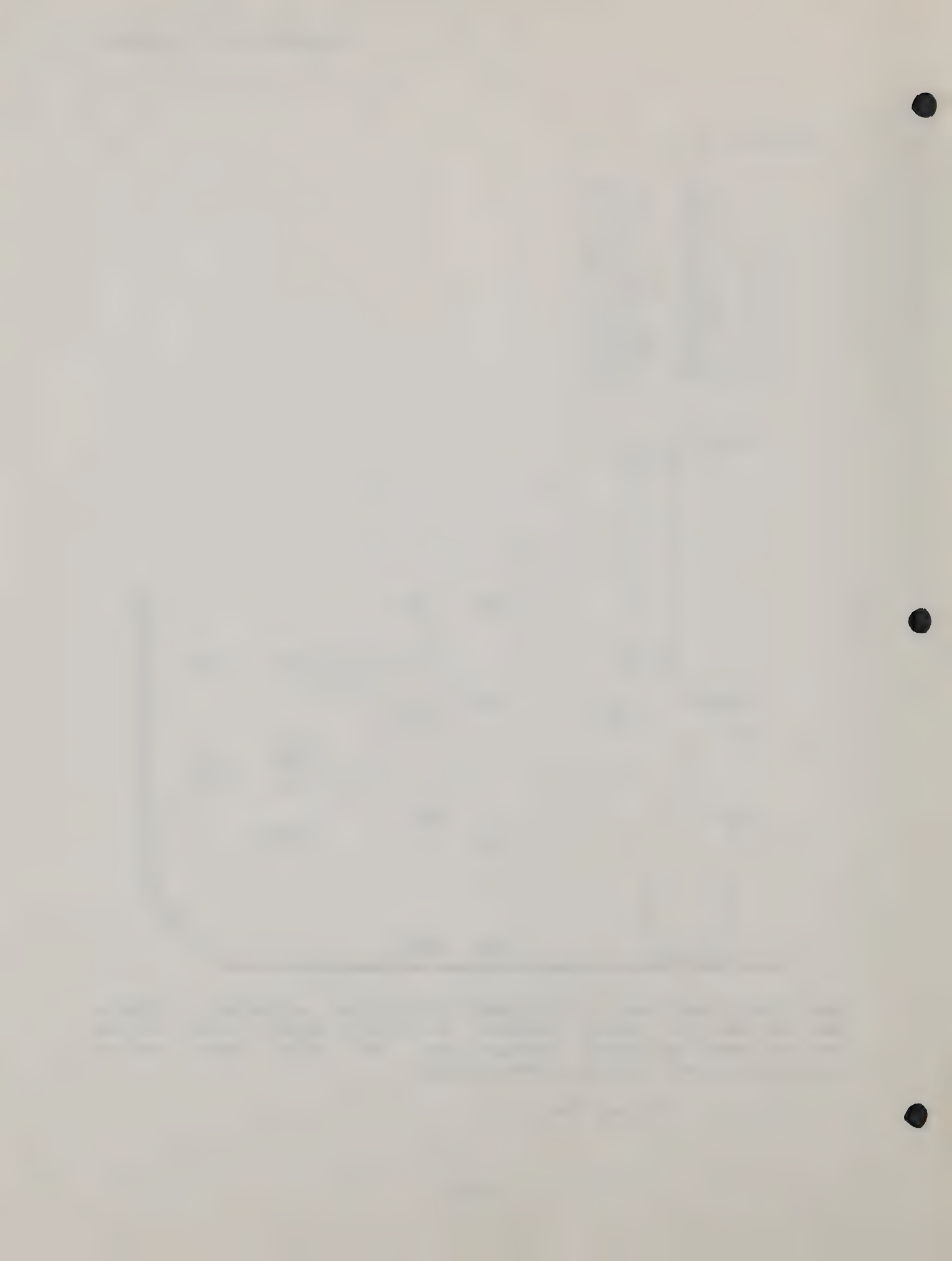
(quadrant 4)

( ) U17 40-pin
( ) U35 14-pin
( ) U19 16-pin
( ) U18 14-pin
( ) U34 16-pin
( ) U25 14-pin
( ) U26 8-pin
( ) U30 16-pin
( ) U28 20-pin
( ) U20 8-pin



Now check your work. All leads should be soldered. There should be no solder bridges (a blob of solder that shorts two adjacent soldered connections) or cold (grey and/or grainy-looking) solder connections.

( ) OK so far.



Resistors

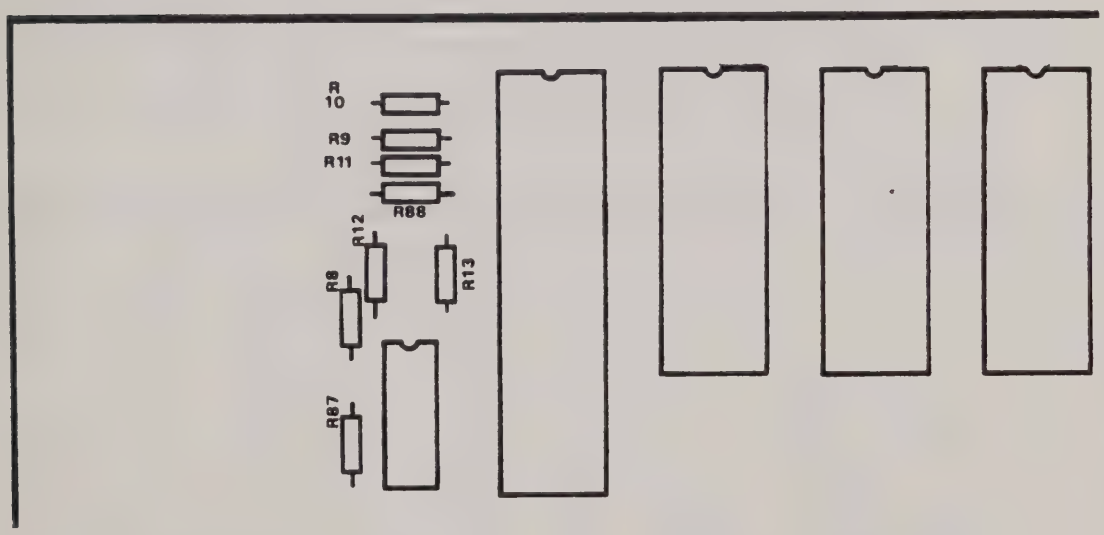
Install the following 5% resistors on the board:

(quadrant 1)

( ) R10 4.7k (yellow-violet-red-gold)
( ) R9 4.7k (yellow-violet-red-gold)
( ) R11 4.7k (yellow-violet-red-gold)
( ) R88 4.7k (yellow-violet-red-gold)
( ) R12 1.0k (brown-black-red-gold)
( ) R13 1.0k (brown-black-red-gold)
( ) R8 4.7k (yellow-violet-red-gold)
( ) R87 470 (yellow-violet-brown-gold)

WARNING: Be careful when clipping leads, as they may have a tendency to fly towards your eyes! Take appropriate precautions (grasp leads and provide eye protection).

( ) Solder and clip the leads (16 total).



(quadrant 2)

( ) R15 100k (brown-black-yellow-gold)
( ) R50 4.7k (yellow-violet-red-gold)
( ) R73 4.7k (yellow-violet-red-gold)
( ) R74 4.7k (yellow-violet-red-gold)
( ) R75 4.7k (yellow-violet-red-gold)
( ) R86 4.7k (yellow-violet-red-gold)

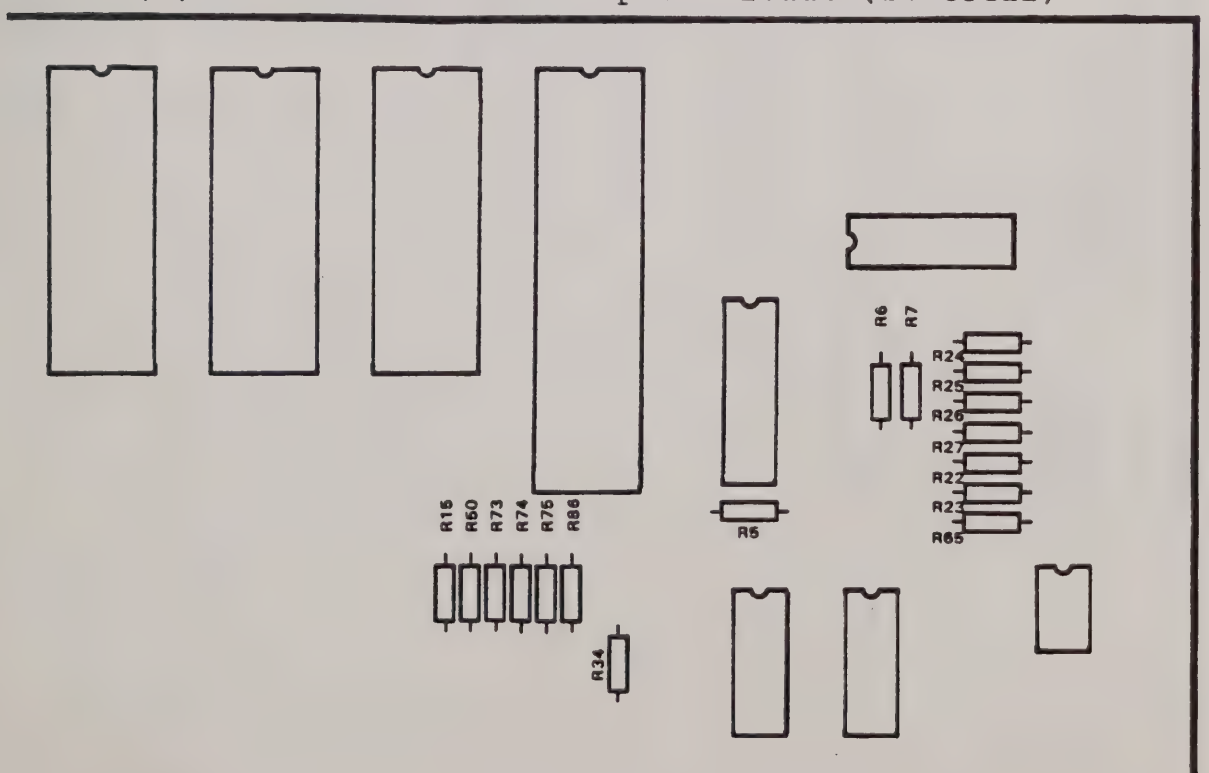
( ) Solder and clip the leads (12 total).

( ) R34 4.7k (yellow-violet-red-gold)
( ) R5 10k (brown-black-orange-gold)
( ) R6 10k (brown-black-orange-gold)
( ) R7 10k (brown-black-orange-gold)

( ) Solder and clip the leads (8 total).

( ) R24 330 (orange-orange-brown-gold)
( ) R25 330 (orange-orange-brown-gold)
( ) R26 330 (orange-orange-brown-gold)
( ) R27 330 (orange-orange-brown-gold)
( ) R22 330 (orange-orange-brown-gold)
( ) R23 330 (orange-orange-brown-gold)
( ) R65 1k (brown-black-red-gold)

( ) Solder and clip the leads (14 total).



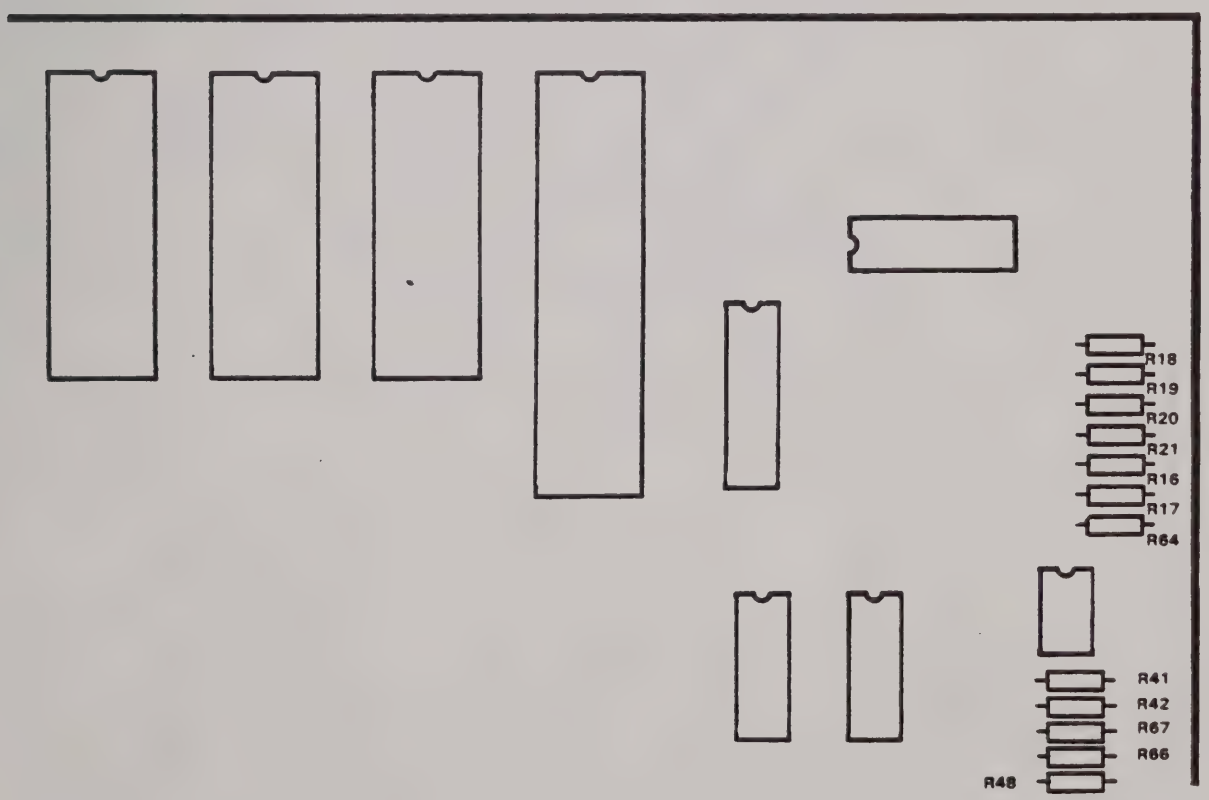
(quadrant 2 - continued)

( ) R18 330 (orange-orange-brown-gold)
( ) R19 330 (orange-orange-brown-gold)
( ) R20 330 (orange-orange-brown-gold)
( ) R21 330 (orange-orange-brown-gold)
( ) R16 330 (orange-orange-brown-gold)
( ) R17 330 (orange-orange-brown-gold)
( ) R64 1k (brown-black-red-gold)

( ) Solder and clip the leads (14 total).

( ) R41 30k (orange-black-orange-gold)
( ) R42 1k (brown-black-red-gold)
( ) R67 3.3k (orange-orange-red-gold)
( ) R66 68k (blue-grey-orange-gold)
( ) R48 470k (yellow-violet-yellow-gold)

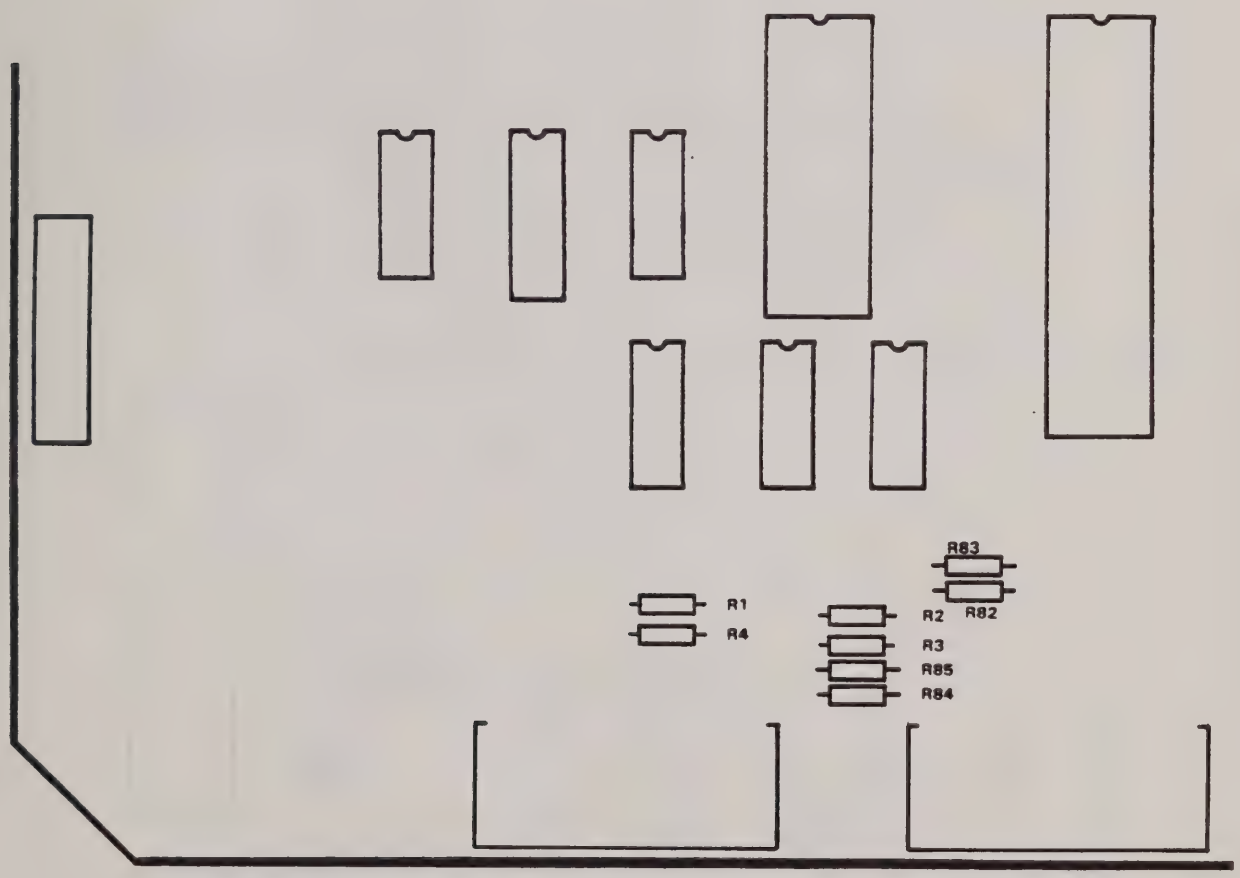
( ) Solder and clip the leads (10 total).



(quadrant 3)

( )	R1	33 k (orange-orange-orange-gold)
( )	R4	33 k (orange-orange-orange-gold)
( )	R2	6.8k (blue-grey-red-gold)
( )	R3	33 k (orange-orange-orange-gold)
( )	R85	10 k (brown-black-orange-gold)
( )	R84	10 k (brown-black-orange-gold)
( )	R83	10 k (brown-black-orange-gold)
( )	R82	10 k (brown-black-orange-gold)

( ) Solder and clip the leads (16 total).



(quadrant 4)

( ) R76 680 (blue-grey-brown-gold)
( ) R77 680 (blue-grey-brown-gold)
( ) R79 680 (blue-grey-brown-gold)
( ) R78 680 (blue-grey-brown-gold)

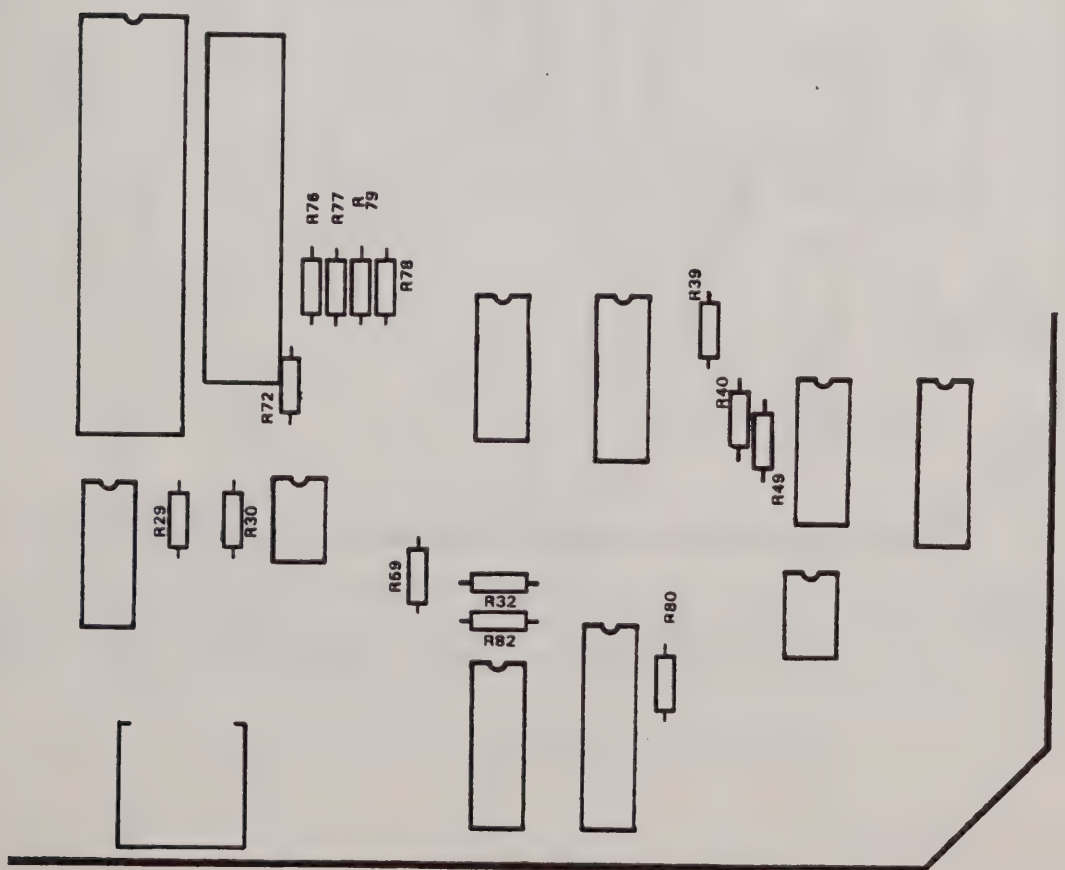
( ) Solder and clip the leads (8 total).

( ) R72 4.7k (yellow-violet-red-gold)
( ) R39 200 (red-black-brown-gold)
( ) R40 10k (brown-black-orange-gold)
( ) R49 10k (brown-black-orange-gold)

( ) Solder and clip the leads (8 total).

( ) R29 4.7k (yellow-violet-red-gold)
( ) R30 10k (brown-black-orange-gold)
( ) R59 560 -- not installed at this time.
(normally not needed, see page 2-3)
( ) R32 4.7k (yellow-violet-red-gold)
( ) R82 100k (brown-black-yellow-gold)
( ) R80 1k (brown-black-red-gold)

( ) Solder and clip the leads (10 or 12 total).



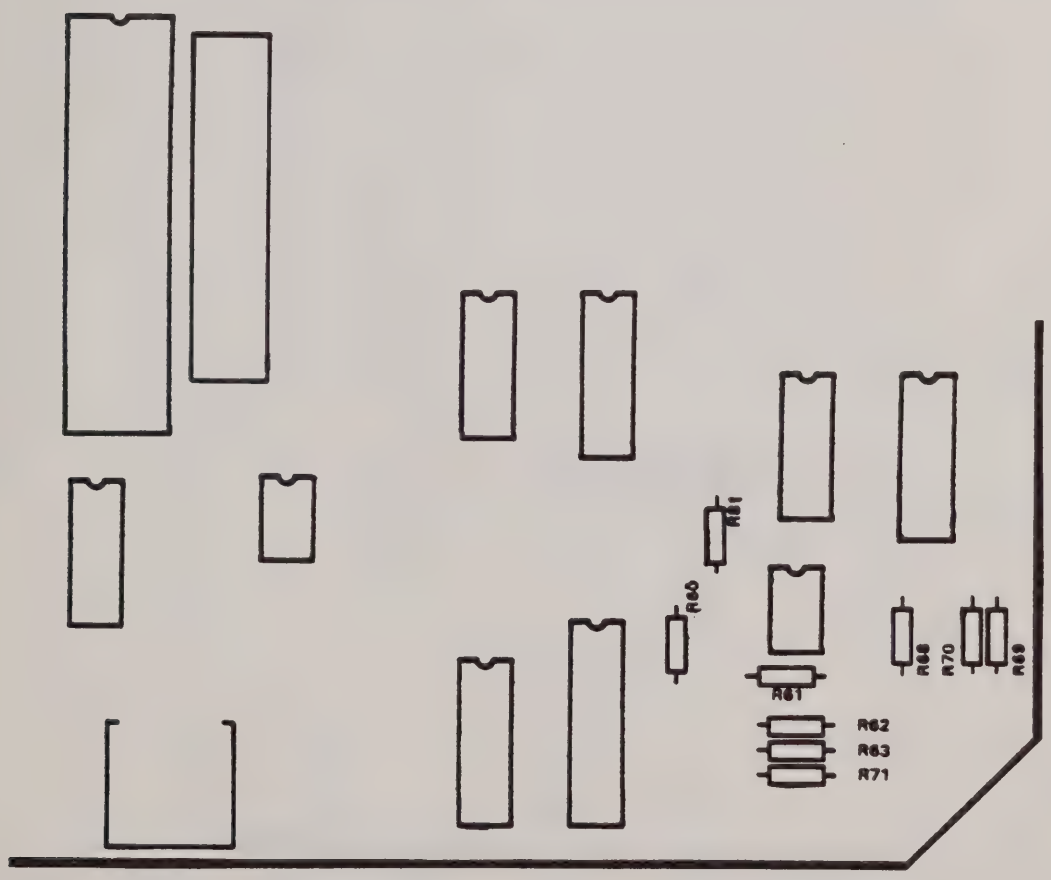
(quadrant 4 - continued)

( ) R60 10k (brown-black-orange-gold)
( ) R81 680 (blue-grey-brown-gold)
( ) R61 10k (brown-black-orange-gold)
( ) R68 100k (brown-black-yellow-gold)
( ) R70 100k (brown-black-yellow-gold)
( ) R69 10k (brown-black-orange-gold)

( ) Solder and clip the leads (12 total).

( ) R62 39k (orange-white-orange-gold)
( ) R63 39k (orange-white-orange-gold)
( ) R71 1k (brown-black-red-gold)

( ) Solder and clip the leads (6 total).



There should be three 5% resistors remaining, one 510k (green-brown-yellow-gold), one 100k (brown-black-yellow-gold), and one 560 ohm (green-blue-brown-gold).

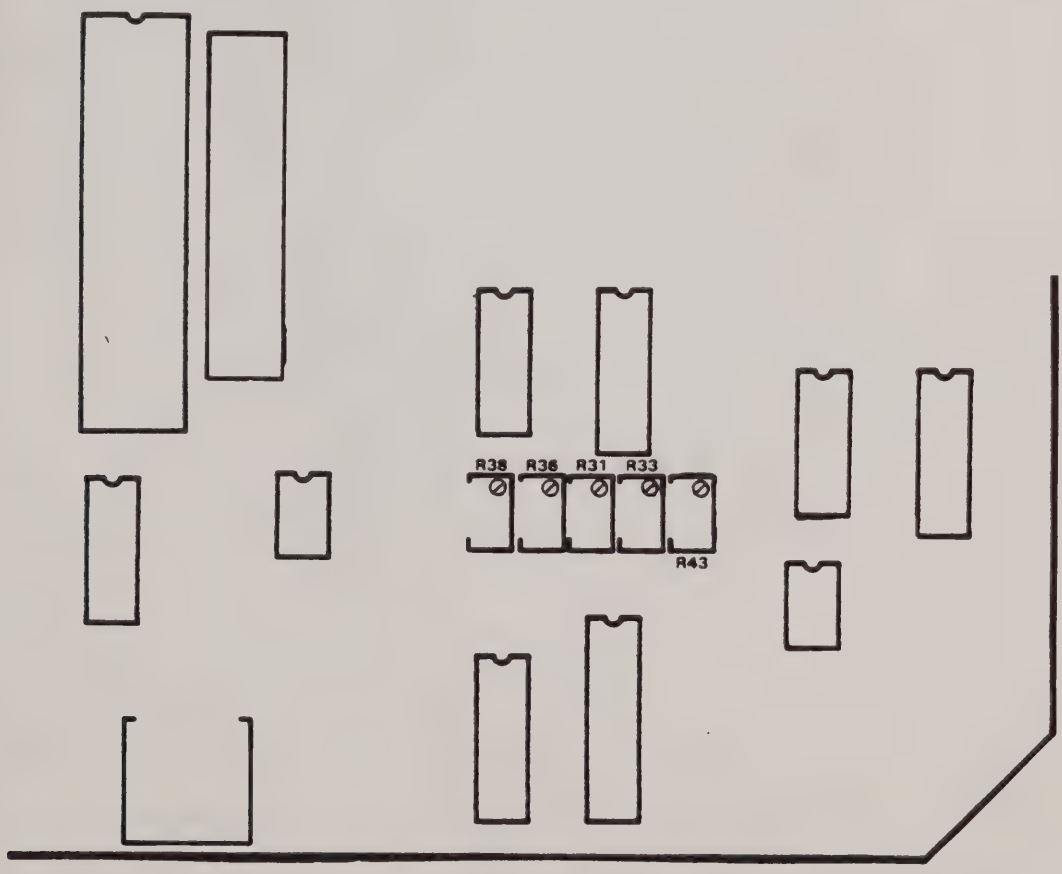
( ) Three 5% resistors remaining.

The following devices are trimpots. When you install one, tack solder the center lead to the PC board. Do not solder the other leads, nor clip any leads, until directed. This enables you to line up all the trimpots neatly before you commit any of them to a "final" position.

NOTE: Orient each trimpot with the adjustment screw towards the top of the board.

( ) R38 20k
( ) R36 10k
( ) R31 100k
( ) R33 50k
( ) R43 10k

( ) Solder and clip the leads (15 total).



Now check your work. All leads should be soldered and clipped close to the bottom of the PC board. You should have all thirteen 1% and three 5% resistors remaining. There should be no solder bridges nor cold solder connections.

( ) OK so far.

Diodes

NOTE: LEDs are polarity sensitive! The flat side of the LEDs (cathode) should match the flat side of the silk screen outline.

NOTE: The LED cathode lead is the shorter of the two.

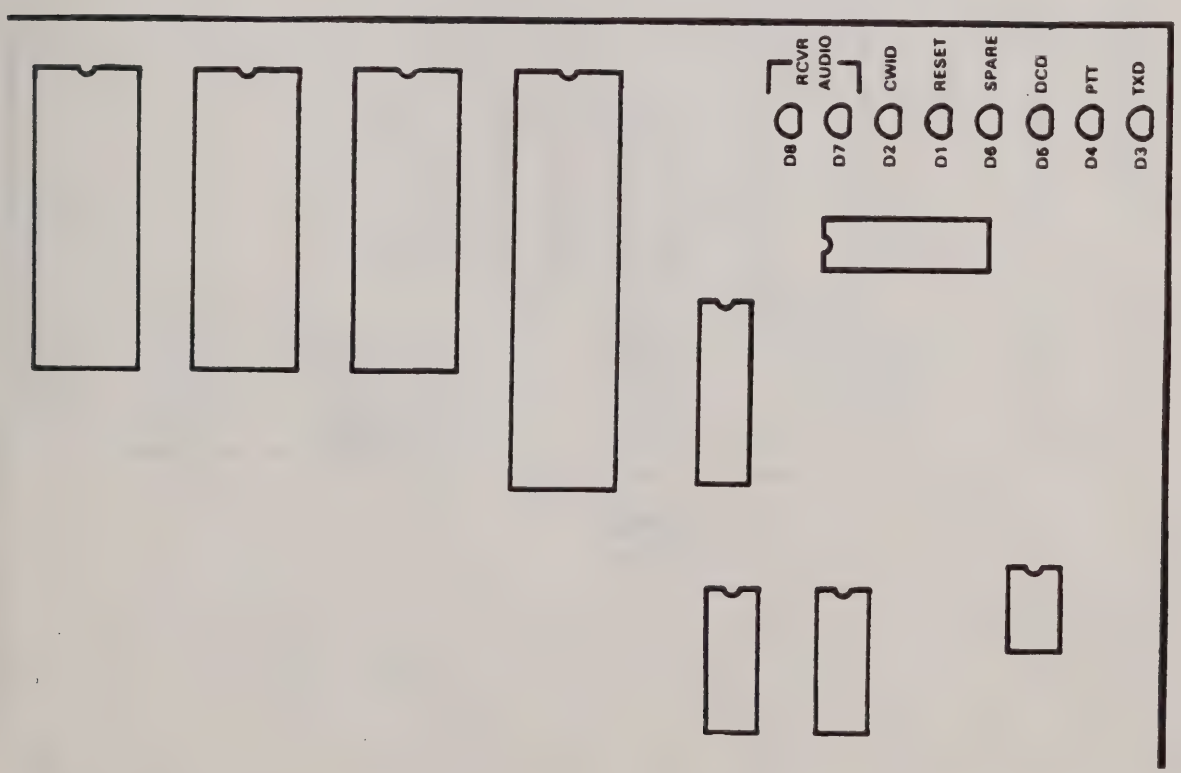
WARNING: When installing LEDS, do not bend the leads to secure the LED to the board. Bending the leads can fracture the plastic casing and cause the device to fail. Instead, hold the LED while soldering it.

(quadrant 1) -- (none)

(quadrant 2)

NOTE: All LEDs are oriented with the flat (cathode) to the right.

( ) D8 LED, red
( ) Solder and clip the leads (2 total).
( ) D7 LED, red
( ) Solder and clip the leads (2 total).
( ) D2 LED, red
( ) Solder and clip the leads (2 total).
( ) D1 LED, red
( ) Solder and clip the leads (2 total).
( ) D6 LED, red
( ) Solder and clip the leads (2 total).
( ) D5 LED, red
( ) Solder and clip the leads (2 total).
( ) D4 LED, red
( ) Solder and clip the leads (2 total).
( ) D3 LED, red
( ) Solder and clip the leads (2 total).



WARNING: Diodes are polarity sensitive! The marked (banded or tapered) end of the diodes (cathode) should match the banded end of the silk screen.

(quadrant 3)

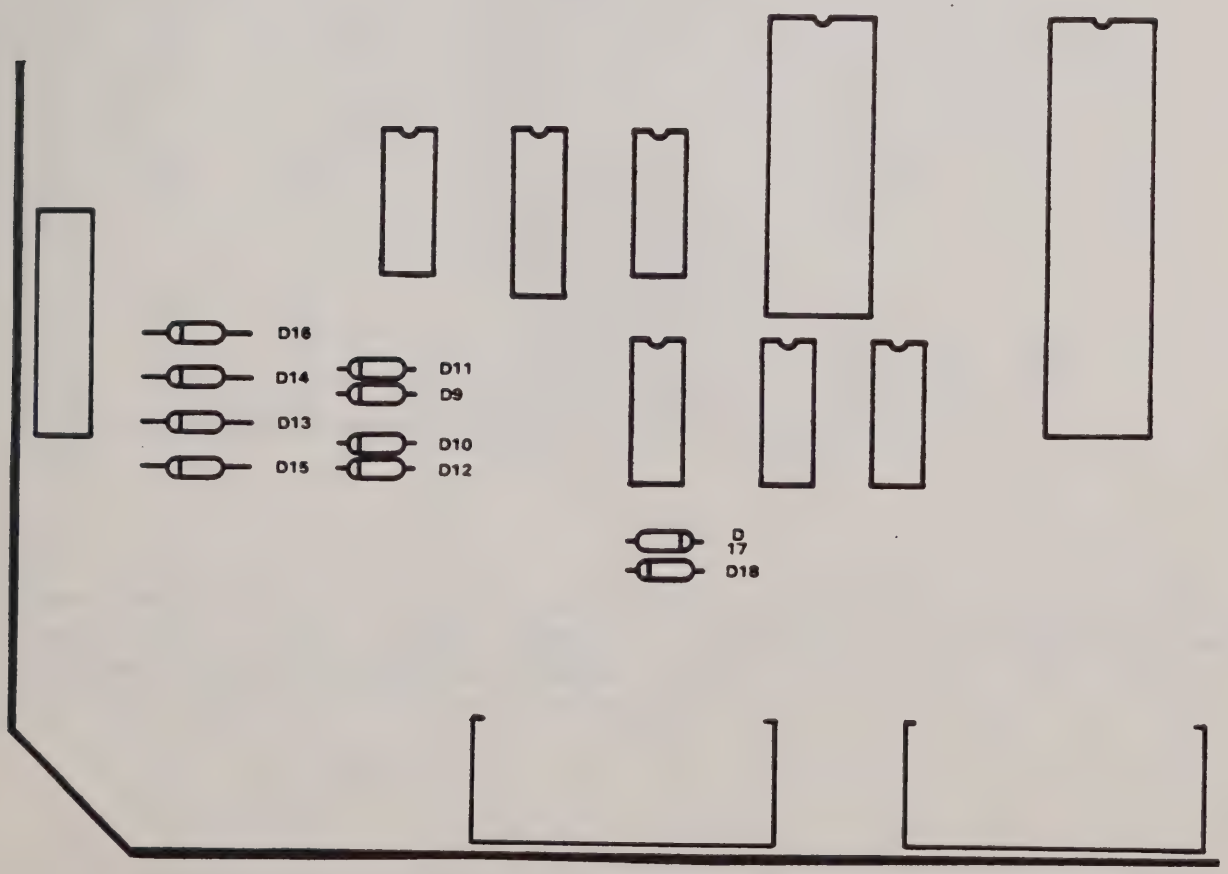
NOTE: The next 4 diodes should be placed so they are about 1/8 inch above the PC board surface.

( ) D16 1N5400
( ) D14 1N5400
( ) D13 1N5400
( ) D15 1N5400

( ) Solder and clip the leads (8 total).

( ) D11 1N4001
( ) D9 1N4001
( ) D10 1N4001
( ) D12 1N4001
( ) D17 1N4001
( ) D18 1N4001

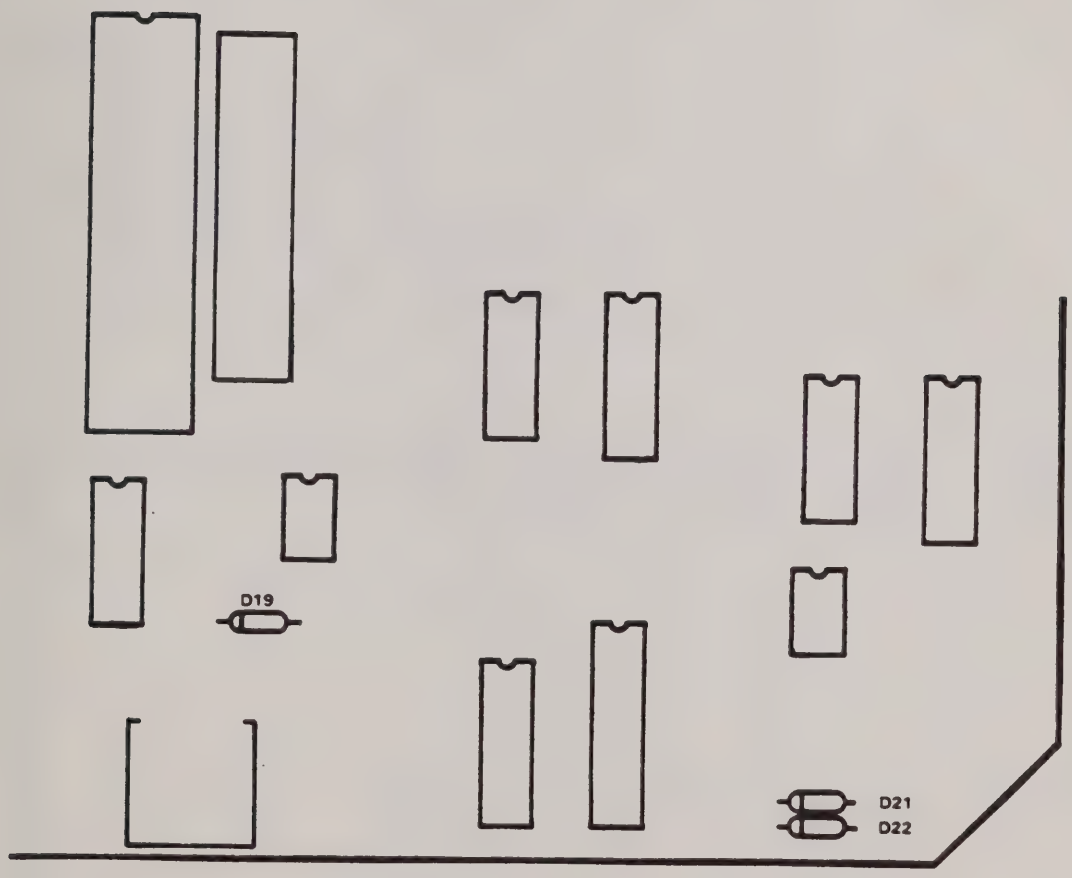
( ) Solder and clip the leads (12 total).



(quadrant 4)

( ) D19 1N4752
( ) D21 1N4148
( ) D22 1N4148

( ) Solder and clip the leads (6 total).



Now check your work. You should have no diodes nor LEDs remaining. All leads should be soldered and clipped close to the bottom of the PC board. There should be no solder bridges nor cold solder connections.

( ) OK so far.

Capacitors

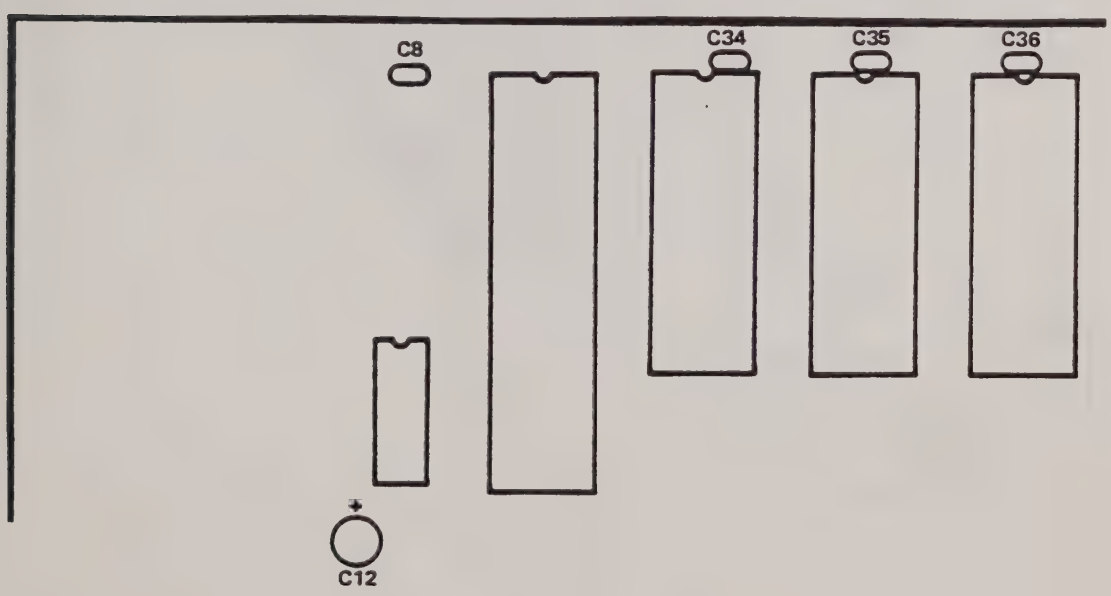
WARNING: The electrolytic capacitors are polarity sensitive. The silk screen indicates the positive lead, while most electrolytic capacitors have the negative lead marked. Be careful to ensure the parts are correctly installed or damage may occur at power up!

NOTE: Inspect your Ø.1 uF disc capacitors and choose one that has a thin profile (there may be several). Save it until specifically called for in the assembly instructions! It will be installed in a tight place on the board.

(quadrant 1)

( ) C8 Ø.1 uF
( ) C34 Ø.1 uF
( ) C35 Ø.1 uF
( ) C36 Ø.1 uF
( ) C12 1ØØ uF electrolytic

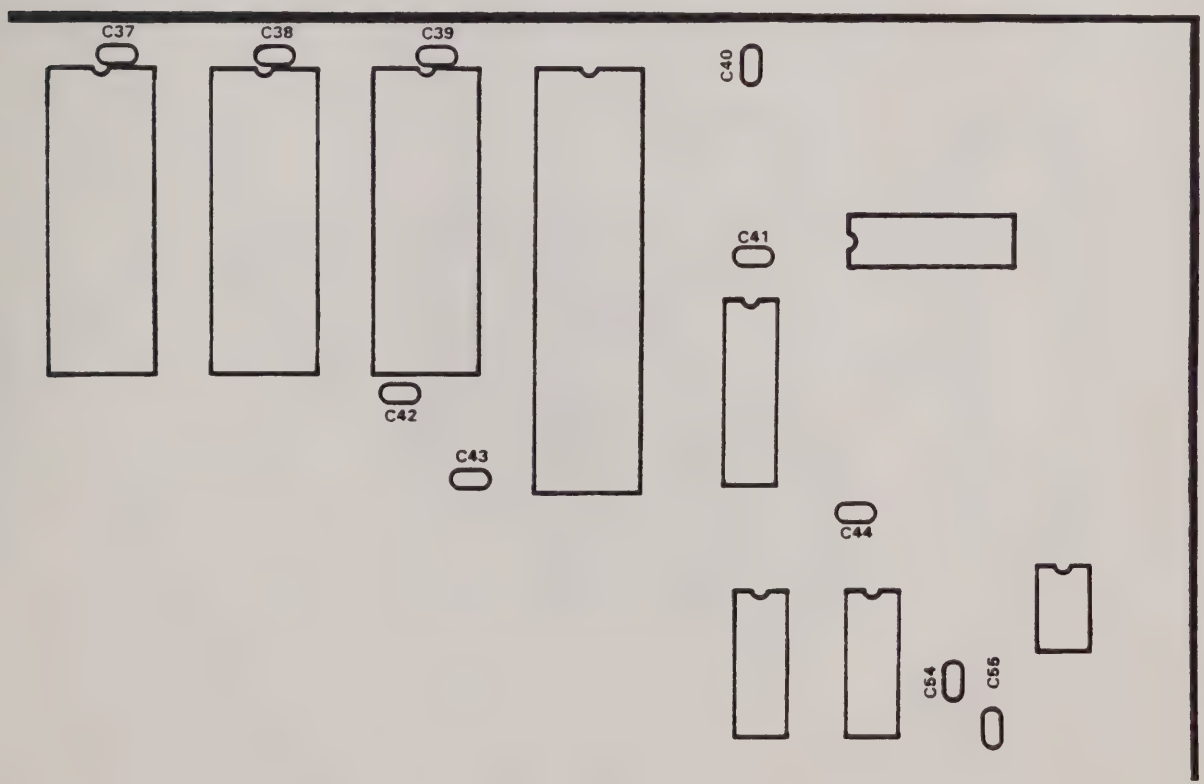
( ) Solder and clip the leads (1Ø total).



(quadrant 2)

( ) C37 Ø.1 uF
( ) C38 Ø.1 uF
( ) C39 Ø.1 uF
( ) C4Ø Ø.1 uF
( ) C41 Ø.1 uF
( ) C42 Ø.1 uF
( ) C43 Ø.1 uF
( ) C44 Ø.1 uF
( ) C54 Ø.1 uF
( ) C55 Ø.1 uF

( ) Solder and clip the leads (2Ø total).



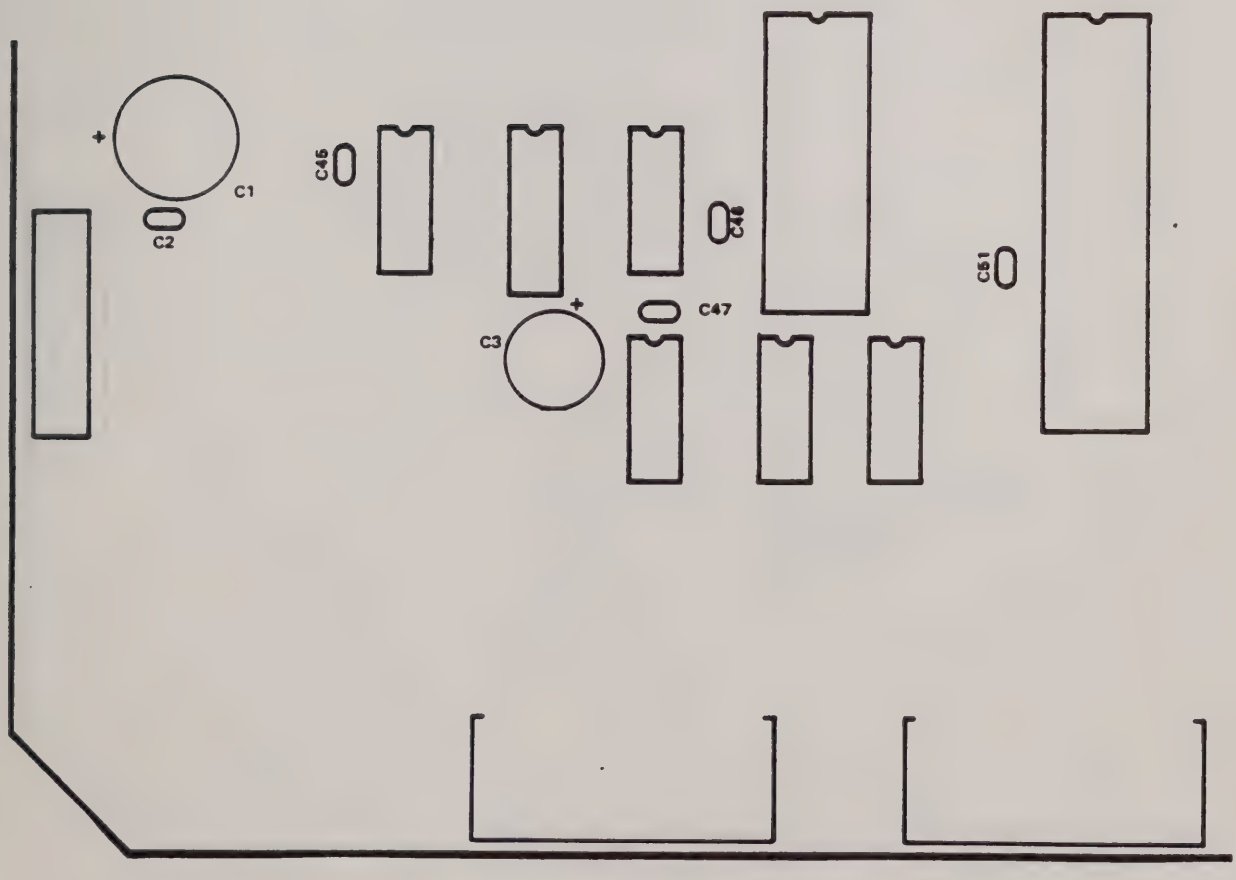
(quadrant 3)

( ) C1 2200 uF electrolytic
( ) C2 0.1 uF
( ) C45 0.1 uF

NOTE: In the following step, the + side of C3 goes towards the top of the board.

( ) C3 470 uF electrolytic
( ) C47 0.1 uF
( ) C46 0.1 uF
( ) C51 0.1 uF

( ) Solder and clip the leads (14 total).



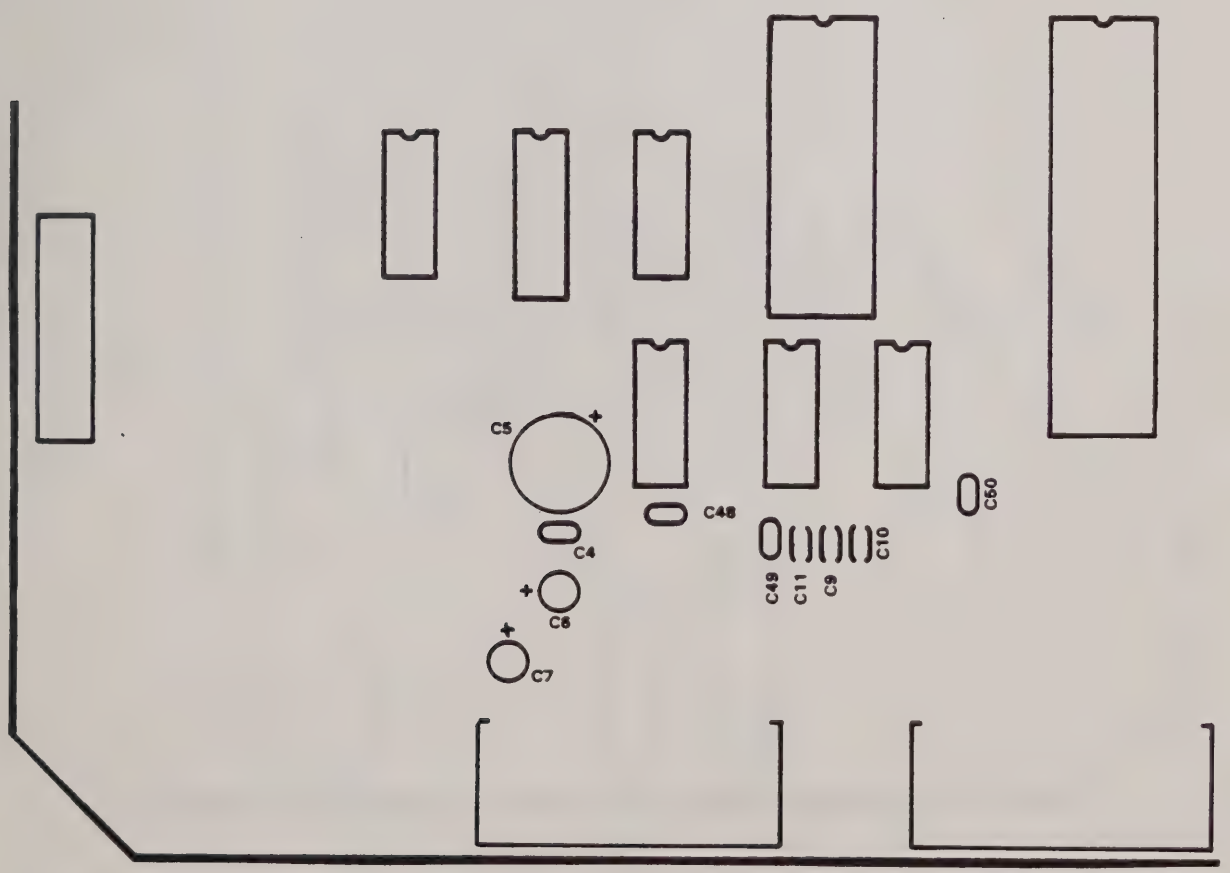
(quadrant 3 - continued)

( ) C5 470 uF electrolytic
( ) C4 Ø.1 uF
( ) C6 1Ø uF electrolytic
( ) C7 1Ø uF electrolytic
( ) C48 Ø.1 uF

( ) Solder and clip the leads (1Ø total).

( ) C49 Ø.1 uF
( ) C11 33Ø pF
( ) C9 33Ø pF
( ) C1Ø 33Ø pF
( ) C5Ø Ø.1 uF

( ) Solder and clip the leads (1Ø total).



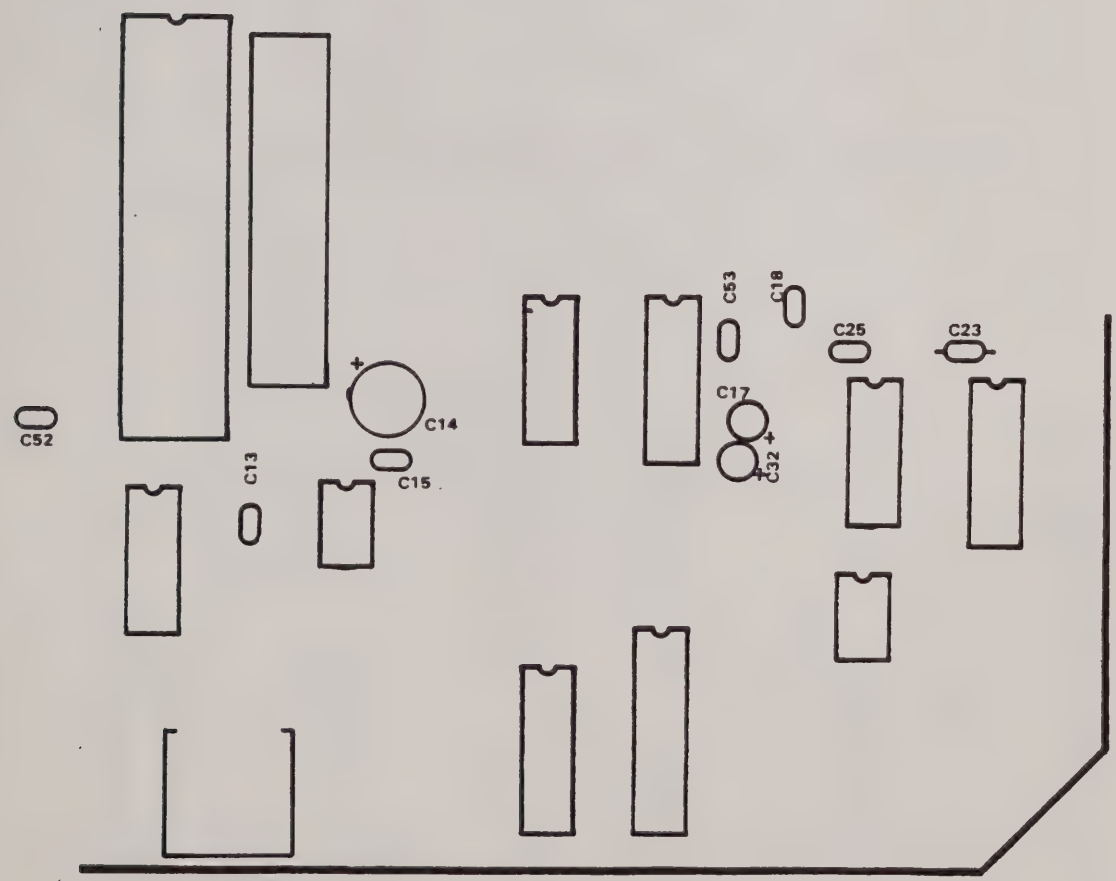
(quadrant 4)

( ) C52 Ø.1 uF
( ) C13 Ø.Ø1 uF
( ) C14 33Ø uF electrolytic
( ) C15 Ø.Ø1 uF

( ) Solder and clip the leads (8 total).

( ) C53 Ø.1 uF
( ) C32 1Ø uF electrolytic
( ) C17 1 uF electrolytic
( ) C18 Ø.Ø5 uF
( ) C25 Ø.1 uF
( ) C23 Ø.15 uF

( ) Solder and clip the leades (12 total).



(quadrant 4 - continued)

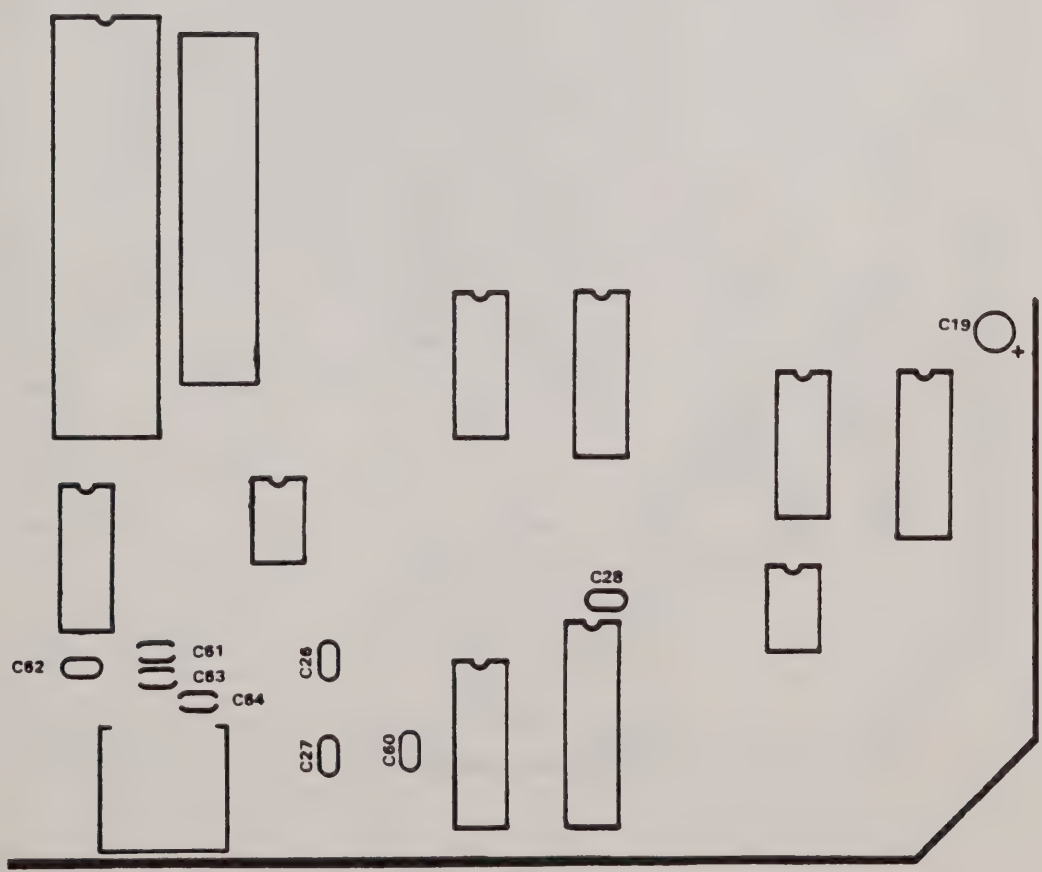
The next capacitor, C19, is the mic audio coupling capacitor. If you will use the TNC with a low impedance microphone input on your radio (such as 600 ohms), use the 1 uF part. If you will be using a radio with a high impedance microphone input, use the 0.1 uF capacitor. If you are not sure, or plan on using both, use the 1 uF device. Holes are provided for both capacitor styles.

( ) C19 1 uF electrolytic
or 0.1 uF

( ) Solder and clip the leads (2 total).

( ) C62 0.1 uF
( ) C61 0.01 uF
( ) C63 0.01 uF
( ) C64 0.01 uF
( ) C26 0.1 uF
( ) C27 0.1 uF
( ) C60 0.1 uF
( ) C28 0.05 uF

( ) Solder and clip the leads (16 total).



(quadrant 4 - continued)

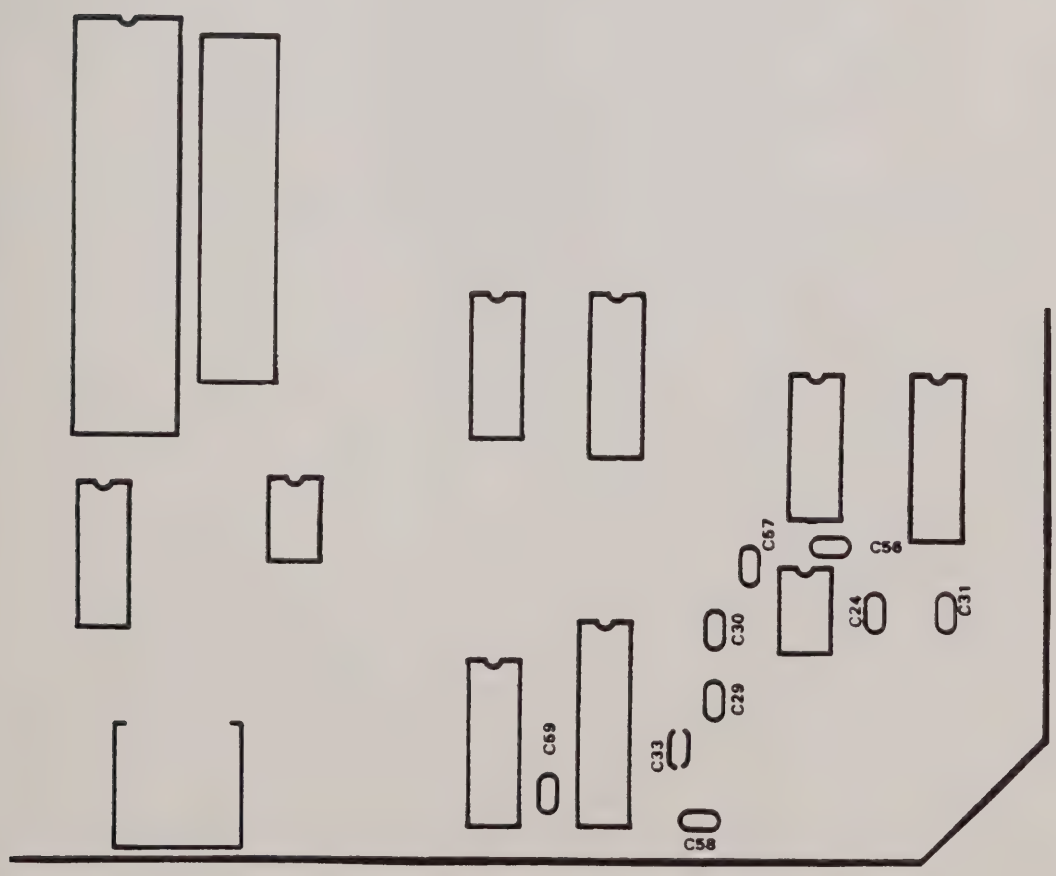
( ) C30 0.0015 uF
( ) C57 0.1 uF
( ) C56 0.1 uF
( ) C24 0.1 uF
( ) C31 0.01 uF
( ) C29 0.0015 uF
( ) C33 0.01 uF
( ) C58 0.1 uF

( ) Solder and clip the leads (16 total).

The selected "thin" capacitor should be installed in the next step.

( ) C59 0.1 uF

( ) Solder and clip the leads (2 total).



You should have 5 capacitors remaining (two Ø.Ø22 uF monolithic, one Ø.ØØ47 uF monolithic, one Ø.ØØ22 uF monolithic and one of either Ø.1 uF or 1 uF left over from step C19).

( ) 5 capacitors remaining.

Now check your work. All leads should be soldered and clipped close to the bottom of the PC board. There should be no solder bridges nor cold solder connections.

( ) OK so far.

Miscellaneous Components

NOTE: In the following steps you will complete soldering parts on the TAPR TNC printed circuit board. Solder each component as you install it in this assembly phase. Be sure to proceed carefully and continue to exercise good workmanship!

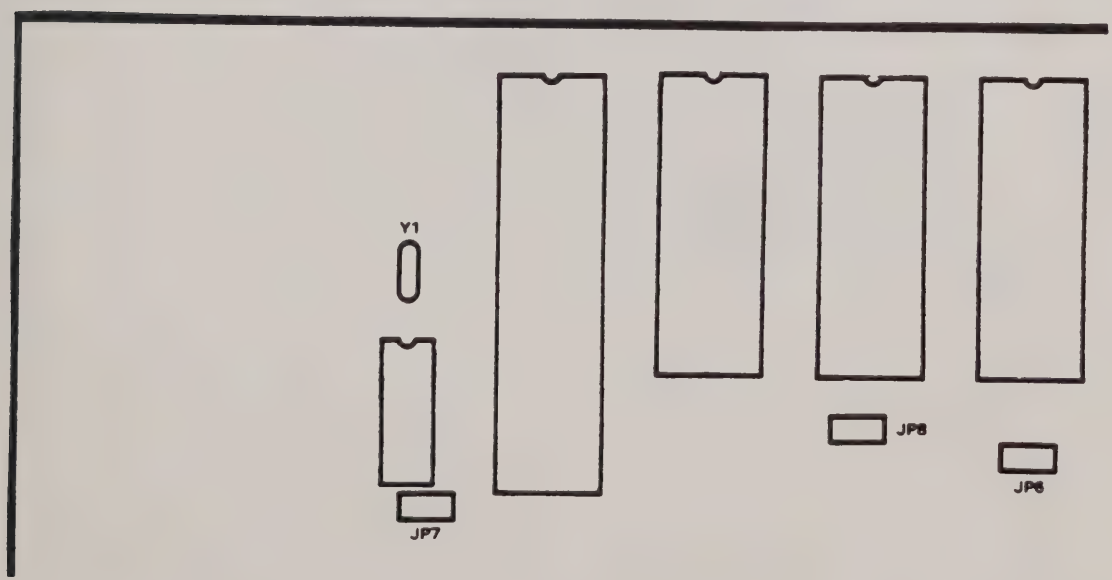
(quadrant 1)

( ) Y1 7.3728 MHz crystal (Do not overheat!)

NOTE: When installing jumper strips, be sure that the shorter pins are soldered to the PC board and the longer pins stick up.

WARNING: Don't grip jumpers with your fingers while soldering. The pins quickly get very hot!

( ) JP7 3-pin jumper strip
( ) JP8 3-pin jumper strip
( ) JP6 3-pin jumper strip

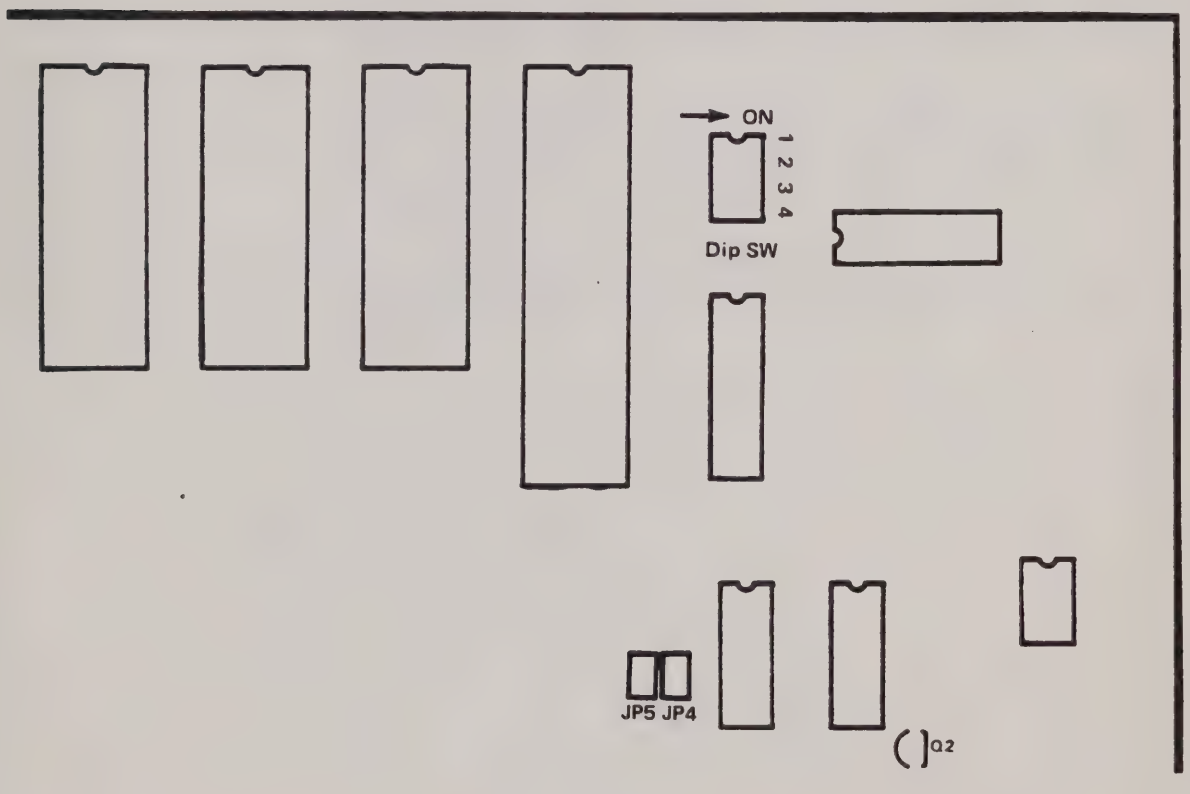


(quadrant 2)

( ) SW DIP switch. Install this part with the "on" positions to the right.
( ) JP5 2-pin jumper strip
( ) JP4 2-pin jumper strip

WARNING: Q2 is static sensitive. Exercise proper handling procedures.

( ) Q2 VN1ØKMA. Install this transistor with the flat side towards the right.



(quadrant 3)

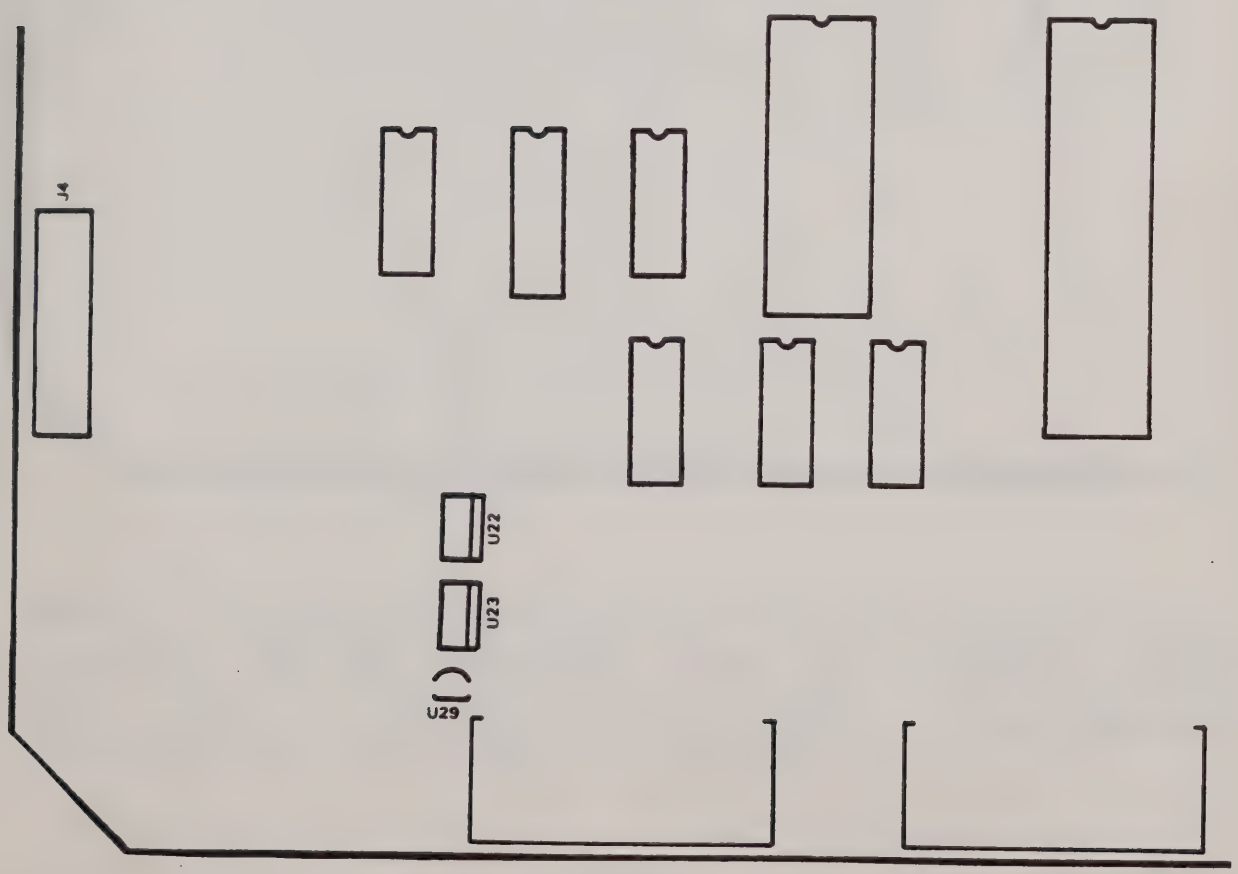
NOTE: The "shoulders" on the leads of the regulators in the next two steps may be of different lengths. The result is that, if you seat the parts against the top board surface, they will stand at different heights. For best appearance, they should stand at the same height.

( ) U22 7812. Mount with the plastic body of the part facing the left edge of the board.

( ) U23 7912. Mount with the plastic body of the part facing the left edge of the board.

( ) U29 79L05. Mount with the flat of the body (if the part is "round") or the tapered portion (if the part is rectangular) facing the lower edge of the board.

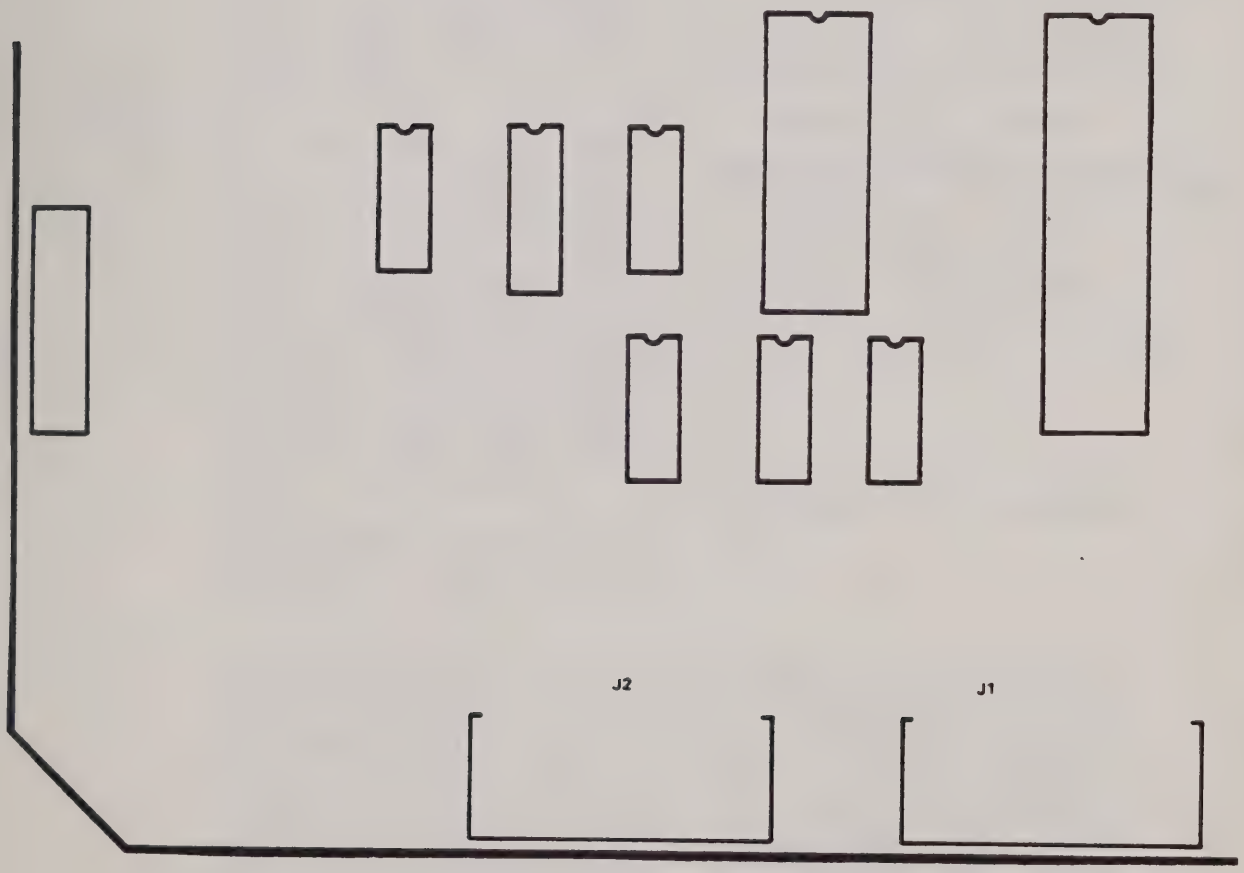
( ) J4 7-pin Power Connector. Install with the "wall" to the right (away from the edge of the board).



(quadrant 3 - continued)

NOTE: The two pc mount right angle connectors will be installed in the next two steps. One is a socket (female) and the other is a plug (male). The "sex" of the connector refers to the contacts, not the plastic shell. Be certain you install them in their correct locations!

( ) J2 25-pin FEMALE DB-25 connector.
( ) J1 25-pin MALE DB-25 connector.



NOTE: If you intend to use the TAPR recommended case for your TNC, skip the following 5 steps! If you elect to mount your TNC in a case, be alert to the dangers of overheating U24 if it is mounted on the board without adequate ventilation or other means of removing the heat.

(quadrant 3 - continued)

( ) U24 LM309K. (See Fig. F.4.) If you are going to mount the main 5 volt regulator on the PC board, place it on the heat sink and insert the two 6-32 x 5/8 machine screws through the mounting holes in the case of U24 and the corresponding holes in the heat sink.

( ) Be sure the two pins of the IC are centered in the holes in the heat sink. The hole locations are not symmetrical and severe damage to the TNC may result if U24 is improperly oriented in this and the next three steps!

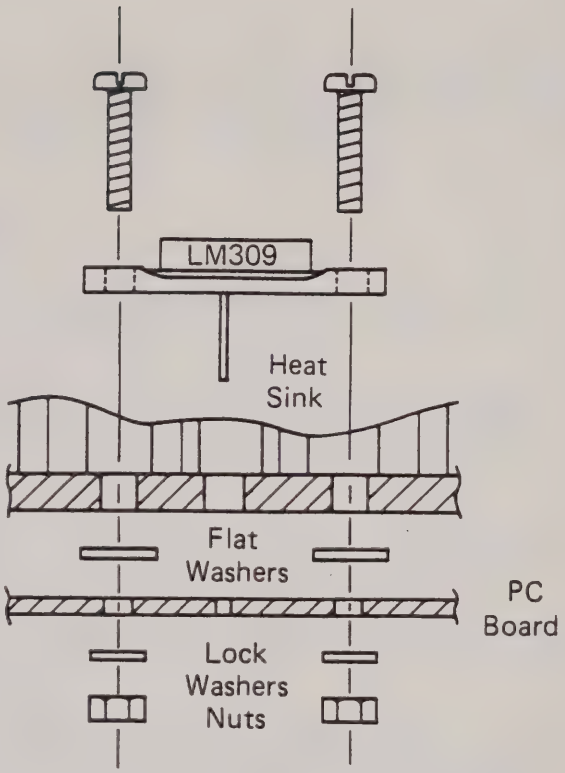


Fig. F.4 U24 Assembly

( ) Now, place the screws through the flat washers.

( ) Seat the heat sink/U24 assembly over the provided holes in the lower left corner of the PC board. The flat washers will be between the heat sink and the board. Attach the assembly to the PC board with 6-32 lock washers and 6-32 nuts. Tighten snugly, as the screws are a power connection in addition to being mechanical fasteners.

( ) Solder the two pins from U24 to the PC board and clip the leads.

(quadrant 4)

( ) J3 9-pin female DE-9 connector. Fasten this connector in place with two 6-32 x 3/8 screws, #6 lockwashers and 6-32 nuts.

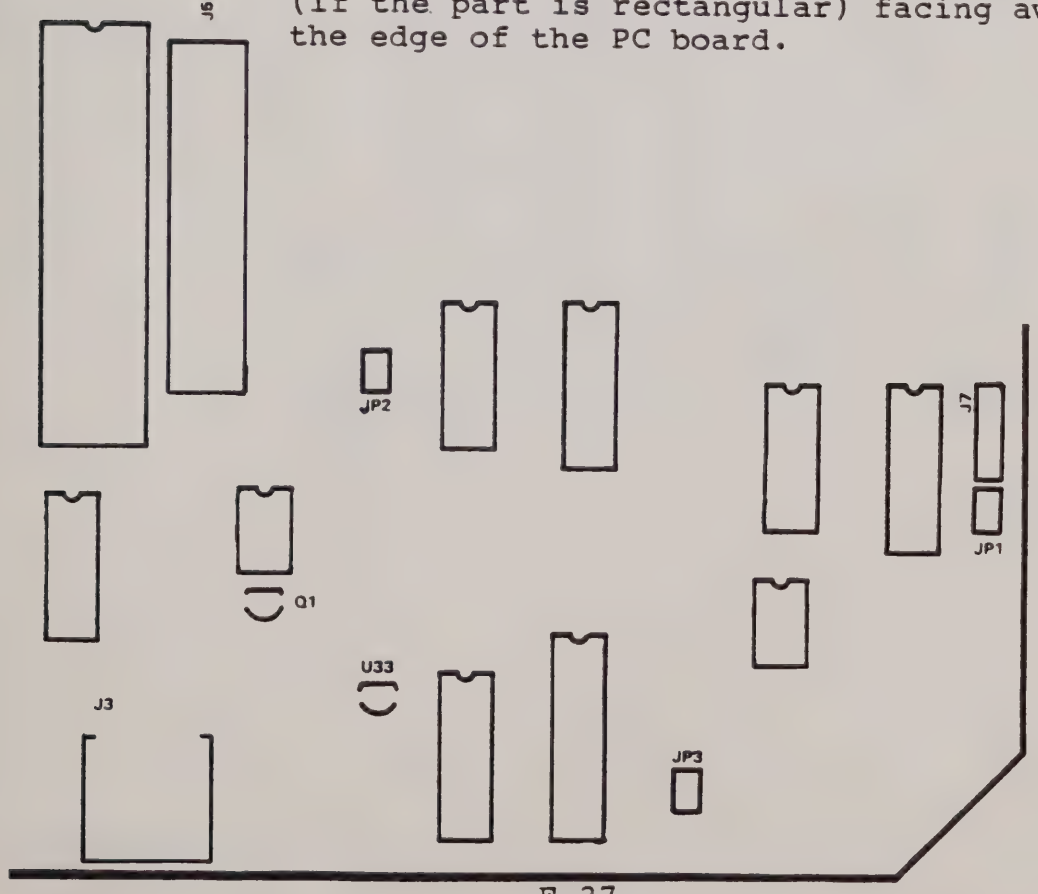
( ) JP2 2-pin jumper strip
( ) JP3 2-pin jumper strip
( ) JP1 2-pin jumper strip
( ) J7 5-pin male strip connector. Orient this connector with the wall nearest U34.

( ) J5 20-pin male header with ejector/latches. Orient this part so the notched wall is nearest U17.

WARNING: Q1 is static sensitive. Exercise proper handling procedures.

( ) Q1 VN10KMA. Install this transistor with the flat side nearest U26.

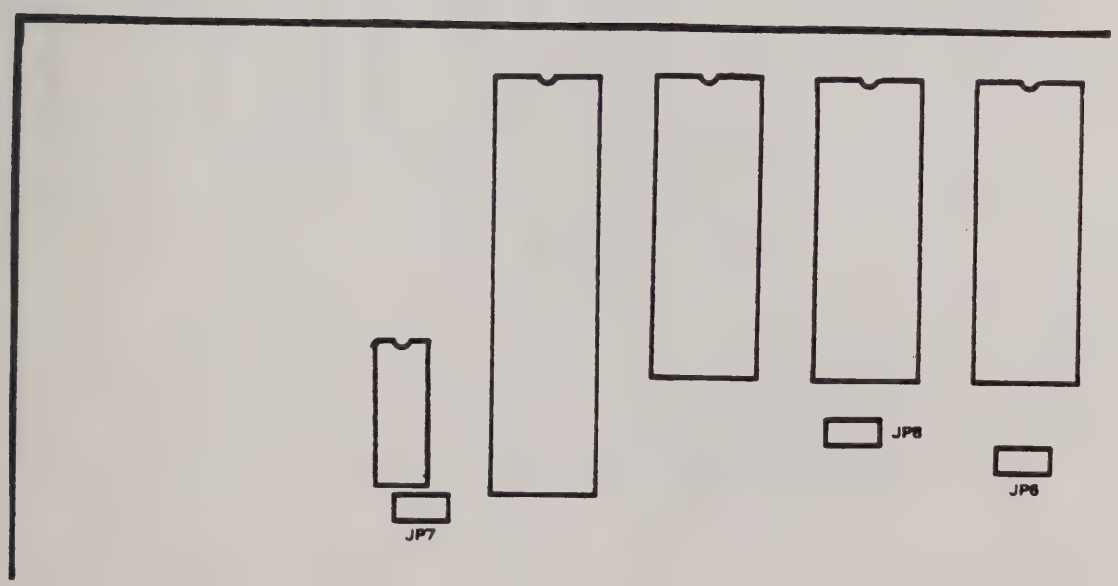
( ) U33 78L05. Mount with the flat of the body (if the part is "round") or the tapered portion (if the part is rectangular) facing away from the edge of the PC board.



In the following steps you will install the push-on jumpers in their default positions. They are installed by simply pressing down over the indicated pins. DO NOT solder them in place!

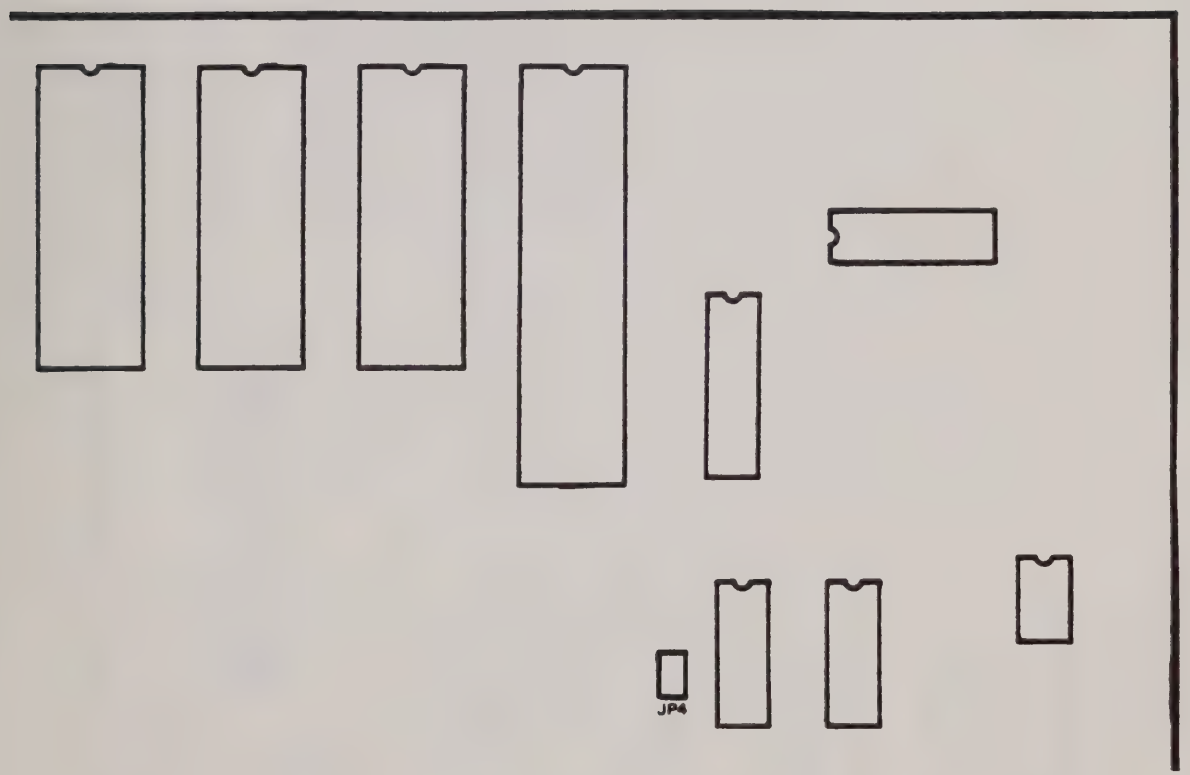
(quadrant 1)

( ) JP7 place over the center and right pins.
( ) JP8 place over the center and left pins.
( ) JP6 place over the center and left pins.



(quadrant 2)

( ) JP4



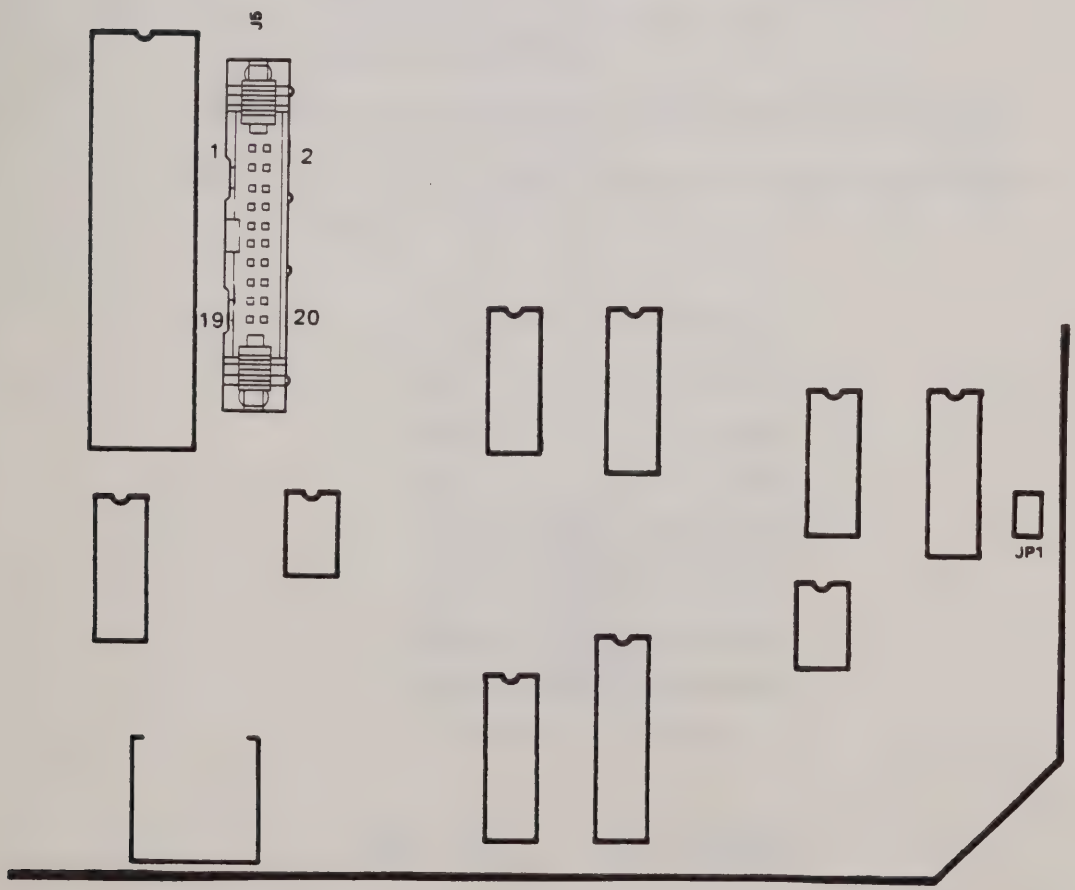
(quadrant 3) -- (none)

(quadrant 4)

( ) JP1

A total of 8 jumpers will be installed in header J5. This is the large 20-pin unit with the ejector/latches. The jumpers will be installed parallel to the long dimension of the board (left to right). The connector pins are numbered with pin 1 in the upper left hand corner, pin 2 in the upper right, pin 3 below pin 1, etc. Thus, the jumpers will short pins 1 and 2, or 9 and 10, etc.

( ) J5 pins 1 and 2
( ) J5 pins 5 and 6
( ) J5 pins 7 and 8
( ) J5 pins 9 and 10
( ) J5 pins 11 and 12
( ) J5 pins 13 and 14
( ) J5 pins 17 and 18
( ) J5 pins 19 and 20



Now lay the PC board aside and prepare to wire the DIP headers. These are the devices that plug into an IC socket but have notched pins above the plug to allow soldering discrete components to the header.

NOTE: DO NOT overheat the headers when soldering or they may deform!

U35

The following devices mount on a 14-pin DIP header, U35. Form the leads on the parts so the component can lay in the slots provided on the top of the header. Solder the two leads, then clip them flush with the sides of the post. Note the bevelled corner marking pin 1 of the header.

WARNING: Capacitors are easily confused due to similarity in markings. Be very sure you install the proper one.

NOTE: The resistor in the next step is 5%.

( ) R28 100k (brown-black-yellow-gold)
(pins 4 and 11)
( ) C16 0.022 uF monolithic (223)
(pins 5 and 10)
( ) R35 16.2k 1% (brown-blue-red-red-brown)
(pins 6 and 9)
( ) R37 23.7k 1% (red-orange-violet-red-brown)
(pins 7 and 8)

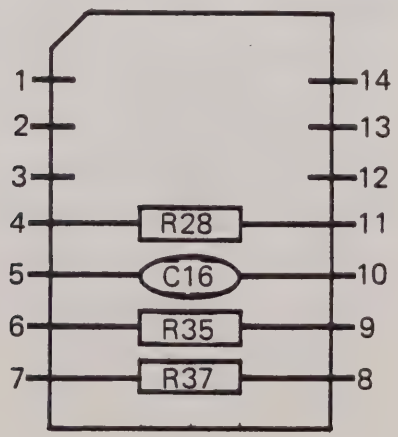


Fig. F.5 U35 Assembly

U34

The following devices mount on a 16-pin DIP header, U34. Note the bevelled corner marking pin 1 of the header.

( ) C22 Ø.Ø22 uF monolithic (223)
(pins 2 and 15)
( ) R47 22.1k 1% (red-red-brown-red-brown)
(pins 3 and 14)
( ) R46 47.Øk 1% (yellow-violet-black-red-brown)
(pins 4 and 13)
( ) C2Ø Ø.ØØ47 uF monolithic (472)
(pins 5 and 12)
( ) C21 Ø.ØØ22 uF monolithic (222)
(pins 6 and 11)
( ) R45 1ØØk 1% (brown-black-black-orange-brown)
(pins 7 and 1Ø)

NOTE: The resistor in the next step is 5%.

( ) R44 51Øk (green-brown-yellow-gold)
(pins 8 and 9)

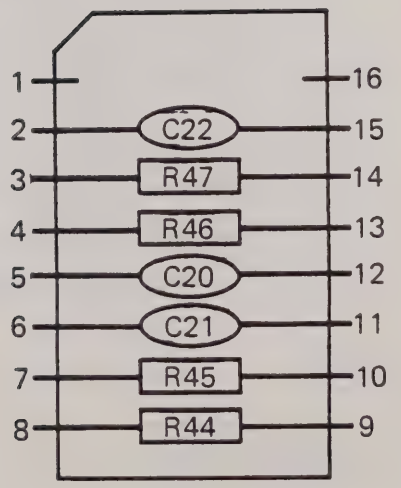


Fig. F.6 U34 Assembly

U30

The following 1% resistors mount on a 16-pin header, U30. Note the bevelled corner marking pin 1 of the header.

( ) R58 37.4k 1% (orange-violet-yellow-red-brown) (pins 1 and 16)
( ) R57 10.0k 1% (brown-black-black-red-brown) (pins 2 and 15)
( ) R56 16.2k 1% (brown-blue-red-red-brown) (pins 3 and 14)
( ) R55 10.0k 1% (brown-black-black-red-brown) (pins 4 and 13)
( ) R52 63.4k 1% (blue-orange-yellow-red-brown) (pins 5 and 12)
( ) R54 10.0k 1% (brown-black-black-red-brown) (pins 6 and 11)
( ) R53 57.6k 1% (green-violet-blue-red-brown) (pins 7 and 10)
( ) R51 10.0k 1% (brown-black-black-red-brown) (pins 8 and 9)

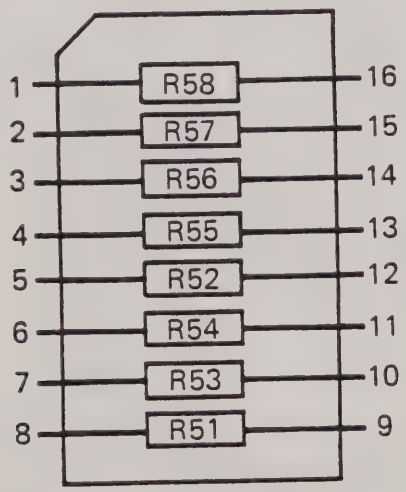


Fig. F.7 U30 Assembly

Now lay aside the three DIP headers you have just completed and prepare to wire the power transformer and connector.

Power Transformer Wiring

The TAPR TNC utilizes a custom wound power transformer for maximum efficiency and minimum bulk. Inspect the transformer carefully and note the three sets of terminal strips on it. On the side with two sets of terminals, the lower set has a black dot near one end. This is the primary terminal strip.

Of the remaining two terminal strips, one has two terminals and the other three terminals. These are the secondary terminals. The two-terminal set (the ac output to drive the main +5 volt regulator) consists of terminals 1 and 2, while the three-terminal set (for the + and - 12 volt, -5 volt and modem +5 volt regulators) are called terminals 3, 4 and 5. These will be connected to the 7-pin power connector in the following steps. (See Fig. F.8.)

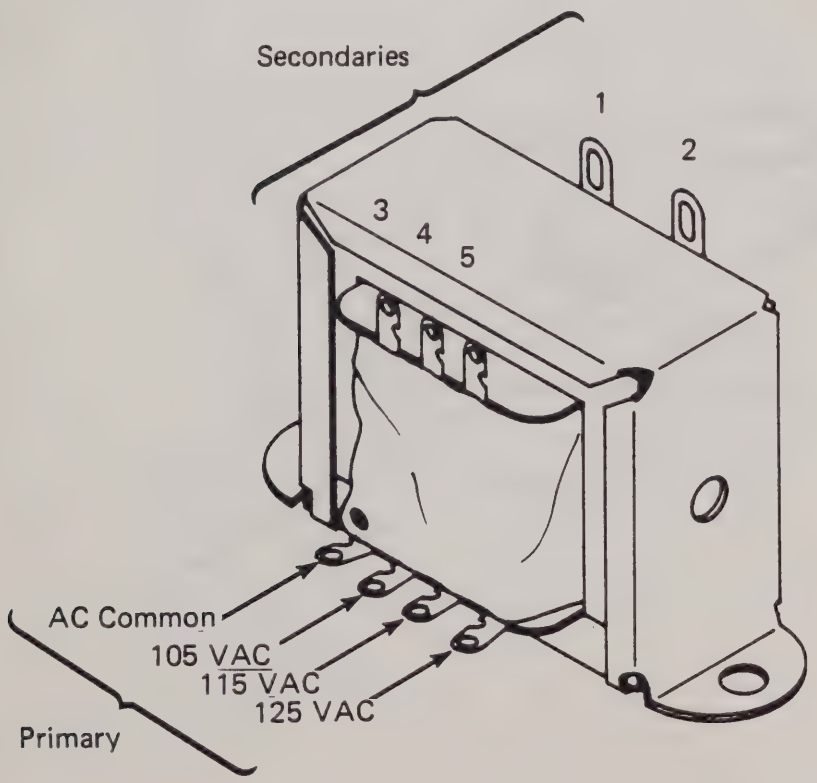


Fig. F.8 Power Transformer Terminals

Transformer Secondary Wiring

( ) Cut five 7" lengths of the supplied hookup wire.

( ) Strip 1/4" of insulation from each end of the five wires.

( ) Attach a MOLEX pin to one end of one of the wires by crimping it with long nose pliers and neatly soldering it. (See Fig. F.9.) Do not allow any solder to run up into the flexing portion of the pin or the locking tab!

( ) Similarly, attach a MOLEX pin to one end of each of the remaining four wires.

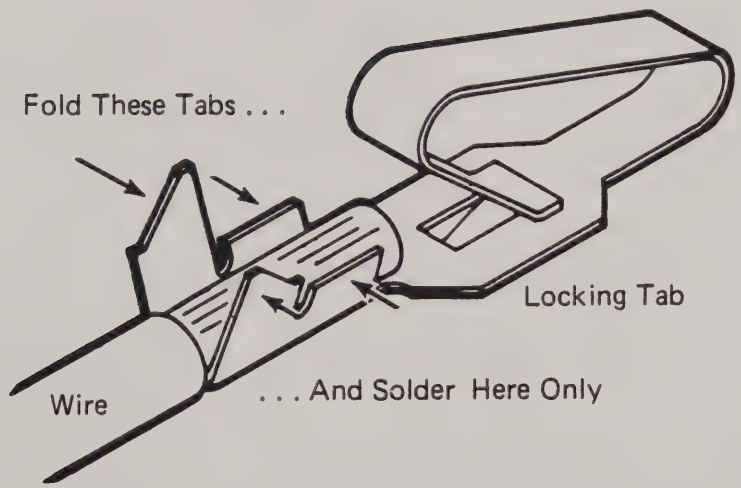


Fig. F.9 MOLEX Terminal Attachment

( ) Insert one of the wired MOLEX pins into the connector housing at pin 6 and push until the tab on the bottom of the pin clicks in place. (See Fig. F.1Ø.)

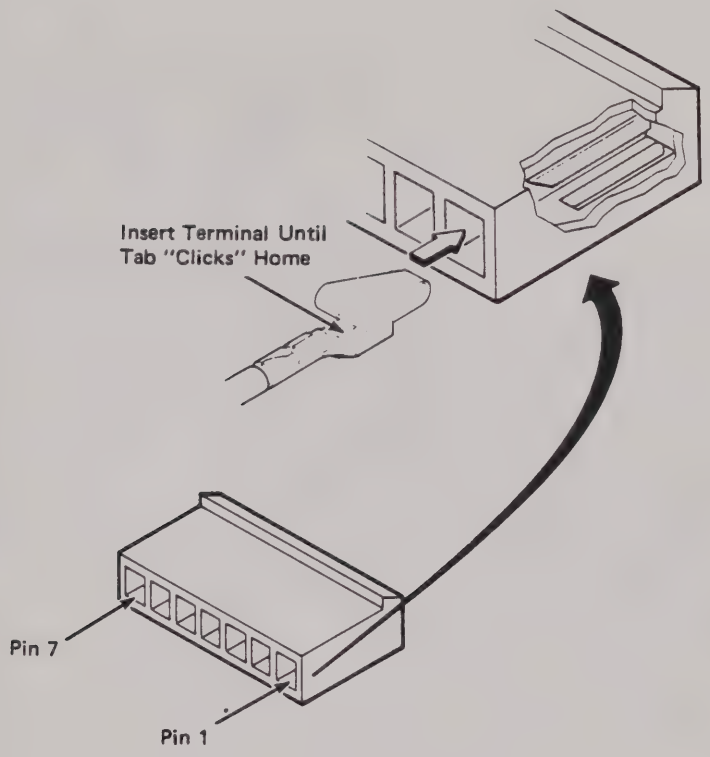


Fig. F.1Ø Power Connector Construction

( ) Solder the free end of this wire to transformer terminal 5.

( ) Insert one of the wired MOLEX pins into the connector housing at pin 5 and push until the tab locks.

( ) Solder the free end of this wire to transformer terminal 3.

( ) Insert one of the wired MOLEX pins into the connector housing at pin 4 and push until the tab locks.

( ) Solder the free end of this wire to transformer terminal 4.

( ) Insert one of the wired MOLEX pins into the connector housing at pin 2 and push until the tab locks.

( ) Solder the free end of this wire to transformer terminal 1.

( ) Insert the remaining wired MOLEX pin into the connector housing at pin 1 and push until the tab locks.

( ) Solder the free end of this wire to transformer terminal 2.

You are now done with wiring the transformer secondary. If you have elected to mount the +5 volt regulator U24 off board, and if you are not using the TAPR recommended cabinet kit, complete the following steps.

If you have purchased the TAPR TNC cabinet kit, proceed to the instructions supplied with it.

NOTE: The +5 volt regulator U24 must be mounted on a suitable heat sink. The following wiring steps assume you have made provision for this. The lead length from U24 to the power connector should not exceed 4 inches or external bypass capacitors of Ø.1 to Ø.5 uF must be added from the regulator's input and output pins to ground with the capacitors mounted at the regulator.

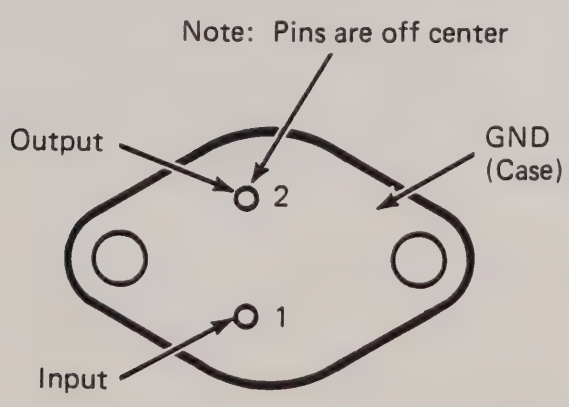


Fig. F.11 U24 Bottom View

( ) Prepare three lengths of hookup wire. Use number 22 AWG or larger. The length should be sufficient to run from your proposed mounting location of U24 to the power connector.

( ) Strip 1/4" of insulation from both ends of each wire.

( ) Attach and solder a MOLEX pin to one end of one of the wires.

( ) Solder the free end of this wire to pin 1 of U24.

( ) Insert the end of this wire with the MOLEX pin into the power connector housing, pin 3.

( ) Similarly, attach a MOLEX pin to one end of another wire.

( ) Solder the free end of this wire to pin 2 of U24.

( ) Insert the end of this wire with the MOLEX pin into the power connector housing, pin 7.

( ) The remaining wire must be attached to the case of U24 (ground).

( ) The free end of this wire will be attached to TNC ground at the mounting screw that passes through the silk screened outline of U24. For purposes of testing, attach it to this point on the TNC board now, using a 6-32 screw, #6 washers and a 6-32 nut.

Transformer Primary Wiring

One side of the ac line will connect to the dotted terminal, and the other, via a Ø.5 amp fuse and switch which you must supply (included in the TAPR TNC cabinet kit) connects to one of the three remaining terminals, depending on your local line voltage. The tap nearest the dotted tap is for 1Ø5 vac nominal, the next is for 115 vac and the last is for 125 vac. Be sure to use the proper tap, as low output will degrade the TNC's performance and high output will overheat it! If you have no way to measure your line voltage, use the middle (115 vac) tap. (See Fig. F.12.)

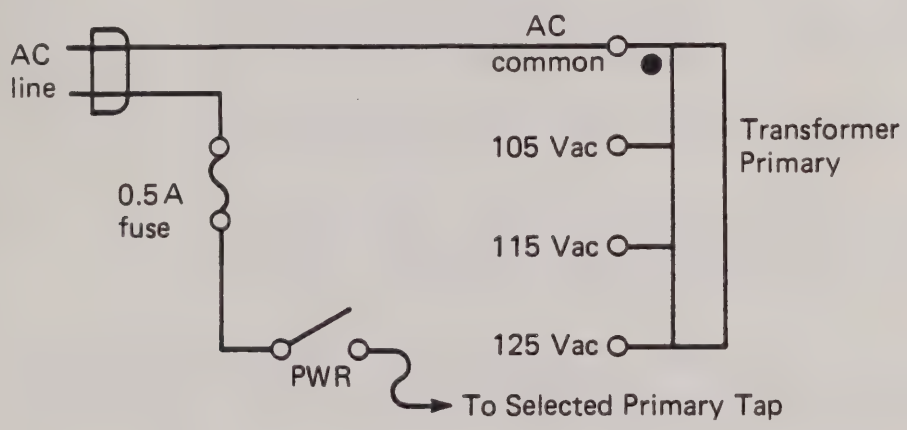


Fig. F.12 Transformer Wiring

If you are using the TAPR TNC cabinet, follow the directions supplied with it for wiring the transformer primary. If you are using your own cabinet, or no cabinet, follow the guidelines given below. Since each installation is different, parts are not supplied with the TNC kit for the primary circuitry.

( ) The primary lug nearest the black dot on the transformer should be attached to one side of the ac line.

( ) The selected tap should go to a power switch.

( ) The other side of the power switch should go to a fuse of not greater than 1/2 amp.

( ) The other side of the fuse should go to the remaining side of the ac line.

At this point, the TNC is wired. You should have a wired transformer, three headers (U30, U34 and U35) and a number of ICs.

Initial Kit Checkout

For initial checkout you will need a voltmeter capable of measuring up to 30 volts dc. If you encounter any problems, such equipment as an ohmmeter, oscilloscope, and logic probes will prove very useful.

The first test to perform is the power supply test. With no ICs socketed, but with U24 installed (on or off board as you chose), proceed with the following steps:

( ) Be sure the transformer primary is NOT connected to a power source.

( ) Attach the power plug to J4, on the TNC circuit board.

( ) Ensure that the lip on the power connector is against the wall on J4 and that the plug is not misaligned. See Fig. F.13.

In the following step, power will be applied to your TNC. Be ready to remove power quickly! If you smell anything that resembles a burning TNC, remove power immediately.

WARNING! Dangerous voltages may be present on the TNC and its associated wiring! Take steps to insure your safety. Do not contact any transformer primary voltages or otherwise endanger yourself!

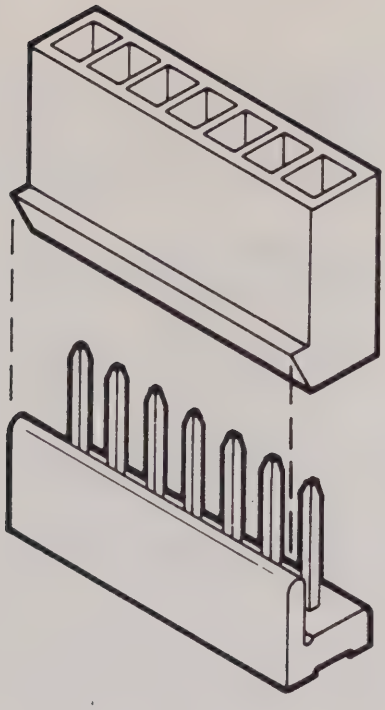


Fig. F.13 Power Connector Mating

If you have problems at any point disconnect power and determine the cause of the problem before proceeding!

( ) Apply power to the transformer.

( ) Measure the +5 volt line to GND. A convenient place to measure this voltage is the bus in the wire wrap area. The 5 volt bus is marked on the silkscreen of the TNC circuit board.

( ) Verify that the voltage is 5 volts +/- Ø.3 volts. (Measured value: _____)

( ) Measure the +12 volt line to ground. Again the wire wrap area is the best place to test this.

( ) Verify that the voltage is 12 volts +/- 1.3 volts. (Measured value: _____)

( ) Measure the -12 volt line to ground. As before, the marked bus in the wire wrap area is a convenient place to test.

( ) Verify that the voltage is -12 volts +/- 1.3 volts. (Measured value: _____)

( ) Measure the analog +5 volts. A convenient place is U28 (located in quadrant 4's lower center), pin 7 or 8. Be very careful not to damage the socket by forcing in probe tips!

( ) Verify that the voltage is 5 volts +/- Ø.3 volts. (Measured value: _____)

( ) Measure the analog -5 volts. (U28 pin 6, 13 or 14.)

( ) Verify that the voltage is -5 volts +/- Ø.3 volts. (Measured value: _____)

( ) Remove power from the TNC transformer and disconnect the connector at J4.

If the above tests were successful, you are ready to install the ICs in your TNC! If the tests were not successful, check the power supply per the instructions in Chapter VI.

IC Installation

The ICs were shipped to you in an anti-static carrier. Many of them are VERY sensitive to static discharge. These parts were purchased from franchised distributors and are of top quality. The probability of them being defective as shipped is extremely low. They should be handled with extreme care.

If you live in a climate with low humidity, or if you have carpeting in your work area, take special precautions. You might try spraying the carpet with some sort of fabric softener/anti-static preparation (such as a 50% solution of "Downy" and water) to reduce static.

If you have a metal working surface, ground it! Place a hand on the working surface before you touch the ICs and be sure to frequently ground yourself to the working surface. If you don't have such a surface, use a large cookie sheet or aluminum foil. Handle ICs only while sitting quietly, never while moving or walking!

Avoid static discharge near the ICs!

( ) Anti-static precautions understood and implemented.

Install the ICs using special IC handling tools, such as those produced by OK Machine Tool. If you lack these, you

may gently rock both sides of an IC on your metal work surface to make the leads straight and parallel to each other and install it in its socket with firm, even pressure applied over the length of the IC body.

WARNING: The TNC circuit board will flex excessively (when installing ICs) if it is laid on a flat surface, due to the mounting screws on U24. Do not allow the board to flex when inserting ICs.

If you make a mistake, or have to remove an IC for any reason, special anti-static removal tools are available. If you don't have access to such tools, you may try using a small screwdriver and gently prying the IC out of the socket, a little at a time, from alternate ends of the IC, but to use other than the proper tools exposes you to risking damage to your IC.

NOTE: It is very easy to bend a pin or fold it under the IC during installation. Be sure to check each IC as you install it in the steps below to be sure that all pins are actually in the socket and not tucked under the IC or hanging over the socket! It is easy to make this error!

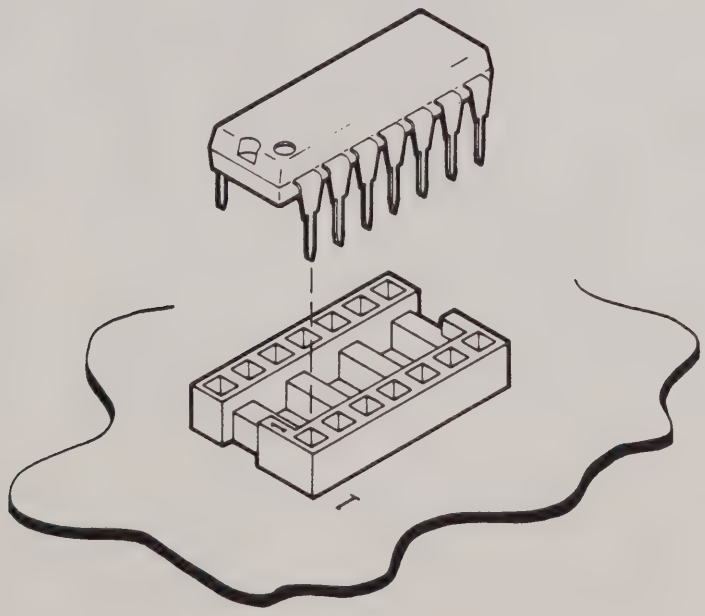


Fig. F.14 IC Pin 1 Identification

Now you may begin.

(quadrant 1)

( ) U1 74LSØ4
( ) U5 MC68Ø9
( ) U7 6264
( ) U8 (No IC is installed in this position.)
( ) U9 2764 EPROM marked "8ØØØ"

(quadrant 2)

( ) U1Ø 2764 EPROM marked "AØØØ"
( ) U11 2764 EPROM marked "CØØØ"
( ) U12 2764 EPROM marked "EØØØ"
( ) U6 6522
( ) U27 2212
( ) J6 (No IC is installed in this position.)
( ) U32 74LS86
( ) U21 74Ø6
( ) U31 1458

(quadrant 3)

( ) U2 74LS393
( ) U4 74S288 (or 82S123 or 76Ø3)
( ) U3 74LSØØ
( ) U14 6551
( ) U13 6821 (or 652Ø)
( ) U36 74LS1Ø
( ) U15 1488
( ) U16 1489 (or 1489A)

(quadrant 4)

NOTE: The next step is to install the HDLC controller, U17. This IC is very expensive and the version with a "hump" in the middle is very delicate. Exercise extreme caution when installing this IC. If you ever have to remove it, standard IC handling tools tend to break it into a pair of non functioning 20-pin ICs! Be very careful!

( ) U17 1933 (or 1935)
( ) U35 14-pin header with capacitors and resistors.
( ) U19 2206
( ) U18 2211
( ) U34 16-pin header with capacitors and resistors.
( ) U25 7406
( ) U26 555
( ) U30 16-pin header with resistors only.
( ) U28 MF-10
( ) U20 1458

At this point you should have all the ICs installed. The only parts remaining should be the alternate capacitor for C19 (0.1 uF or 1 uF), a 9-pin male DE9P connector and the backshell for that connector, and possibly R59 (560 ohm 5%). If you elected to mount U24 off board, you may have some hardware left as well.

( ) Carefully inspect all ICs again to be sure they are all seated in their sockets and there are no bent or otherwise improperly installed pins. Be sure pin 1 of each IC matches pin 1 of its socket or you will destroy the IC.

The next step will be to apply power to the TNC. If it is working properly, you will see a certain sequence of LED activity. Please read the following eight steps before you perform any of them!

( ) Be sure the transformer primary is NOT connected to a power source.

( ) Attach the power plug to J4 if it is not already connected.

( ) Ensure that the lip of the power connector is against the wall on J4 and that the plug is not misaligned.

( ) Be sure the DIP switches are set as follows:

Switch 1 (top)	ON (to the right)
Switch 2	ON
Switch 3	OFF
Switch 4	ON

( ) Apply power to the transformer.

( ) Note that LEDs D1 (RESET) and D6 (SPARE) illuminate.

( ) After about 1 to 2 seconds, LED D2 (CWID) will blink on.

( ) Remove power from the TNC.

If this test is successful, it means that most ICs on the TNC have been exercised, the EPROMs read and the TNC digital circuitry is probably functional. If this test was not successful, please refer to Chapter VI for troubleshooting information.

The next step in bringing your TNC to life is to attach a terminal to the serial port. If your terminal (or computer with a terminal emulation program) has a "standard" RS-232C serial port using a DB-25 type 25 pin connector, your task is probably simple. Construct a pin-for-pin cable with wires in at least positions 1 through 8 and 20, using a male connector for the TNC end and whatever sex connector matches your terminal at the other end.

If, however, you have a terminal that uses a nonstandard connector or uses some of its serial port pins for other purposes, please refer to Chapter V (under the heading "Terminal Interfacing") for details on your TNC's serial port.

Unfortunately, there are many nonstandard implementations of RS-232C serial ports, so a mating cable to interface your TNC and terminal is not included. Furthermore, many computer hobbyists already have a standard cable to interface to a modem, and since the TNC is configured to look like a standard modem at its serial port connector, this cable can be used. One likely source for such a cable is Radio Shack, or any typical computer store.

( ) Terminal interfacing cable constructed.

Now set your terminal to 300 baud. Set the word length to 7

and parity to space, or word length to 8 and parity to none. If the number of stop bits is settable, select 1.

( ) Terminal set to 300 baud with proper word length and parity.

( ) Be sure the TNC transformer primary is NOT connected to a power source.

( ) Attach the power plug to J4 if it is not already connected.

( ) Ensure that the lip of the power connector is against the wall on J4 and that the plug is not misaligned.

( ) Attach the TNC end of the terminal interfacing cable to J2. Do not force it in place -- it should press on snugly and remain in place. Excessive force may damage the connector or PC board.

( ) Attach the other end of the same cable to your terminal.

( ) Apply power to your terminal and allow it to warm up for a minute or two.

( ) Be sure the DIP switches are set as described above.

( ) Apply power to the TNC transformer primary.

( ) The TNC will flash its LEDs as above, then send the following message to your terminal:

Please type an asterisk (*) for autobaud routine

If this message does not appear, toggle the reset switch (switch 3) on then off. If the message still does not appear, consult Chapter VI for troubleshooting hints.

Assuming the autobaud message appears, proceed with the following steps.

( ) As the autobaud message starts to appear on your terminal, strike (gently!) the * key.

If you allow the message to complete before you strike the key, strike it two more times with about 1/2 to 2 seconds between key presses. If the autobaud message starts to appear again, strike the carriage return key WHILE THE MESSAGE IS BEING TYPED ON YOUR SCREEN.

( ) The following message will appear on your terminal:

```
Tucson Amateur Packet Radio Corporation
TAPR/AMSAT AX.25 level 2 version X.Y
RAM length is 2000
cmd:
```

where X.Y represents the software revision level.

The "RAM length is 2000" portion of the sign-on message indicates the number of "bytes" (in hexadecimal) that the software exercised and found operational on your TNC when it went through the power-up reset routine. The value $2000 (where the prefix "$" indicates a hex number) is the same as 8192 (decimal) or "8 k". A different number than "2000" indicates that the RAM IC in socket U7 may be improperly installed or otherwise defective.

If you don't get this message, try again by toggling the reset switch (DIP switch 3) on then off. If it still doesn't work, consult Chapter VI.

Assuming all is well at this point, turn to the "Modem Calibration" section in Chapter V and proceed with the calibration. Please note that these instructions were written for use during the life of the TNC, and not just for the initial calibration, so ignore any messages to remove jumpers that you may not have installed on your TNC. When you are done, return to this section for further instructions.

( ) Modem calibration complete.

Now that your modem is calibrated (with the possible exception of the transmitter drive level and tone null if you weren't able to get access to an oscilloscope or AC voltmeter), it is time to connect your radio to your TNC.

The TNC radio port is designed to work with almost all common (and many not so common) transceivers or

transmitter-receivers.

( ) Read the Chapter II "Radio Interfacing" subsection.

Since there is such a wide variety of radio connectors in use in Amateur radio, the mating connector for your radio, as well as the wire to connect to it, are not included in your TNC kit. However, we have included a mating connector for the radio port on the TNC, along with a backshell. Please get these parts out and warm up your soldering iron for the following steps.

In constructing the radio interface cable, you will need to obtain a mating connector(s) for your radio's microphone and headphone or external speaker jack(s). We recommend you keep the length of the radio interface cable to under about four feet (1.3 m). If possible, use shielded wire for the microphone audio connection. The instructions below assume you are using such wire.

NOTE: Hookup information is contained in Appendix D for some common Amateur radio equipment. Please check this Appendix for information regarding your radio. If your radio is not documented in Appendix D, please take a few moments of your time to fill out and submit to TAPR the "RADIO INTERFACING DATA" page found at the end of Chapter II once you have successfully interfaced your radio and TNC. This is so we may include it in future editions of this manual.

( ) Locate the DE9P connector.

( ) Prepare a length of shielded wire. This means to separate the braided shield from the insulated center conductor, then strip about 1/4" of insulation from the center conductor.

( ) Solder the center conductor to the audio output terminal of your radio headphone/speaker connector.

( ) Solder the shield to the audio common (or negative power common, NOT audio common if such point is at a positive potential with respect to your radio's power "ground").

( ) At the other end of the same shielded wire, solder the center conductor to pin 3 of the

DE9P.

( ) Solder the shield to pin 7 of the DE9P, being careful to insulate or otherwise prevent the shield from contacting any other pins of the DE9P.

( ) Prepare another length of shielded wire.

( ) Solder the center conductor to the microphone audio input contact of your radio microphone connector.

( ) Solder the shield to the microphone audio common (this common MUST be at the same dc potential as the headphone/speaker audio common as well as dc power common.

( ) Solder the center conductor of the other end of the wire to pin 5 of the DE9P.

( ) Solder the shield to pin 9 of the DE9P, again taking care to ensure that the shield doesn't contact any other pins.

( ) Prepare a length of unshielded wire the same length as the microphone audio cable just installed.

( ) Strip 1/4" of insulation from each end of this wire.

( ) Solder one end of this wire to the PTT (microphone key) contact of your radio microphone connector.

( ) Solder the other end of this wire to pin 4 of the DE9P.

( ) Verify that there are no shorts or opens in the cable you have just fabricated.

( ) Assemble the backshell(s) (if any) onto your microphone and headphone/speaker connectors.

( ) Remove the cover from the rear or side cable entrance hole of the backshell provided for your DE9P connector (depending on which one

you desire to route your cable through). See Fig. F.15.

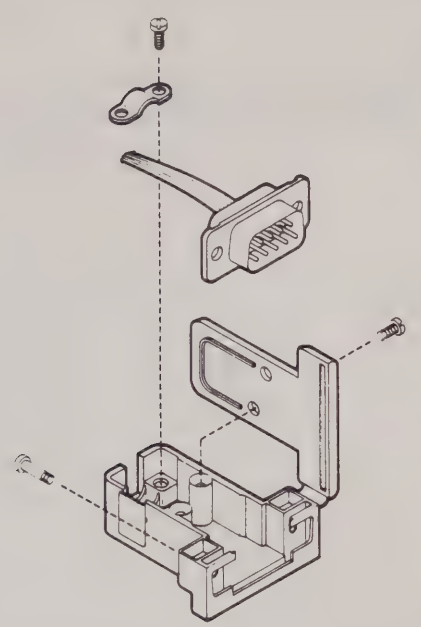


Fig. F.15. Radio Interface Connector

( ) Using the supplied screws, attach the cable clamp to the backshell, allowing a slight amount of slack in the cable to prevent strain on the soldered connections on the DE9P.

( ) Fold the backshell over the DE9P and assemble it. Fasten it with the supplied screws.

( ) Finally, thread the two captive screws through the flange on the DE9P. These screws will be used to attach the connector to the radio port connector (J3) on your TNC circuit board.

( ) Now attach the radio port connector to J3 on your TNC and tighten the two captive retaining screws snugly.

( ) With your radio switched off, attach the microphone and headphone connectors from your TNC radio port cable to your radio.

( ) Verify that your TNC's DIP switches are set as described above.

Congratulations! You are now ready to operate your new Amateur packet radio station!

Please refer to Chapter III (Operation) where you will be guided through the initialization procedures for your new TNC and place your packet station on the air.

Parts Procurement For The TAPR TNC

This section will describe all the parts needed to be procured in order to build a fully operational TNC. Various sources for the parts are given here. The sources listed are merely suggested sources. The parts need not be purchased exclusively from them in order to make the TNC work. Parts may be purchased prime from major distributors, or they may be junk box parts or obtained from swapmeets/fleamarkets. Be aware that used parts may cause problems so care must be taken when selecting and using them.

The listing shows the quantity for each part, the generic part number, the description, and the TNC unit designation. Illustrations are provided for some parts to aid in selection. It is recommended that the assembly instructions be read thoroughly to familiarize yourself with the parts and procedures. In order to provide you with the most information possible, the complete TAPR assembly instructions and parts list are provided.

APPENDIX G PARTS PROCUREMENT

INTEGRATED CIRCUITS

74XX LOGIC:

1	74LSØØ U3	QUAD 2 INPUT NAND
1	74LS04 U1	HEX INVERTER
2	7406 U21, U25	HEX INVERTER O.C.
1	74LS10 U36	TRIPLE 3 INPUT NAND
1	74LS86 U32	QUAD 2 INPUT EXCLUSIVE OR
1	74LS393 U2	DUAL 4 BIT COUNTER

LINEAR

1	1488 U15	EIA DRIVER
1	1489 U16	EIA RECEIVER
1	555 U26	TIMER
1	MF10CN U28	CMOS ACTIVE FILTER
2	1458 U31,20	OP AMP
1	2206 U19	FSK MODULATOR
1	2211 U18	FSK DEMODULATOR
1	LM309K U24	(340-5 or 7805) TO-3 case +5vdc regulator
1	78L05 U33	+5vdc regulator
1	7812 U22	+12vdc regulator
1	79L05 U29	-5vdc regulator
1	7912 U23	-12vdc regulator

MICROPROCESSOR

1	6809 U5	8 BIT MICROPROCESSOR (1 MHZ)

I/O

1	6821 U13	(6520) PERIPHERAL INTERFACE ADAPTOR (this chip is optional, not used in normal operation of TNC)
1	6522 U6	VERSATILE INTERFACE ADAPTOR
1	6551 U14	UART

APPENDIX G PARTS PROCUREMENT

MEMORY

1	6264 U7	8K X 8 STATIC RAM	
4	2764 U9-U12	8K X 8 EPROM (450ns)	
1	82S123 U4	(74S288) BIPOLAR PROM (address decoder)	
1	X2212 U27	NOVRAM (nonvolatile RAM) Xicor Inc. 851 Buckeye Ct. Milpitas, Ca. 95035 408-946-6920 or	
	52212	NOVRAM NCR Corp.	
		Distributed by Applied Digital Technology or Wyle Electronics 30423 Canwood St. Suite 133 Agoura Hills, Calif. 91301 818-880-9000	

SPECIAL

1	WD1935 U17	HIGH LEVEL DATA LINC CONTROLLER HDLC	
		WESTERN DIGITAL 2445 McCabe Way Irvine, Cal. 92714 714-863-0102	
		Distributed by Wyle Electonics or Applied Digital Technology	

APPENDIX G PARTS PROCUREMENT

RESISTORS

1% 1/8W (1/4W may be substituted)

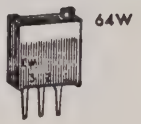
4	10.0K	R51,54,55,57
2	16.2K	R35,56
1	22.1K	R47
1	23.7K	R37
1	37.4K	R58
1	47.5K	R46
1	57.6K	R53
1	63.4K	R52
1	100K	R45

5% 1/4W

X1	200	R39
X12	330	R16-24
X1	470	R87
X1	560	R59
X5	680	R76-79,81
X7	1K	R12,13,42,64,65,71,80
X1	3.3K	R67
X14	4.7K	R8-11,29,32,34,50,72-75,86,88
X1	6.8K	R2
X13	10K	R5-7,30,40,49,60,61,69,82-85
X1	30K	R41
X3	33K	R1,3,4
X2	39K	R62,63
X1	68K	R66
X5	100K	R15,28,68,70,82
X1	470K	R48
X1	510K	R44

VARIABLE RESISTORS 22 TURN

2	10K	R36,43
1	20K	R38
1	50K	R33
1	100K	R31



APPENDIX G PARTS PROCUREMENT

CAPACITORS

CERAMIC DISC

3	330 pf	C9,11
2	1500 pF	C29,30
7	.01 uf	C13,15,31,33,61,63,64
2	.05 uf	C18,28
36	.1 uf	C2,4,8,24-27,34-59,60,62
1	.15 uf	C23

MYLAR or MONOLITHIC

1	2200 uf	C21
1	4700 uf	C20
2	.022 uf	C16,22

ELECTROLYTIC

2	1 uf/16V	C17,19 R
3	10 uf/16V	C6,7,32 R
1	100 uf/6V	C12 R
1	330 uf/6V	C14 R
2	470 uf/25V	C3,5 R
1	2200 uf/16V	C1 R

APPENDIX G PARTS PROCUREMENT

DIODES

6	1N4001	D9-12,17,18
2	1N4148	D21,22
1	1N4752	(33V ZENER may be substituted with 1N4750) D19
4	1N5400	D13-16
8	LED	LIGHT EMITTING DIODES any color(s) desired.

TRANSISTORS

2 VN10KMA POWER VFET
Q1,2

SILICONIX, INC.
2201 Laurelwood Rd.
Santa Clara, Ca. 95054
408-988-8000

or

SUPERTEX, INC.
1225 Bordeaux Dr.
Sunnyvale, Cal. 94086
408-744-0100

or

Distributed by Wyle Electronics

APPENDIX G PARTS PROCUREMENT

IC DIP SOCKETS (Solder tail)

3	8 PIN
11	14 PIN
5	16 PIN
1	18 PIN
1	20 PIN
7	28 PIN
4	40 PIN

CONNECTORS

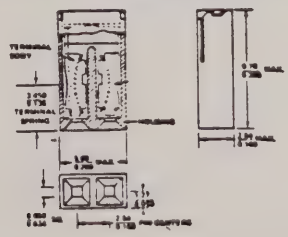
5 2 PIN MALE PCB HEADER .1" CENTERS

3 3 PIN MALE PCB HEADER .1" CENTERS

1 5 PIN MALE PCB HEADER .1" CENTERS
J7
(OPTIONAL: It is needed only if an external tuning indicator is to be connected.)
The original TAPR kit used a connector looking something like this for J7 -->

Note: the above 3 types of headers can be purchased as a single strip of Single-In-Line (SIP) block headers. The strip can then be broken up into the required lengths of smaller headers. If you do this be very careful when connecting a plug to J7 because there is no polarity wall.

13 JUMPERS, PUSH ON
JP1-13 These jumpers contain 2 female pins jumpered together which are .1" apart. To order them from JDR Microdevices use part no. JUMPER with the description SHORTING BLOCKS.



1 20 PIN MALE HEADER WITH EJECTOR LATCH
J5

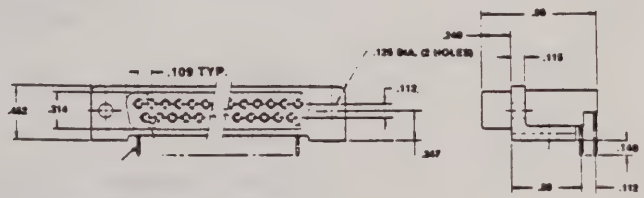
1 DE9P 9 PIN MALE CABLE EIA (to terminate the cable which comes from the radio and plugs into J3.)

1 DE9S J3 9 PIN FEMALE PCB RIGHT ANGLE EIA

1 DB25P J1 25 PIN MALE PCB RIGHT ANGLE EIA (OPTIONAL- used with U13 6821 PIA)

1 DB25S J2 25 PIN FEMALE PCB RIGHT ANGLE EIA

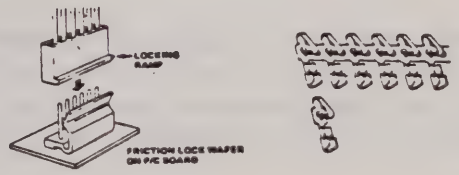
The above 3 connectors are PCB mounted with right angle pins which allow the connector to protrude out the back of the circuit board. They look like this:



1 7 PIN MALE MOLEX PCB POWER CONNECTOR .156" pins J4

1 7 PIN FEMALE MOLEX CABLE POWER CONNECTOR HOUSING

7 MOLEX POWER CONNECTOR PINS



1 14 PIN MALE DIP HEADER

2 16 PIN MALE DIP HEADERS

1 9 PIN BACKSHELL FOR DE9P CONNECTOR

MISCELLANEOUS

1 4 POSITION DIP SWITCH

1 TO 3 HEAT PCB MOUNT HEAT SINK

1 7.3728 MHZ MICROPROCESSOR CRYSTAL (HC-18U HOUSING)
Y1

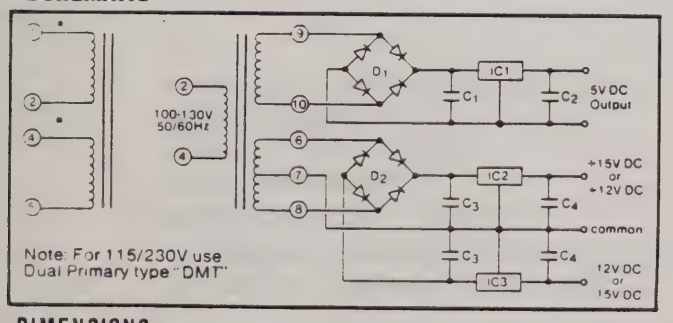
Available from:
Fox Electronics
Sales Dept.
P.O. Box 1078
Cape Coral, Fla. 33910
813-482-7212
or
Jan Crystal
2400 Crystal Drive
P.O. Box 06017
Fort Myers, Florida 33906-9989

1 MT-6-12 DUAL OUTPUT POWER TRANSFORMER

SIGNAL TRANSFORMER
500 Bayview Ave.
Inwood, N.Y. 11696
516-239-4510

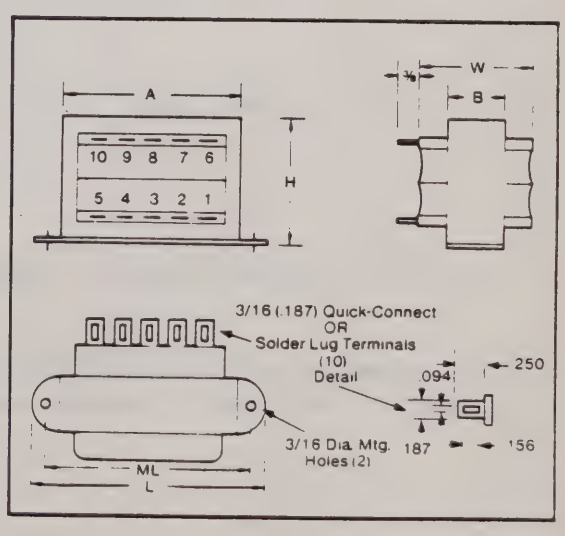
NOTE: This transformer seems to match almost perfectly the custom wound transformer that TAPR had originally designed for this TNC board. Multiple transformers may be substituted for this one if the outputs are compatible with the power supply inputs on the board.

SCHEMATIC



DIMENSIONS

Size	L	W	H	A	B	ML	Lbs.
6	3 1/4	1 1/16	1 5/16	2 3/16	1 1/16	2 13/16	1.1



APPENDIX G PARTS PROCUREMENT

POSSIBLE SOURCES:

Jameco Electronics (See ad in Byte Mag.)
1355 Shoreway Rd.
Belmont, Ca. 94002
415-592-8097

JDR Microdevices (See ad in Byte Mag.)
1244 S. Bascom Ave.
San Jose, Ca. 95128
800-538-5000
800-662-6279 (Calif)

Mouser Electronics
11433 Woodside Ave.
Santee, Ca. 92071
619-449-2222
or
2401 HWY 287 North
Mansfield, Tx 76063
817-483-4422

Dokay Computer Products, Inc. (See ad in Byte Mag.)
2100 De La Cruz Blvd.
Santa Clara, Ca. 95050
408-988-0697

Advanced Computer Products, Inc. (See ad in Byte Mag.)
P.O. Box 17329
Irvine, Ca 92713
714-558-8813

Krueger Technology, Inc. (See ad in Byte Mag.)
2219 South 48th St.
Tempe, AZ 85282
800-245-2235
602-438-1570 (Ariz)
(Krueger sells quality tested used integrated circuits at greatly reduced prices)

Most of these companies accept Visa/Mastercard orders, COD orders and accept orders over the phone. Check with the company for details.

APPENDIX H

MODIFICATIONS AND OTHER NOTES

12 VDC POWER SUPPLY FOR TNC BOARDS

Packeteers in Colorado have adapted a surplus power supply board for use with the VADCG and TAPR TNC's. The supply is made by Iriichi Tsushin Kogyo Ltd., part no. 10053214-2. It is available from several sources, including Radio Shack (#277-1016) and BNF Enterprises (#4NS0427). Our modifications to the supply will convert the -5V output to -12V (as required by the TNC), and will allow it to operate from 10 to 15 volts DC (permitting construction of portable packet stations and digipeaters operating from a 12 volt battery).

Some changes to the TAPR boards will be required, particularly on the older board layouts, to ensure compatibility with the new supply. VADCG boards work without modification, since they were designed for use with an external power supply.

These modifications have been broken down into several parts, so that you may perform only as much as necessary for your particular application. For a copy of the schematic and of these instructions, send a business size SASE with 1 once postage to: RMPRA -- Power Supply Experiments, 3775 E 115th Ave, Thornton CO 80233

**** DISCLAIMER ****

This information is a report on the methods and results of the authors' experiments. The modifications we used were not developed by the power supply manufacturer, the Vancouver Amateur Digital Communications Group, or by Tucson Amateur Packet Radio Corporation. Their approval of these modifications is not implied by our use of their products. We present our results for the information and assistance of others interested in this work; it is still in EXPERIMENTAL stages and those who attempt to duplicate our results assume all risks of doing so.

PRECAUTIONS
**** For All Applications ****

1. Read this procedure through and be sure you understand it, then do the job carefully. The switching power supply is a more complicated and less forgiving device than the three-terminal regulators it replaces.

2. Test the supply with dummy load resistors BEFORE attaching it to the TNC. If there is a defect, you don't want to find it by blowing out the expensive LSI chips on the TNC board!

3. Use a fuse on the supply input lead. The switching supply has NO other protection from excessive current. When testing and connecting the switching supply, do not, ever, short the output leads.

CONVERTING -5V OUTPUT TO -12V
**** For All Applications ****

Remove C14 (1000 UF, 10 V) and replace it with a capacitor rated 470 UF, 16 V (Radio Shack 272-957). Be sure to install C14 with the correct polarity (all the electrolytics have "-" facing the same direction). The reason for replacing C14 is that the 10 V rating will be exceeded, and the capacitor would be likely to fail prematurely. The replacement part must have a body diameter of 1/2 inch (13 mm) or less, and lead spacing of .20 inch (5 mm), or it won't fit onto the PC board. Check the part before you buy it, since a larger "equivalent" won't do the job.

Remove R18 (30K, 2%) and replace it with a series combination of 51K and 6.8K (the total value required is 58K). If you use 5% resistors you may have to change the 6.8K to get the exact resistance needed; you will determine if this is necessary when you come to the TESTING section.

The output is rated at 50 MA when converted to -12V, NOT the original 200 MA. This is plenty for the TNC's. The derating is necessary to keep the peak current in TR4 and L4 the same as in the -5V circuit at 200 MA.

REDUCTION OF INPUT VOLTAGE (OPTIONAL)
**** Skip this section if you will use more than 15 V input ****

This modification changes two resistors, so that TR2 and TR4 will receive the same amount of base current at 12 V input that the original values provided at 18 V input. This is insurance against getting a board with low gain transistors; the change is recommended even if the board seems to work without it. When this change is made, the recommended voltage from D1 "+" to D1 "-" is 10 to 16 VDC. Replace R7 (240 ohm, 1 Watt) with 120

ohm, 1 Watt. Replace R19 (910 ohm) with 470 ohm.

BYPASS INPUT RECTIFIER (OPTIONAL)
**** NOT for switching supplies which will have AC input ****

The input rectifier bridge has a voltage drop of about 1.5 volts. This needlessly reduces the supply efficiency when only DC input is used, and also increases the required DC input voltage. Connect a jumper from the "+" terminal of D1 to the terminal of D1 that connects to L2. Likewise, connect a jumper from the "-" terminal of D1 to the fourth terminal of D1, which connects to L1. Be sure the jumper wires cannot short to anything else. Check the continuity from the input leads (with SW1 on, and no power applied): red wire to D1 "+", and white wire to D1 "-". This modification retains the input RFI filter and on/off switch, and also uses D1 as a reversed-input clamp diode. Be sure to use a fuse in the input lead; 1 1/2 amp normal blow should be suitable. The supply draws about 1/2 amp producing 5.1 W output with 13.5 volt input.

**** THIS IS THE END OF THE POWER SUPPLY MODIFICATION ****

Users of TAPR TNC's should continue with the following sections, which deals with some pitfalls peculiar to that device. VADCG and other users should simply disconnect their old supplies and hook up the switching supply; then rejoin this procedure at the TESTING section.

TAPR BOARD CLOCK CIRCUIT
**** ALL TAPR USERS be sure your board is up to date ****

The clock oscillator on older TAPR TNC's would sometimes work at a parasitic frequency (not the one stamped on the crystal). The different rise-time of switching supply (compared to the original linear regulator) can make the problem appear in TNC's that were previously "clean". If the TAPR logon message is gibberish after installing the switching supply, your clock is malfunctioning!

The most recent TAPR kits have modifications in the oscillator to prevent the parasitic mode. If you have a 7404 at U1, you have the new board. If not, replace U1 (7414) with a 7404. Connect a 4.7 K resistor from pin 5 of U1 to pin 3 of U3 (for a neater installation, tie the U3 end to a free pad in the wire-wrap area and run a wire to U3 -- the resistor leads are much less likely to short out other pins that way). Resistors R12 and R13 should be 1.0 K; some early models have 1.5K.

The latest layout has also deleted CY (20 PF from pin 13 of U1 to ground), and added a 470 ohm resistor between C12 and the reset switches. We did not make these modifications, but you may wish to do so if you want an exact duplicate of the latest TAPR boards' circuit.

TESTING
**** For all applications ****

For initial testing, use dummy load resistors instead of your TNC board (in case something is wrong!).

RECOMMENDED TEST LOADS

OUTPUT	VADCG	TAPR	MINIMUM LOAD
+5V	6.8, 5W	6.2, 5W	51, 1/2W
+12V	62, 3W	180, 1W	470, 1/2W
-12V	470, 1/2W	470, 1/2W	1K, 1/4W

If you don't have the recommended load resistors, substitute higher resistance values (up to "minimum load"). Please note that the switching supply's outputs should NOT be tested without any load at all; some current must flow in L3 and L4 so that the semiconductors operate in their normal modes. POOR PERFORMANCE UNDER NO LOAD CONDITIONS IS NORMAL IN SWITCHING REGULATORS!

Connnect the input wires to a 12 volt source (or 18V, if the reduced input step wasn't performed). Watch the polarity if you have modified for DC input: RED is "+". Don't forget the fuse! Turn on SW1, and measure the +5V output. Adjust RV1 to set it to 5.0 volts. Now check +12 volts; it should be between 11.5 and 12.5 volts (there is no +12 adjustment). Check the -12 volt output; it should be between -11.5 and -12.5 volts. If it is necessary increase the -12V, increase the 6.8K (part of R18); likewise to lower the output decrease the 6.8K. The output changes by .25 V for every 1K change in resistance.

If you have an oscilloscope, you can check the output ripple. It is typically 10 to 20 millivolts peak to peak, at a frequency of 100KHZ. NOTE: poor grounding of the scope will cause noise pickup from the switching transistors, and give a false reading. When the probe tip is touched to the probe's ground lead clip, anything but a flat trace indicates you are picking up noise, not just output ripple.

HOOK IT UP!

**** FOR ALL APPLICATIONS ****

**** TAPR BOARD USERS: SEE DETAILED PROCEDURE IN NEXT SECTION ****

The output of the switching supply should be connected to the board with wire no smaller than #24 AWG, using leads of the shortest practical length. If you choose to install connectors in the leads, be sure they are polarized so you CAN'T POSSIBLY hook it up wrong. If you don't want to use connectors, you can remove the pins at the switching supply output and solder the wires directly to the PC board. VADCG TNC's use the regular power supply connections. DOUBLE CHECK YOUR WIRING BEFORE YOU TURN ON THE POWER.

TAPR INTERNAL POWER SUPPLY HOOKUP

**** TAPR BOARD USERS only ****

Feeding power to the output of integrated three-terminal regulators can cause them to fail [see "Ciarcia's Circuit Cellar" in BYTE, Jan. 1985]. To prevent this from happening, the regulators will be removed from the TAPR board, and the transformer connected to the switching supply. With this modification in place, the TAPR board can run on either 115 VAC or 12 VDC. YOU SHOULD HAVE FOLLOWED ALL OPTIONS UP TO THIS POINT.

Remove the +5 , +12, and -12 regulators (U24, U22, and U23) from the TAPR board. U24 may have been mounted on your cabinet back-panel. Remove D9 and D12 from the TAPR board. Connect an insulated jumper wire (#24 AWG or larger) from the ANODE side of D9 (not banded) to the output of U22 (pin 3). Connect another insulated jumper wire from the cathode of D12 (banded) to the output of U23 (pin 3). These jumpers bypass the 12 V rectifiers and regulators, so the +/- 12 supply can be connected at J4.

Disconnect the three wires from terminals 3, 4, and 5 of the power transformer. Now examine power connector J4. If you had the +5 regulator mounted outboard there will be a wire at pin 7 (closest to C1). If you don't have a wire there, prepare one (using one of the extra pins supplied with the TAPR kit), and install it at pin 7. Connect the wire from pin 7 to the switching supply +5 output. The wire at pin 6 used to go to the transformer. Hook this one to the +12 output of the switching supply. The wire at pin 5 also went to the transformer; hook it to the -12 output of the switching supply. Finally, hook the wire from pin 4 to the output ground of the switching supply.

If you had the +5 regulator mounted outboard, remove pin 3 from J4 by pressing down the locking barb with a small screwdriver and pulling out the wire. Then salvage the pin by removing the old wire. Connect the RED (positive) input lead of the switching supply and the cathode (banded end) of a 1N4001 diode to the pin (or the last spare pin, if the regulator was mounted on-board). Put the pin back into pin 3 position of J4. Slide a piece of spaghetti tubing or heat-shrink tube over a piece of wire, and solder one end of the wire to the anode end of the diode. Slide the tubing over the diode for insulation. The diode isolates the DC supply from the transformer, so the transformer won't put current out of the DC input leads. Connect the other end of the wire to one side of a fuseholder (for the +12V fuse). Connect the other end of the fuseholder to a wire (which will be the positive DC input lead). The white lead from the switching supply is the negative DC input lead; splice on additional wire if necessary. The input leads should be run to the +12 supply in a workmanlike and secure way (chassis connector, strain relief, etc.: take your pick).

Check with a VOM to be sure the wiring is correct (from switching supply outputs to the power traces in the wire wrap area). Check for shorts from +12 input to ground (resistance is normal...a SHORT is not). Check continuity for +12 input and ground, from input leads to the switching regulator card.

**** NOTE ****

The TAPR transformer is less than optimum for this application, but it will work at normal line voltage. For low voltage ("brownout") conditions the transformer may be replaced by a 12.6VAC, 1A transformer, or a lower primary tap selected on the TAPR transformer. If you do use higher transformer voltage, remove C1 (2200UF, 16V). It will be operating too close

to its maximum rating, and the switching supply has its own filters. If a battery is connected to the 12V input for backup, the transformer SHOULD BE CHANGED, or else the battery will be loaded constantly (the transformer / rectifier output has to exceed the battery voltage to prevent this).

MOUNTING

**** For all applications ****

The switching power supply may be mounted with #6 hardware, using a set at the holes near each corner. When mounting in a TNC cabinet, keep it as far away as possible from the critical analog circuitry of the modem.

The mounting hardware also serves to ground the the power supply board. In particular, the mounting hole closest to IC3 may be grounded through the mounting hardware to minimize noise on the +12V.

The mounting hole near SW1 MUST HAVE INSULATING HARDWARE to avoid shorting the power input leads to ground. Use either a plastic washer on BOTH top and bottom, or use non-conductive mounting hardware (ie, nylon screw and standoff). Don't rely on the green soldermask film; it is thin and not intended to be used for insulation.

READY TO GO!

Turn on the TNC (by powering up the switching supply). The logon message (or autobaud routine message) should appear. Measure the +5V on the TNC board, and make a small adjustment to RV1 if the voltage is off. If you are using a TAPR board with provisions for both AC and DC power, be sure it works both on 115VAC and 12VDC.

If you have any further results, we welcome them. They may be sent to us by mail, via the RMPRA bulletin Board (NOBRZ-1), or via NOCCZ on DRNET.

Good luck and 73,
--Don Brown, NOBRZ
--Tim Groat, KROU
--Linc Haymaker, KOZCO
(Rocky Mountain Packet Radio Assoc.)

****

HINTS AND KINKS for the RMPRA (Rocky Mountain Packet Radio Assoc)
12V TNC Power Supply Project

****

The following notes are based on my conversion of the TAPR TNC to 12v using the RMPRA project notes. Help was obtained from the three famous inventors (?) Tim - KROU, Linc - KOZCO and Don - NOBRZ

New Changes:

I have my TAPR TNC now running off of the 117 VAC with a 12 volt deep discharge type car battery as a hot standby (charged each night by a 15 amp battery charger and light timer).

For those interested in adding this 12 volt DC capability to your TNC here are some additional things that I have done:

1. I replaced the TNC power transformer with a Radio Shack 12.6 V, 1.2 amp one (RS # 273-1352). It uses the same mounting holes and fits perfectly. I am now assured that when I have both the 117 VAC and the battery hooked up, that the 117 VAC is the prime source for power (ie. higher voltage level output then the battery).

2. I replaced C1 (2200uf 16v) with a 1000uf 35v capacitor (RS# 272-1032). Even though it is not needed (there is enough filtering in the RS board), I always like my rectified AC to look good??? (You have to remove C1 if you do the above xfmr mod because of the 16v operating voltage).

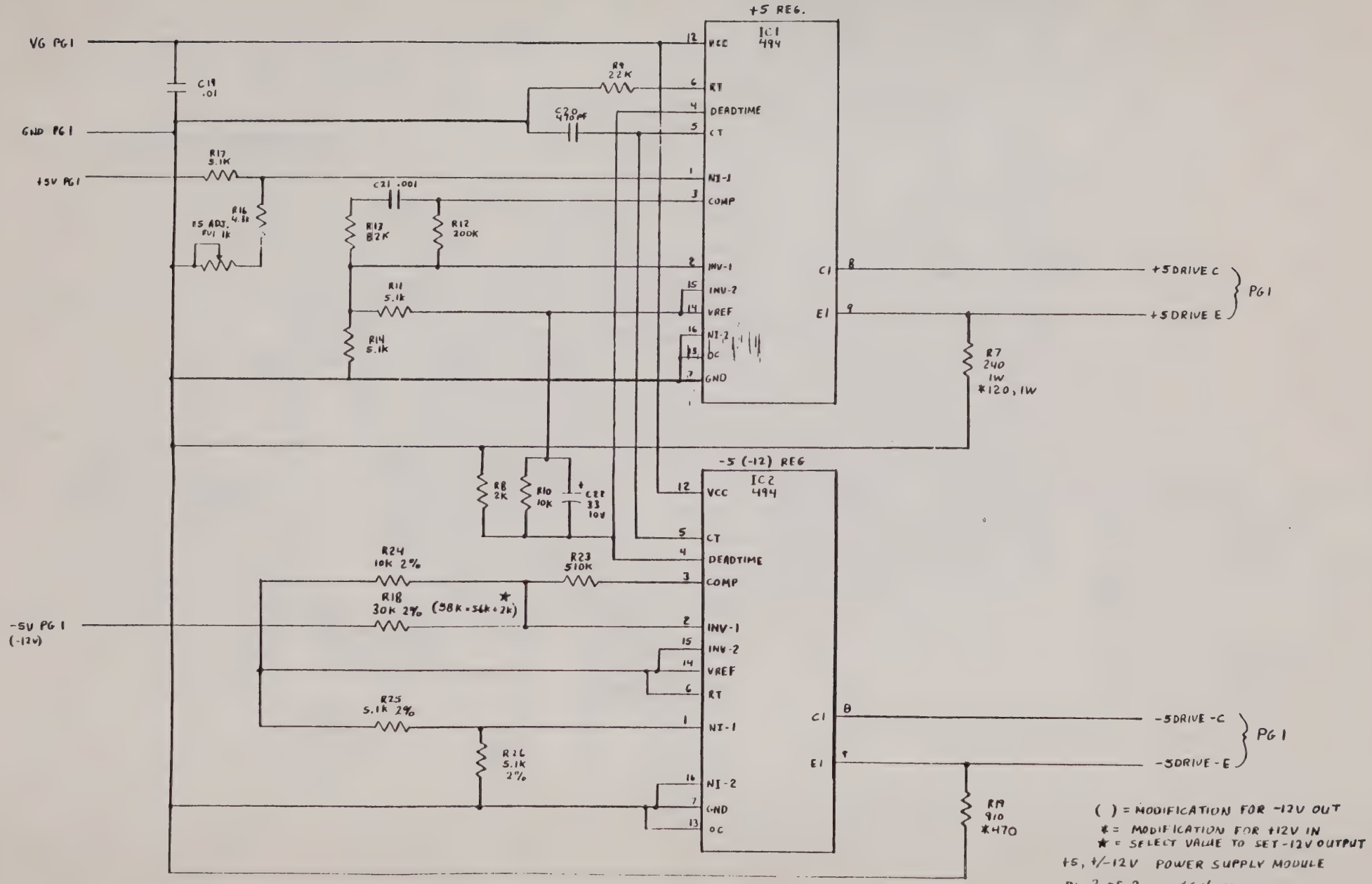
3. For the DC battery connection I installed a red and black insulated binding post (RS# 274-661) on the rear of the TNC cabinet in volt regulator. The holes are offset, so you ruin the symmetry of your Gucci cabinet, but you don't have to drill.

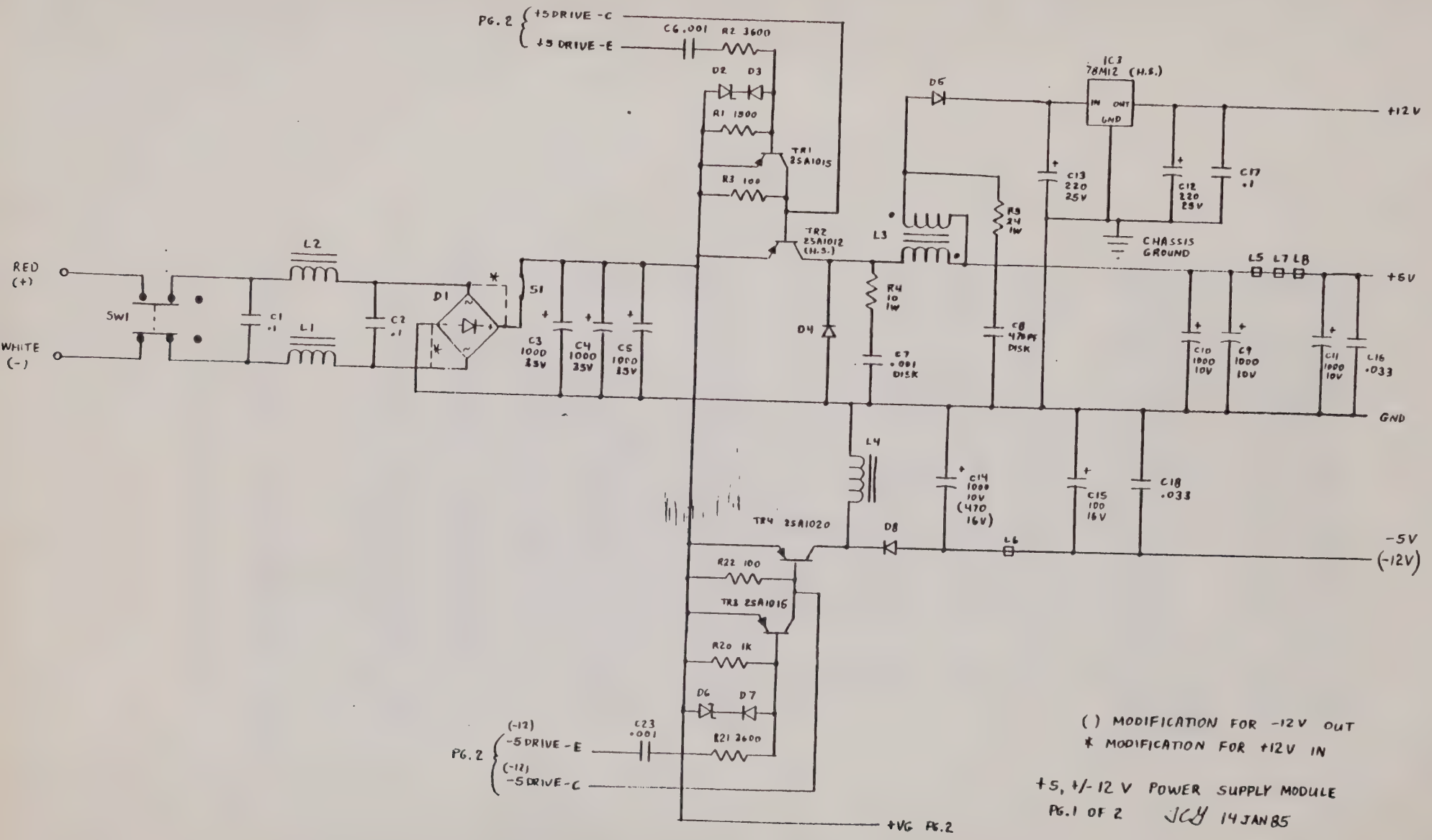
4. Drilling - yes I drilled two holes in the top cover to mount the board. All I need now are some more of those black screws to make it match (purist).

5. I wanted the front panel power switch to turn both the AC and the DC battery sources off, so I rewired the switch to turn on the RS board's power, The RS Power Supply board has a DPDT (SW1) which is modified for this use. First jumper the "negative" leg of this switch (the one going to the white DC input lead). and then parallel the front panel Gucci switch to the "positive" (red) leg. Your 117 volt AC will always exciting the xfmr, but this is no different than most of the inexpensive computer power supply designs that are on the market today

6. Finally, I added Radio Shack Molex type plugs for the input and output lines to the power supply (reuse the Gucci switch one). I now can remove the top cover and work on the unit a little easier.

Hope this info is of some help, 73 Bob WA6ERB (RMPRA).


+5 REG.
IC1
494
VG PG1
GND PG1
+5V PG1
C19
.01
R9
22K
C20
470pF
12 VCC
6 RT
4 DEADTIME
5 CT
1 NI-1
3 COMP
2 INV-1
15 INV-2
14 VREF
16 NI-2
13 OC
7 GND
R17
5.1K
R16
4.3K
+5 ADJ.
PVI 1K
C21 .001
R13
82K
R12
200K
R11
5.1K
R14
5.1K
C1
E1
8
9
+5DRIVE C
+5DRIVE E
PG1
R7
240
1W
*120, 1W
R8
2K
R10
10K
C22
33
10V
-5 (-12) REG
IC2
494
R24
10K 2%
R23
510K
R18
30K 2%
(58K + 34K + 2K)
-5V PG1
(-12V)
R25
5.1K 2%
R26
5.1K
2%
-5DRIVE -C
-5DRIVE -E
R19
910
*470
( ) = MODIFICATION FOR -12V OUT
* = MODIFICATION FOR +12V IN
★ = SELECT VALUE TO SET -12V OUTPUT
+5, +/-12V POWER SUPPLY MODULE
PG 2 OF 2 JCV 14JAN85


PG. 2 +5 DRIVE -C
+5 DRIVE -E
C6 .001
R2 360Ω
D2 D3
R1 130Ω
TR1 2SA1015
R3 100
TR2 2SA1012 (H.S.)
L3
D5
IC3 78M12 (H.S.)
IN OUT GND
+12V
C13 220 25V
C12 220 25V
C17 .1
CHASSIS GROUND
R9 24 1W
L5 L7 L8
+5V
RED (+)
WHITE (-)
SW1
C1 .1
L1
L2
C2 .1
D1
S1
C3 1000 35V
C4 1000 35V
C5 1000 35V
D4
R4 10 1W
C7 .001 DISK
C8 470PF DISK
C10 1000 10V
C9 1000 10V
C11 1000 10V
C16 .033
GND
L4
C14 1000 10V (470 16V)
C15 100 16V
C18 .033
TR4 2SA1020
D8
L6
-5V (-12V)
R22 100
TR3 2SA1016
R20 1K
D6 D7
(-12) -5 DRIVE -E
(-12) -5 DRIVE -C
PG. 2
C23 .001
R21 2600Ω
+VG PG.2
( ) MODIFICATION FOR -12V OUT
* MODIFICATION FOR +12V IN
+5, +/- 12 V POWER SUPPLY MODULE
PG. 1 OF 2 JCH 14 JAN 85

Kit Oscillator Problem

by Lyle Johnson

TAPR has recently received reports of field failures of the 7.3728 MHz oscillator on the TNC. The symptoms are improper reset or failed operation.

Careful analysis of the problem indicated that the oscillator, U1A, U1F, X1 and associated components on the kit board, would free-run at 22 to 49 MHz.

The problem has been duplicated and confirmed here in Tucson. There is a fix, however!

It appears that the Fairchild 74LS14 ICs shipped in kits starting about January may exhibit this symptom, sometimes when cold and starting up, other times when warm. The simple cure is usually just a power off/on cycle or two. This is not a fix, however.

We tested several Fairchild, Hitachi and National parts as well as the Signetics 74LS14s that are scheduled for the next 1200 or so kits. The Fairchild parts are the most susceptible to this fault, but the others may do it as well.

The fix is as follows:

(1) Replace R12 (1.5 k) with a 1k 5% carbon film resistor.

(2) Replace R13 (1.5 k) with a 1k 5% carbon film resistor.

(3) Carefully cut the trace on the top side of the PC board that goes from the lower end of R8 to U1 pin 5.

(4) Add a 470-ohm 5% carbon film resistor from the lower end of R8 to U1 pin 5. This resistor will be R87.

(5) Add a 4.7k 5% carbon film resistor from U5 (6809) pin 37 to U1 pin 5. This resistor will be R88.

(6) Replace U1 with a 74LS04 IC.

An alternate fix, previously circulated, required changing R12 and R13 to 1k and adding a 20 or 22 pF capacitor at location Cy. This fix has been documented and supplied with TNC kits shipped after 6 April 1984. It appears to work in most cases, but the modification outlined above is better.

A secondary benefit may be a reduction in the rate of NOVRAM "forgetting" symptoms -- this has not been confirmed, however. Feedback in this area would be appreciated. Some oscillators before the mod had the symptom of starting at a very high frequency, then locking on to the crystal after a few hundred milliseconds, often requiring a toggle of the RESET switch to bring the TNC up.

NOVRAM HINTS

Have you ever been plagued by the NOVRAM in your TNC 1 forgetting? If you have, and if it is a problem for your station operation, read on!

While the NOVRAM in the TAPR TNC 1 is well-isolated from the 6809 microprocessor (by virtue of being interfaced solely through the 6522 VIA, U6), there are times when the 6809 may "scribble" on the NOVRAM, causing it to lose the parameters you have so carefully stored in it. When this happens, you usually have to reboot the TNC by selecting the "ROM" default and going through the autobaud routine to restore restore the data lost by the NOVRAM.

In order to understand the nature of the two fixes that are presented below, some understanding of the cause of the problem is necessary.

Why the NOVRAM Sometimes Forgets

The only way to change the contents of the NOVRAM in TNC 1 is to write new data to the RAM portion of the chip (XD2212, U27) via lines PA4 through PA7 of U6, then toggling the !WE line to the NOVRAM by line PA2 of U6.

Then, the !STORE line must be toggled by way of line PA3 of U6.

This can all be accomplished by an incorrect write to the "A" port of U6 by the 6809 microprocessor, U5.

It turns out that, when power is turned off to the TNC, the +5 volt line decays slowly in terms of microprocessor time. Thus, the 6809 has time to continue to operate as the voltage decays. One nasty characteristic of the 6809 (and the Z80 for that matter) is that it will begin to misinterpret instructions as the voltage is lowered below its operating specifications. This may allow it to accidentally write incorrect data to 6522 U6 and thus to NOVRAM U27.

Fix #1

The simplest fix to this problem has been submitted by several TNC owners. It consists of isolating the !STORE line of the NOVRAM from line PA3 of U6.

Examine the schematic of your TNC 1, page A-3. You will note that thereis a 10k pull up resistor, R5, from the !STORE line of U27 to the +5 volt bus. Further, switch S4 is used to disconnect the ARRAY RECALL line of the NOVRAM from the reset bus of the TNC.

If you have never used the "soft" reset feature of your TNC (and the vast majority of you would never use it), you can use this switch to protect your NOVRAM. If you have the TNC cabinet, it is especially convenient to do this modification.

The procedure is simple.

First, carefully cut the traces to switch S4 pins 4 and 5 (switch position 4). Next, add a small jumper wire from U27 pin 10 to U6 pin 34. This will enable the reset bus from the TNC to force the NOVRAM to recall its permanently stored contents.

Now, cut the trace going from U6 pin 5 (PA3) to U27 pin 9 (!STORE). Cut this trace so that resistor R5 (10k) is still connected to U27 pin 9, but not to U6 pin 5.

Add a small jumper wire from U6 pin 5 to S4 pin 4. Add another jumper wire from S4 pin 5 to U27 pin 9.

This completes the modification.

When you operate your TNC, leave switch 4 of S4 in the open, or off position. If you have the cabinet, leave S4 open and leave the "soft/hard" switch in the "soft" position.

Whenever you wish to change the "permanent" contents in either bank of your NOVRAM, place the switch in the close dposition (S4 switch 4 on or "soft/hard" in the "hard" position). Issue the PERM command, then return S4 to the off position (or place the "soft/hard" switch in the "soft" position).

Fix #2

For those who desire an "automatic" method of ensuring NOVRAM integrity, the following modification has been engineered by AEA and is part of their PKT-1 packet controller. TAPR extends its thanks to AEA for permission to publish this information.

First, ensure that your TNC is board Rev 3. If it has the TAPR logo on it, it is Rev 3. If your TNC is Rev 2, perform the oscillator modifications listed in PSR number 11 (June, 1984).

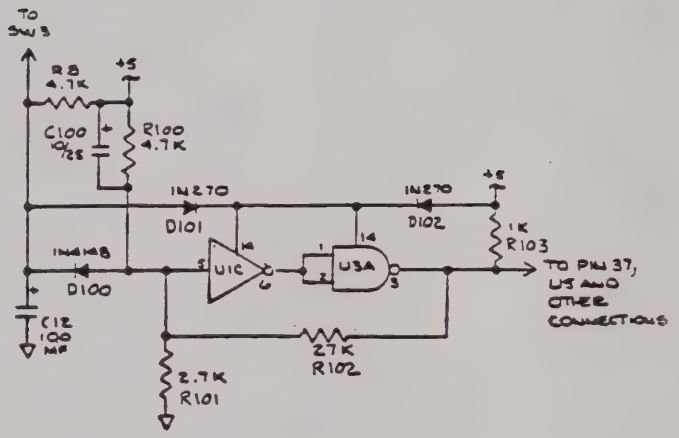
Then modify your circuit board to make the changes shown below.

This modification is more complex than Fix #1 and detailed instructions are not provided here. Basically, you have to cut the power traces going to U1 and U3 and jumper around these ICs to provide power to other ICs that are powered by the +5 volt bus after it passes through ICs U1 and U3. Then, add the resistors, capacitors and diodes called out in the parts list below. These are labelled R100-R103, D100-D102 and C100 in the schematic above.

(If you have a Rev 3 TNC, remove R87 and install diode D100 in its place. Remove R88 and replace it with R102.)

This circuit senses when the +5 volt bus is falling and asserts the reset line to the 6809 at that time. With reset held low, the 6809 will cease operation, thus preventing it from "scribbling" on your NOVRAM. The reset capacitor, C12, provides power to ICs U1 and U3 through diode D101 to ensure that the these TTL ICs perform properly until after the +5 volt bus has fallen sufficiently to ensure that the 6809 will do no further damage to the NOVRAM contents.

Operate your TNC as before.



Parts required for Fix #2

D100	1N4148	Silicon Diode
D101	1N270	Germanium Diode
D102	1N270	Germanium Diode
C100	10 uF 25 volt	Electrolytic Cap
R100	4.7k 1/4 W 5%	Resistor
R101	2.7k 1/4 W 5%	Resistor
R102	27k 1/4 W 5%	Resistor
R103	1k 1/4 W 5%	Resistor

Note: These parts are not available from TAPR and no upgrade kit for this modification will be made available.

Modem Sensitivity Modifications

by Lyle Johnson, WA7GXD

See the end of this Note for ordering instructions for parts kits to accomplish these modifications. TAPR parts kits come with complete assembly instructions.

NOTE: DO NOT attempt ANY modifications until the TNC has been constructed, tested and successfully used on-the-air!

Improved Sensitivity

TAPR is currently conducting experiments to improve the already excellent performance of the TNC modem. While the results are still coming in, field testing by a number of packeteers has indicated that modem performance may be improved by about 2 db under weak signal conditions with the following modification. The improvements are particularly applicable to hf and OSCAR-10 operations.

NOTE: Beta part number callouts are in parentheses following kit callouts.

(1) Change the value of C20 (C25) from 0.01 uF to 0.0047 uF. C20 is located on header U34.

(2) Change the value of R46 (R14) from 30.1 k to 47.0 k. R46 is on header U34.

(3) Attach a 4.7 k resistor from U18 pin 7 to +12 volts.

Kits: A good place to add this resistor is at the plate-through hole at the right of the trace just below U21/just above C18 and R39. The other lead should be carefully soldered to the trace leading to pin 1 of U18.

Beta: The 4.7k resistor just below and to the left of U18 pin 7 should be removed. Replace it with another 4.7k resistor. The right end of the resistor should be installed as before, but the left end should be re-routed to the rightmost hole of the +12 volt bus in the wire-wrap area. This is the hole just below and to the left of the 4.7k resistor.

(4) Kits: The trace from U18 pin 7 passes under R76, R77, R78 and R79 on its way to J5. Just to the right of these resistors and the plate-through hole adjacent to R78, cut the trace carefully, exposing about 1/16 inch (0.2 mm) of bare PC board. Scrape some of the coating off of this trace for a distance of about 1/8" (0.4 mm) on the side that leads away from R78.

Beta: On the top of the board, locate the trace which coming from JP3 which goes to a plate-through hole near the left end of J3. Turn the board over and follow this trace until it ends at a plate-through hole under U17 (in line with pins 14 and 27 of U17). Carefully cut the trace near the plate-through hole under U17, exposing about 1/16" (0.2 mm) of bare board. Scrape some of the coating off of this trace for a distance of about 1/8" (0.4 mm).

(5) Attach a VN10KM transistor as follows:

Kits: Drain to the plate-through hole adjacent to R78; Source to the ground strip at the bottom of R78; Gate to the trace from U18 pin 7 (cut in [4] above).

Beta: On the solder side of the board, attach gate to the trace leading back to JP3; Source to U17 pin 28; Drain to the plate-through hole mentioned in step [4] above.

(6) Beta only: Install a 4.7k resistor on the solder side of the board. One end should attach to the U17 pin 27 and the other end should be soldered to the "top" side of the 0.1 uF capacitor between U17 and U25 (this is the side that goes to U25 pin 14).

(7) Recalibrate the demodulator center frequency to 1685 Hz by typing:

cal:3/1094

while in the calibrate mode.

300 Baud Modifications

For 300 baud use (such as on hf), a 200 Hz shift appears optimum. To incorporate these changes on your TNC, make the following changes:

(1) Change C21 (C11) from 0.0022 uF to 0.01 uF.

(2) Change R46 (R14) from 30.1 k to 227 k.

(3) Set the modulator low tone to 1600 Hz (cal:1/1152).

(4) Set the modulator high tone to 1800 Hz (cal:2/1024).

(5) Set the demodulator center frequency to 1700 Hz (cal:3).

Note that these parts mount on header U34 on the kits; they are part of the circuit board assembly on the Beta boards.

Special Radio / Modem Combinations

While the default FM modem filter configuration works satisfactorily with the radios listed below, special filter values have been calculated and are presented for those interested.

Resistor	Icom 2A	Kenwood 7950
R51	10.0 k	10.0 k
R52	51.8 k	47.3 k
R53	57.1 k	45.4 k
R54	10.0 k	10.0 k
R55	10.0 k	10.0 k
R56	14.3 k	10.0 k
R57	10.0 k	10.6 k
R58	38.6 k	20.7 k

HF/OSCAR Flat Filter

While covered in greater detail in PSR 9 (January, 1984, pg 6), the following data is provided for completeness.

Kits: Change the resistors on header U30 as follows:

R58	23.7 k
R57	10.0 k
R56	11.4 k
R55	10.0 k

(and move the end previously attached to pin 4 to pin 7 of the header)

R52	43.7 k
R54	10.0 k
R53	38.0 k
R51	10.0 k

Credits

The improved sensitivity and 300 baud demodulator mods are a result of extensive investigations conducted by Eric Gustafson, N7CL. The filter resistor mods are due to the efforts of Dan Morrison, KV7B, and Margaret Morrison, KV7D.

In addition, TAPR would like to thank all who have been assisting in the testing of these modem enhancements.

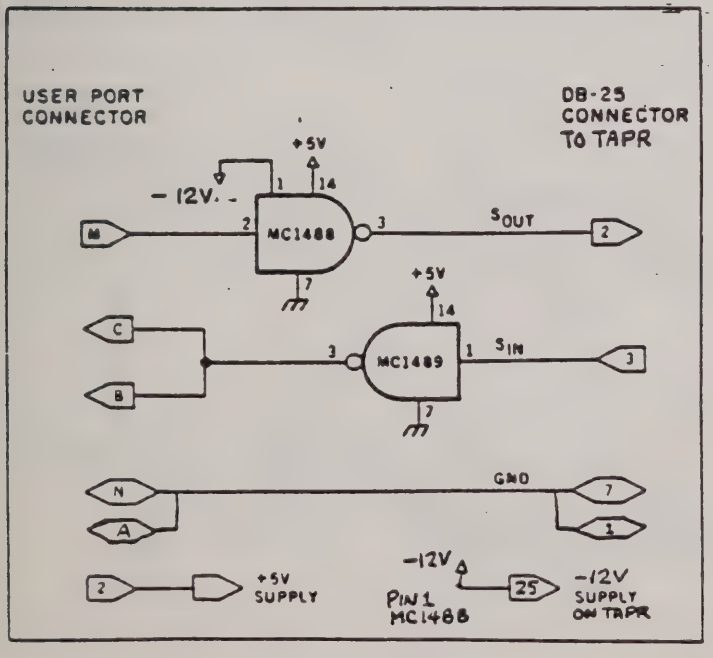
★★★★★★★

COMMODORE 64 AS A PACKET TERMINAL

by Steve Hall, WB6FSK

I recently assembled TAPR board #1427 without any noticable difficulty and all LEDs blinked as predicted by the assembly instructions. What follows may aid the next Commodore 64 owner who wishes to use a C64 as a packet terminal.

The first area to be addressed is the voltage level requirement of the RS-232 line between the TNC and the C64. The TNC needs level shifts of at least 3V DC either side of zero, so any available plus and minus 5 to 12 volt DC supplies will work just fine. I used the article by WB8TKL, "Vic 20 Terminal" that appeared in the March, 1984, PSR as a starting point. After building the described RS-232 adapter using MC1488s and MC1489s purchased from a local Radio Shack, I found that my C64 required one modification to WB8TKL's design. The signal input to the C64 required both pin B and C of the Commodore user port to be connected to pin 3 of the MC1489.



The use of pins B and C is detailed in an article "The Enhanced VIC-20, Part 4: Connecting Serial RS-232C Perpherals to the VIC's TTL Port," Joel Swank, May, 1983, BYTE magazine.

I obtained the required positive voltage from pin 2 on the C64 and have taken a negitive voltage from the TAPR board. I've used pin 25 of the TNC's RS-232 line to connect the -12V to pin 1 of the 1488. This requires a one inch jumper on the TNC from the -12V, found on the wire wrap area, to pin 25 of the serial connector.

Pin 25 is normally unused with RS-232, but be sure to check any other terminal you might use in the future to determine if pin 25 is used for any other purpose.

A second method of getting a negative voltage would be to rectify the 9V AC available at the C64 user port edge connector. This way any RS-232 device could be used with your C64.

As the size of the 1488, 1489 and associated circuity are small, I built the adapter circuit within the edge connector shell plugged into the Commodore. If you are unable to get a connector shell with enough space this small circuit could be put on the TAPR board wire wrap area or within the Commodore.

The second area to be dealt with is the terminal program to allow the C64 to send and receive true ASCII characters. If you don't wish to purchase a program, the one listed in the Commodore 64 Programmers Reference Guide on page 357 can be used. One modification is required though. The statement "100 OPEN 5,2,3 CHR$(6)" sets up the program for parity disabled, none generated/received and 8 bit word length. This needs to be changed to "100 OPEN 5,2,3 CHR$(38)+CHR$(224)" which gives the required space parity and 7 bit word length. The Programmers Reference Guide gives a semi-cryptic explanation of the opening and use of an RS-232 channel on pages 348-351. The format for the open statement allows selection of a number of options needed for TNC operation.

The open statement needed contains CHR$(38) for the control register which is 00100110 in binary. This sets up a 7 bit word length and 300 baud data rate. The CHR$(224) puts binary 11100000 into the command register which selects space parity as needed by the TNC using default selections.

With the terminal programs I have tried the Delete OFF option should be selected for proper backspace operation.

TNC Modem Tuning Indicator

by Lyle Johnson, WA7GXD

Many users of the TAPR TNC have expressed the desire for a simple tuning indicator that would enable them to easily tune in a packet transmission in non-FM modes. If you are among this group, read on!

Background

For normal VHF-FM packet activity, a tuning indicator is not needed. One simply tunes the receiving radio to the local packet channel, and the FM audio recovery process assures that the demodulated FSK tones are at the right frequency (pitch).

On HF and OSCAR, however, the modulation method used is typically FSK (generated by injecting audio tones into the microphone input of an SSB transmitter). Thus, accurate recovered audio pitch is not assured, and careful tuning is required. To make matters worse, the tuning accuracy required is on the order of 20 Hz, and becomes more and more critical as the data rate (baud) increases for a given shift.

The XR2211 FSK demodulator IC used in the TAPR TNC was originally designed for use on baseband (audio) links, such as a telephone line. Thus, tuning indicator outputs were not considered needed as part of the IC design.

Development

Examination of the 2211 phase-locked-loop demodulator used on the TNC reveals that the information needed for a tuning indicator is in fact present. Please refer to your TNC schematic diagram, sheet 3. In the following discussion, the U1t callout will be given first, followed by the Beta board callout in parentheses.

U18 (U18) is the XR2211 PLL demodulator. Note that there is a block marked "VREF" with an output at pin 10. This reference voltage is used in the 2211 to determine when an incoming signal is above or below the center (free-running) frequency of the PLL. C24 (C12) is simply a filter on this reference voltage to suppress high frequency noise.

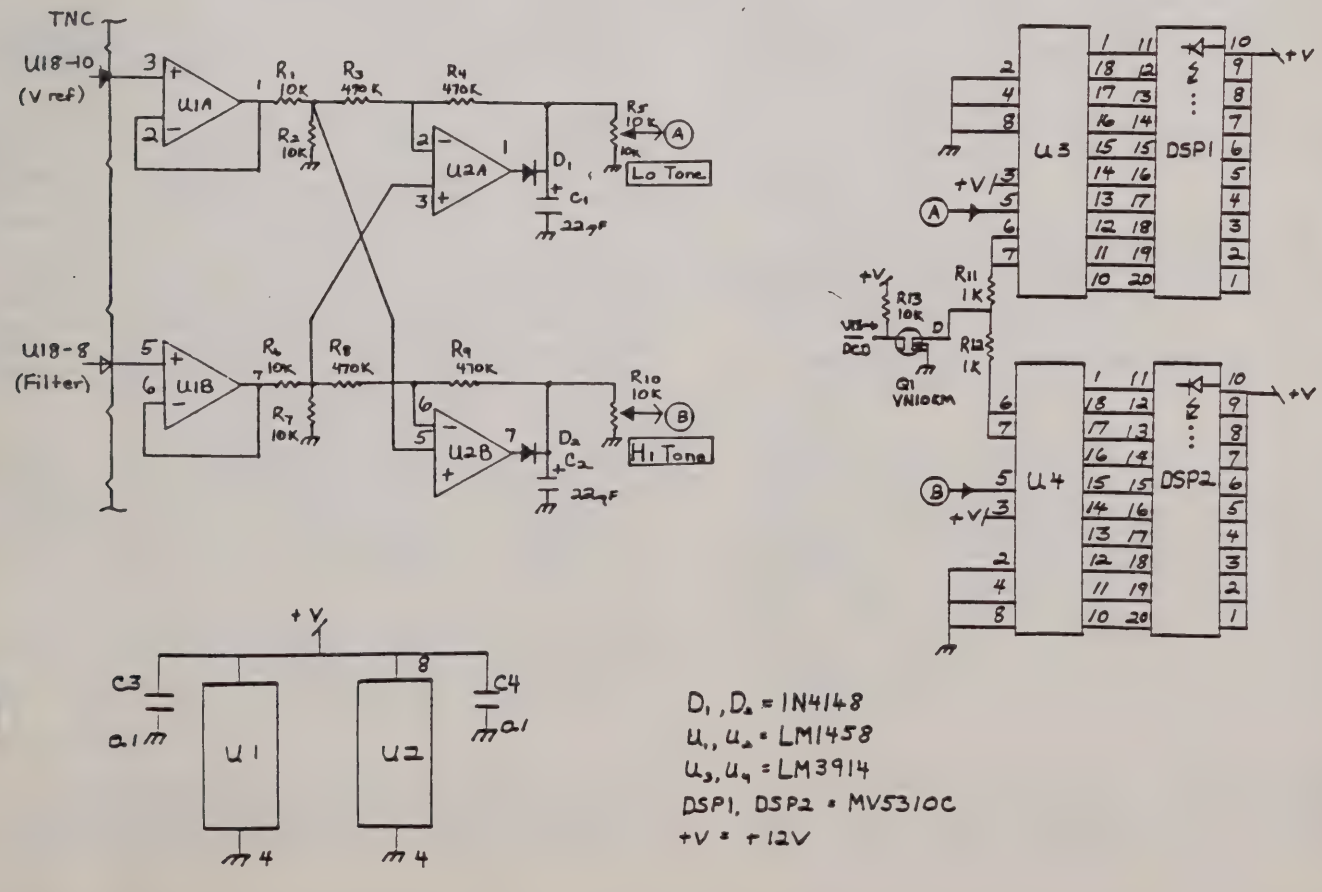
The block marked "FSK" in U18 (U18) is simply a comparator that determines whether the voltage at the output of the loop detector filter C20/R45/C21 (C25/R13/C11) is above or below the pin 10 reference voltage. The filter voltage is developed in the loop detector and is proportional to the frequency of the incoming signal. It is used to steer the 2211's VCO to the incoming signal frequency to maintain lock.

The voltage at the output of the filter (pin 8 of the 2211) is below the reference voltage when the applied signal frequency is above the vco center frequency, and above the reference voltage when the applied signal is lower in frequency.

What is needed, then, is a voltmeter that can read the instantaneous voltage at pin 8 and have its scale calibrated in frequency.

A practical circuit is presented below.

(continued on page 11)



Implementation

The signal voltages from the XR2211 are buffered by Op Amps U1A/U1B. These are configured as simple voltage followers to present a high impedance to the '2211; the loop data filter (pin 8) is especially sensitive to loading. The voltage follower outputs are then attenuated to 1/2 the actual level by R1 and R2 for U1A and by R6 and R7 for U1B.

Each buffered channel is then applied to a peak detector circuit consisting of an Op Amp section (U2A/R3/R4 and U2B/R8/R9) whose positive peak output is sampled by a detector (D1/C1 and D2/C2). Each channel's reference point (the non-inverting input) is tied to the other channel's signal.

Perhaps an example will help clarify the circuit operation: Consider the case where the applied frequency to the 2211 is below the vco center frequency. The voltage at the output of the loop data filter (pin 8) will be some value more positive than the reference voltage generated at pin 10. This means that the output of U1A will be lower than the output of U1B. U2A will seek to force its input pins to the same value (basic Op Amp operation). Since the non-inverting (+) input is more positive than the inverting (-) input, the output voltage will rise. When the output voltage is exactly twice the input voltage difference, the inputs will be matched and the output will stabilize.

U2B, on the other hand, will have its + input lower than its - input, so its output will be driven towards ground. This will back bias D2, effectively forcing U2B to the ground rail.

In the case of a signal frequency above the vco center frequency, the opposite will occur and U2A will go to ground on its output, with U2B providing a voltage at its output proportional to the difference in voltages received from the 2211.

The voltage differential between the 2211 pins 8 and 10 is proportional to the frequency difference between the vco center frequency and the applied signal.

The time constant C1/R5, or C2/R10, determines the peak "hold" time of the circuit. The values given provide good operation of the circuit, but you may wish to experiment. The idea is to have fast response time with a hold time longer than the slowest applied data rate.

The outputs of R5 and R10 are applied to identical LED bar-graph displays. The National LM3914 is used, as it has a linear response (don't use the LM3915 with its logarithmic weighting!). The ten outputs are passed to a 10-LED bar-graph display, with the LM3914 set to provide a "moving dot" as opposed to a "stretching bar".

The brightness of the display is set by R11 and R12. Q1 is tied to the otherwise unused data carrier detect output of the 2211 (pin 6), assisted by pullup R13. When no signal is being received, Q1 is off, forcing the LED displays to minimum intensity. When a signal is detected, Q1 is turned on, causing the display to operate at normal brightness.

Each display will show a moving dot, proportional to the applied frequencies. With the hold time introduced by C1 and C2, the dot will tend to hang at the location corresponding to the high or low tone frequency of the input FSK signal.

Calibration

With the demodulator configured for operation at the data rate desired (see the 300 baud and 1200 baud modem configuration articles elsewhere in this PSR), set the TNC to the CALIBRATE mode.

Verify the calibration of the 2211 center frequency to be exactly 1700 Hz (or whatever frequency you have configured it for).

Now, select the low tone (1600 Hz for 300 baud operation). Place a jumper at J3 pins 3 and 5 (Beta boards simply ensure that J8 is in place) to cause the output of the 2206 FSK modulator to loop back to the input of the 2211 demodulator.

Adjust the frequency of the 2206 to the exact value desired. Next, adjust R5 on the tuning indicator so that the two middle LEDs of DSP1 are glowing and flickering with equal intensity. It is important that both LEDs be glowing.

Select the high tone adjust the 2206 for the value desired. Then, adjust R10 on the tuning indicator so the two middle LEDs of DSP2 are glowing with equal intensity.

Remove the loopback jumper and set the TNC up for normal operation.

Operation

When operating on FM, the center LEDs on DSP1 and DSP2 should glow when receiving packets. Observe the displays carefully in this mode. You may notice that one or the other of the center LEDs glows brighter than the other, and this tendency will vary with the data that is being received. This is due to the loop filter characteristics at pin 8 of the 2211. The loop filter is properly configured to be -3db at the design data rate, so when a number of "0's" are sent in HDLC format, the peak output voltage at pin 8 of the 2211 is slightly less than at lower data rates. It is this lower voltage that causes one of the center LEDs to tend to be brighter than the other.

After gaining familiarity with the patterns to be expected with a properly tuned signal, change modes to SSB and try to tune in a 300 baud HF signal (10.147 MHz, LSB is a packet standard HF frequency) or a 1200 baud OSCAR signal (145.835 MHz downlink). The correct tuning will be greatly simplified with the use of this circuit.

With a short "training" period, it is easy to tune in a stable signal to an accuracy of 10 Hz.

Construction Hints

At present, no kit is available for this tuning indicator. However, TAPR is planning on having a PC board and parts kit available. As in all TAPR projects, do not send money or orders until the kit is officially announced in PSR.

Not counting the PC board, the cost to build the indicator should not exceed about 12 to 20 dollars.

No special precautions need to be taken regarding parts layout. You may find it most convenient to place the LED displays horizontally, with one display immediately above the other. An alternative display method, which will save a bit of money and space, is to parallel the LM3914 outputs and drive a single 10-element LED bar-graph display. In this case, the display may be easier to interpret if the the outputs are criss-crossed (pin 1 of U3 tied to pin 10 of U4, pin 18 of U3 tied to pin 11 of U4, etc.). However, the calibration of the indicator may prove more challenging in this case!

Credits

The design of the tuning indicator presented here was done by Eric Gustafson, N7CL, who is also the person most responsible for the modem sensitivity and 300 baud modifications.

★★★★★★★★

This list contains the names of organizations on national, local, and regional levels that are active in the furtherance of amateur packet radio. Also shown are individuals that are actively interested in organizing groups in their respective areas.

N A T I O N A L
AMRAD
P.O. Drawer 6148
McLean, VA 22106-6148

AMSAT
850 Sligo #601
Silver Spring MD 20910
301-589-6062

TAPR
Tucson Amateur Packet Radio
P.O. Box 22888
Tucson, AZ 85734-22888

A L A B A M A
Frank Emens, W4HFU
3714 Lakewood Circle
Huntsville, AL 35811

Henry Wingate, K4HAL
104 Von Dale Drive
Birmingham, AL 35215

A R K A N A S A S
Elmer Wingfield, W5FD
26 Belmont Drive
Little Rock, AR 72204

C A L I F O R N I A
Los Angeles Area Packet Group (LAPG)
P.O. Box 6026
Mission Hills, CA 91345

So. Calif. Digital Coordination Council
SCDCC
P.O. Box 6026
Mission Hills, CA 91345

Pacific Packet Radio Society
P.O. Box 51562
Palo Alto, CA 94303

San Diego Packet Group (SDPG)
c/o Mike Brock WB6HHV
10230 Mayor Circle
San Diego, CA 92126

C O L O R A D O
Rocky Mountain Packet Radio Association
RMPRA
c/o Andy Freeborn, N0CCZ, Secretary
5222 Borrego Drive
Colorado Springs, CO
(303) 598-8373

F L O R I D A
Florida Amateur Digital Communications Assoc.
FADCA
812 Childers Loop, Brandon, FL 33511
(813) 689-3355

G E O R G I A
Georgia Radio Amateur Packet Enthusiast Society
GRAPES
P.O. Box 1354
Conyers, GA 30207

Southern Amateur Packet Society (SAPS)
c/o Wayne Harrell, WD4LYV
RT 1 Box 185
Sycamore, GA 31790

I L L I N O I S
Chicago Amateur Packet Radio Assn. (CAPRA)
P.O. Box 8251
Rolling Meadows, IL 60008

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Packet of New York (PONY)
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Source: GATEWAY #16

PACGrams

By Jay Nugent, WB8TKL

Now that Packet Radio has a foothold in the amateur community with some 1500 plus TAPR TNC's scattered around the country it is time for something more than RTTY-like ragchewing, an occasional program transfer, or leaving messages for one another on the local Packet Bulletin Board System.

What I am proposing is an application for Packet that makes effective use of its capabilities. It also offers an improvement over any existing systems, not merely a duplication. The application that comes to mind, where rapid error free transmission is the goal, is traffic handling. A very effective protocol is already in widespread use for this task, the Radiogram.

A Radiogram is an addressed and numbered message format with handling instructions and simple error checksum. In order to merge this existing structure with a new transmission method ,yet continue to use the message content in the existing worldwide networks, we must have an accepted standard for coding, recognizing and returning the radiogram back to its original form. So we don't limit the number of stations able to use this method of sending traffic a human readable form would be desired. To this end I propose PACGRAM.

PacGram is an application software package used on the host computer that is connected to a TNC. The software is responsible for prompting the operator for the proper Radiogram information one field at a time and forming a PacGram message. It is then sent to the TNC for transmission into the network.

At the receiver, PacGram watches the data stream for its starting character. When this character is detected the software knows to convert the message back into the standard Radiogram format for display. Other special characters are used to seperate the fields within the message and to signal the end of a PacGram.

PacGram Definition

Selection of special characters used in PacGram was based on the fact that they should not appear in the heading or body of a normal message. For those stations not possesing PacGram software, such special characters should allow a PacGram message to be read and easily returned to standard Radiogram form by hand.

The START of a PacGram shall be a pound sign.

"#"

The purpose of this character is to differentiate a PacGram from any other data sent from the TNC to the host computer. This start character also helps facilitate locating a PacGram in RAM for later retrieval.

The DELINIATION character shall be an asterisk.

"*"

This character is present in the PacGram to deliniate the fields of a standard Radiogram from one another. The absense of data in a field will cause two consecutive asterisks to appear within the PacGram. The asterisks may later be converted into tabs by the PacGram software to facilitate printing to a formated screen or printer.

The END of a PacGram shall be the ampre sign.

"&"

The purpose of this character is to signal the end of the PacGram. This is used to identifify the end of the PacGram in RAM for later retrieval.

The FIELD ORDER of a PacGram is as follows.

Number /Precedence /Handling instructions /Station of origin /Check /City & state of origin /Time filed /Date filed

Name to /Number & street /City & state /Zip /Phone number

Text of the message

Signature /Title of signee

FIELD LENGTHS and TYPES within the PacGram.

The fields may be of any length. Types may be either alphabetic or numberic. This is in line with a standard Radiogram which is handwritten or typed allowing very flexible use and applications.

Just as a Radiogram has well defined fields in a specific order, so does a PacGram. By design the PacGram will have an identical layout to a Radiogram. Here is a sample Radiogram with an equivelant data stream in PacGram form.

Number: 126 Routine WB8TKL Check: 5 Ann Arbor MI 1430Z May 21
TO: Mike Nugent
123 Hollywood Ave
Hollywood, Cal 54321
(818) 555-1234
TEXT: How is the weather X
Signed: Jay

The equivelent PacGram would be:

#126*R**WB8TKL*Ann Arbor Mi*1430Z*0521*Mike Nugent*123 Hollywood Ave*Hollywood Cal*54321 *(818)555-1234*How is the weather X*Jay**&

The length is well within the maximum AX.25 PACLEN. A single full length packet could contain a very large text field, one that many traffic handlers would groan at. Yet it could be sent in a fraction over a second, an enormous improvement over existing amateur traffic systems.

Also notice that since the fields for Handling Instructions and Title were left blank PacGram simply put no data between the two asterisks. This is neccessary to maintain the field count for decoding the PacGram at the receiving end.

This formating of Radiograms into PacGrams was tested using very crude software during the Simulated Emergency Test in Michigan a year ago and proved to work efficiently. Since that time I have been seeking improvements and suggestions and would welcome any that you may have. The work I have done so far has been in assembler for an 8080 under CP/M.

I am sure there are many of you better qualified and more knowledgable in writing software, so I will make the software available to those that want to take a stab at it. Please return the favor by sending me a copy of what you have done to improve my code.

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